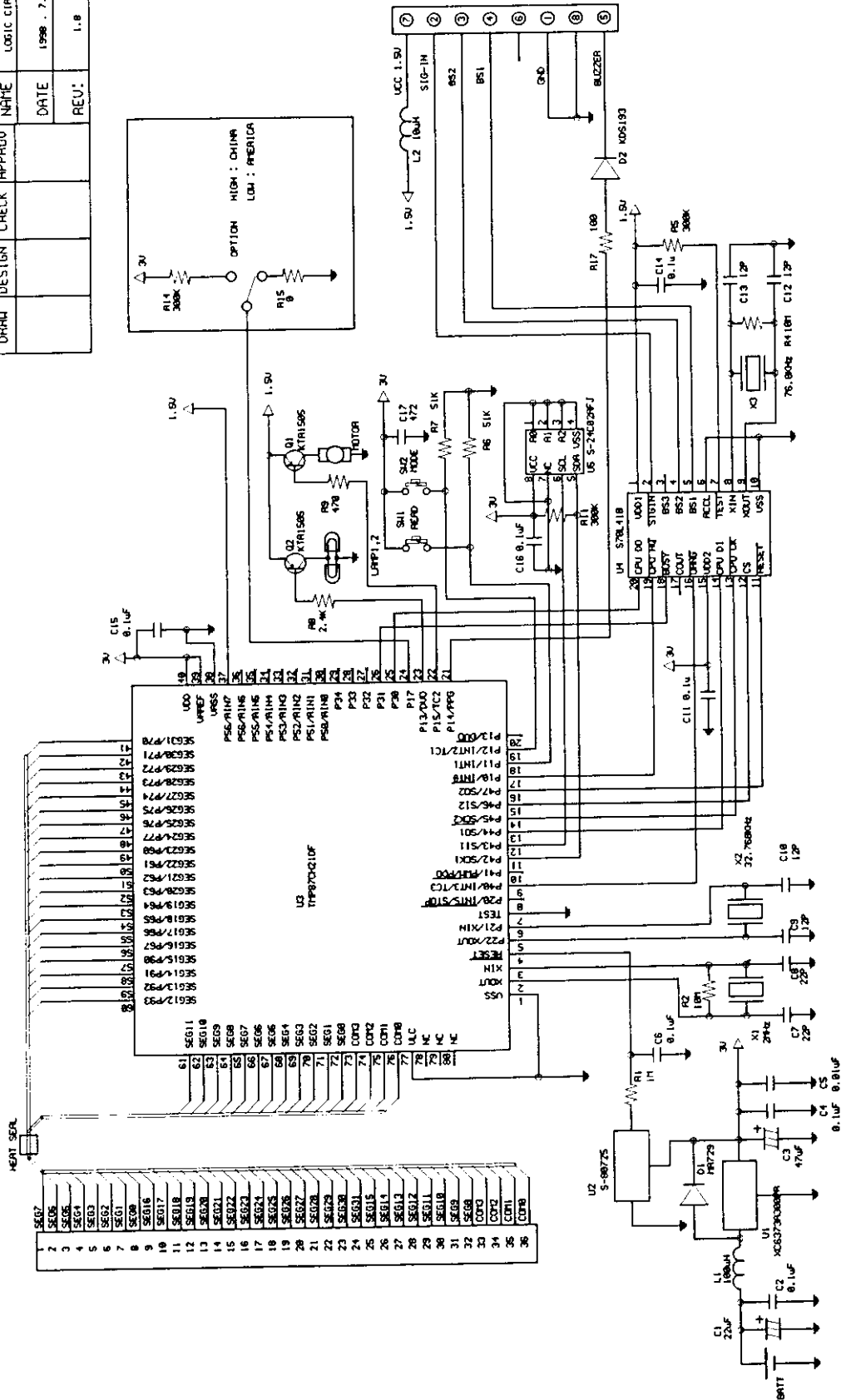
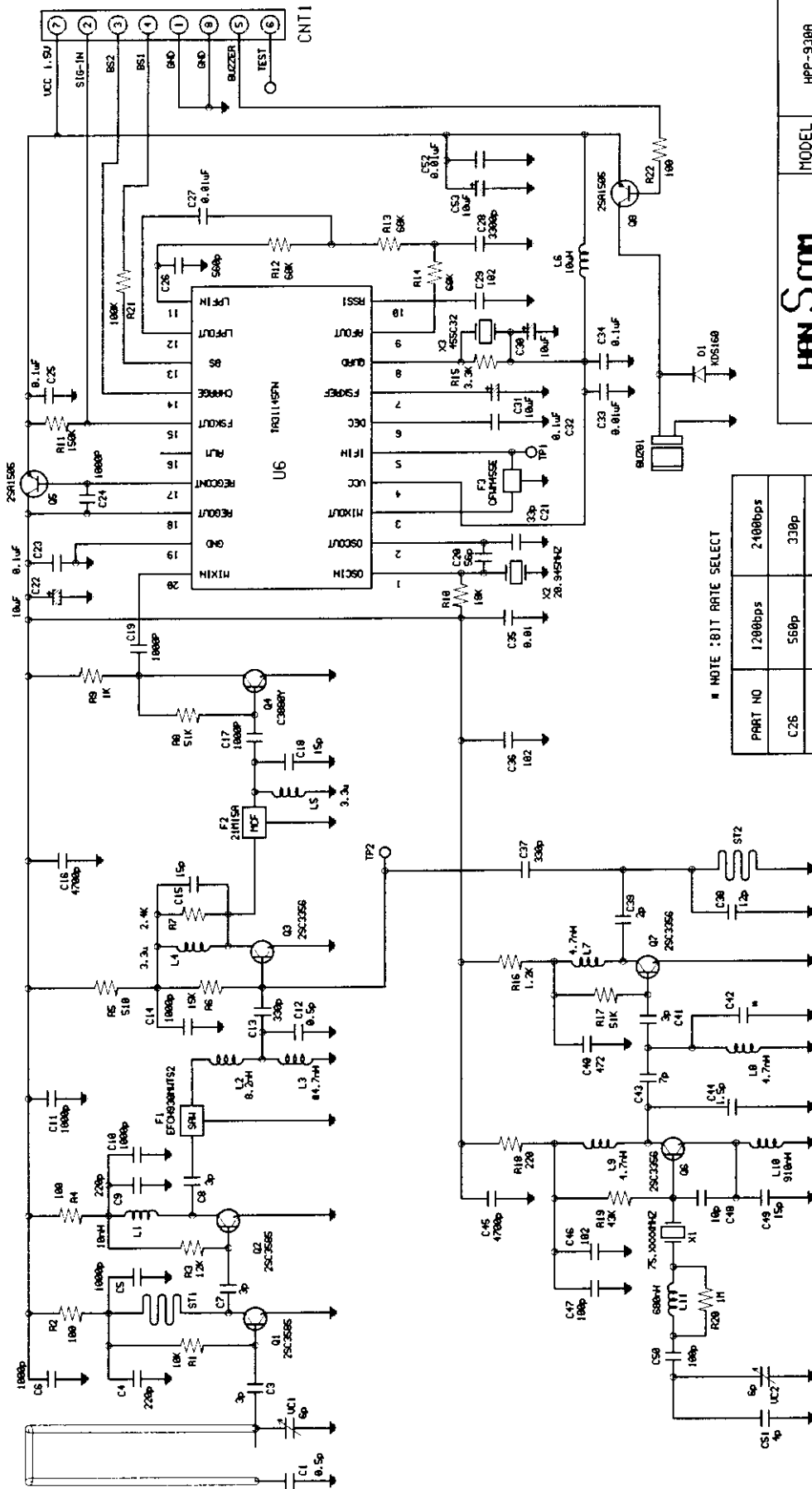


HON S COM				MODEL	POCSAG #F
DRAW	DESIGN	CHECK	APPROV	NAME	LOGIC CIRCUIT
				DATE	1998. 7. 18
				REV:	1.8





HAN S COM

MODEL	HPP-938A
NAME	RF CIRCUIT
DATE	1998.7.18
REV:	2.1

NOTE: BIT RATE SELECT

PART NO	1200bps	2400bps
C26	560p	330p
C27	0.01u	4700p
C28	3300p	2200p
C31	10u	1u

POCSAG PAGER
930MHZ

NOTE: NO C2