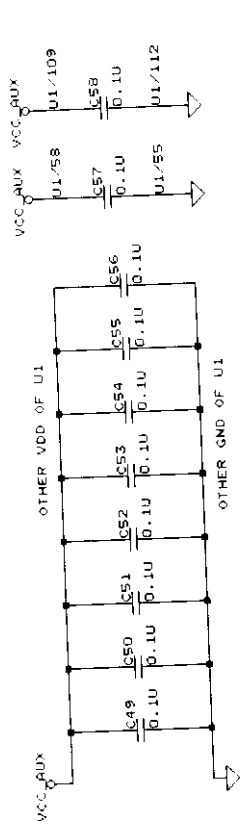
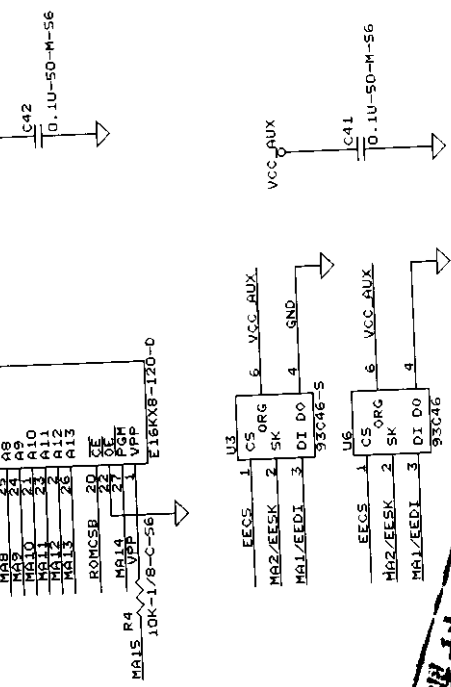
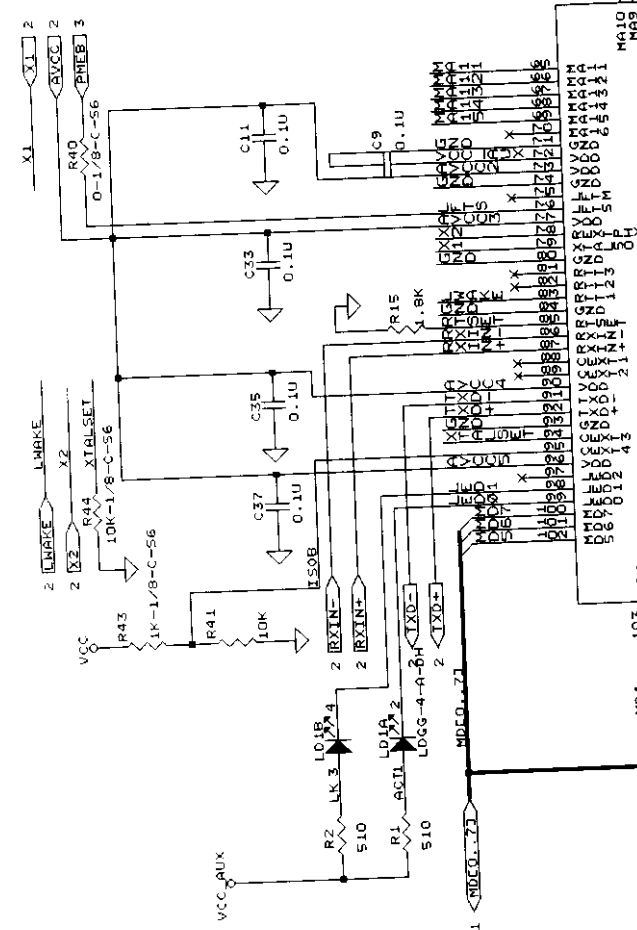


LINK
STR. SCH
SLOT. SCH

NOTE: Apply R44 to use 25MHz crystal as the clock source.
otherwise, discard R44 to use 50MHz OSC as the clock source.



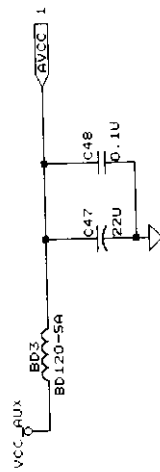
DE-COUPLE CAPS OF RTL8139B
MUST BE PLACED AS CLOSE TO CHIP
AS POSSIBLE. (INCLUDING C11, C33, C35, C37)



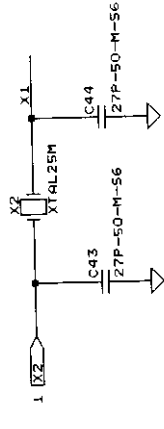
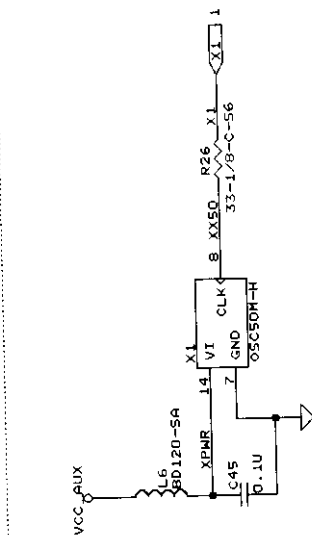
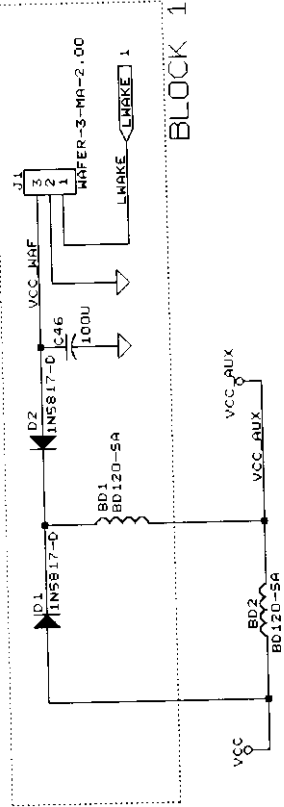
振曜科技股份有限公司
機密文件

NOTE: 1. Without ACPI must be removed the BLOCK 1
, and connect BD2

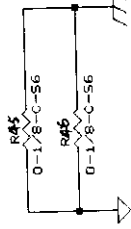
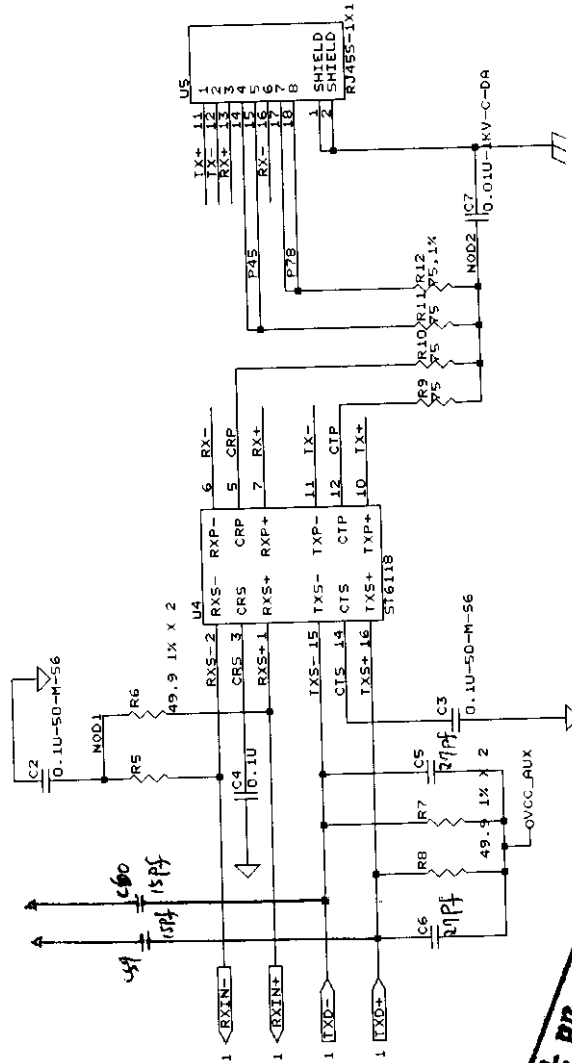
Analog power



ACPI interface



OSC OR XTAL SELECT



Enlarge contact plane



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