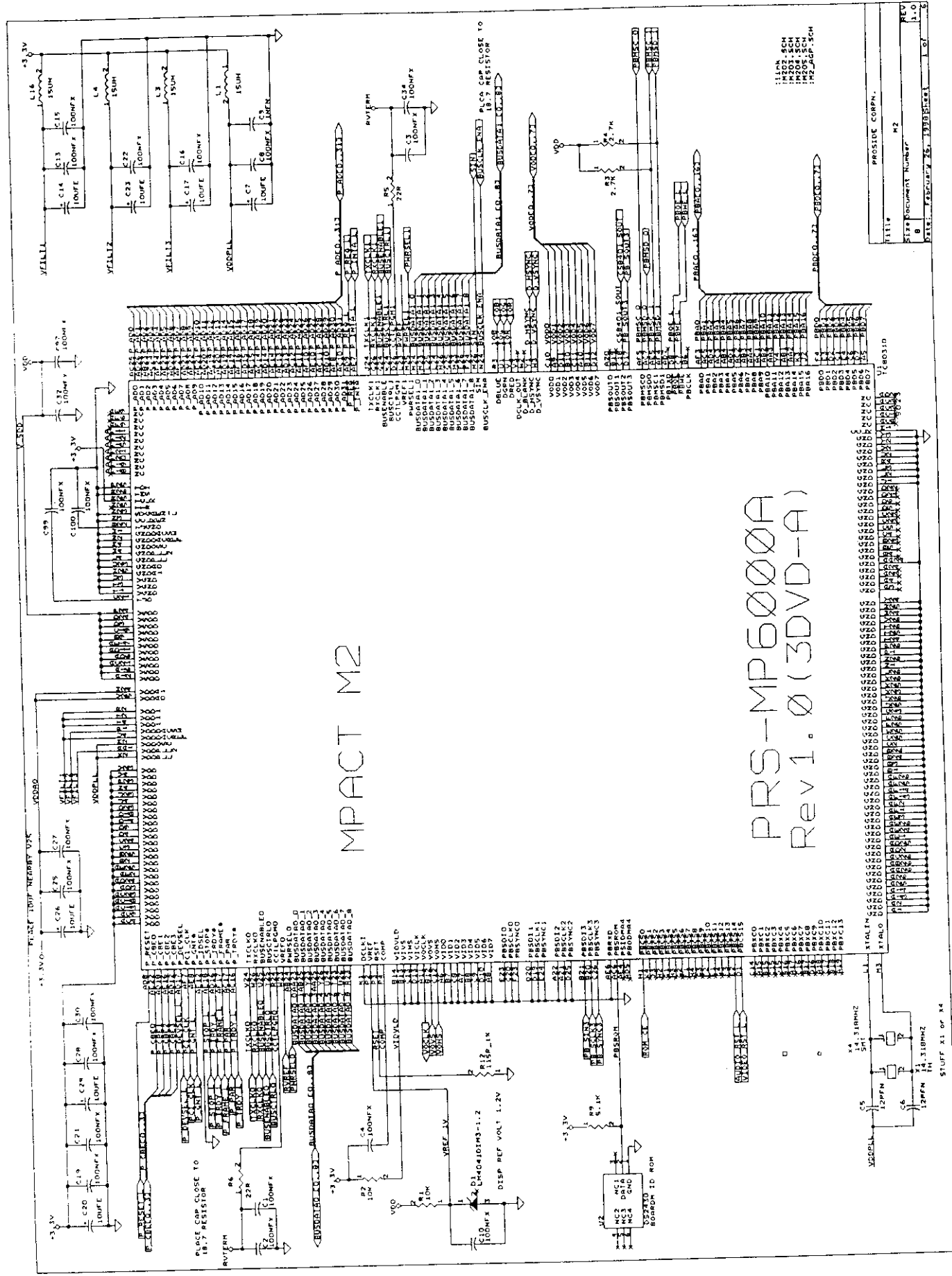
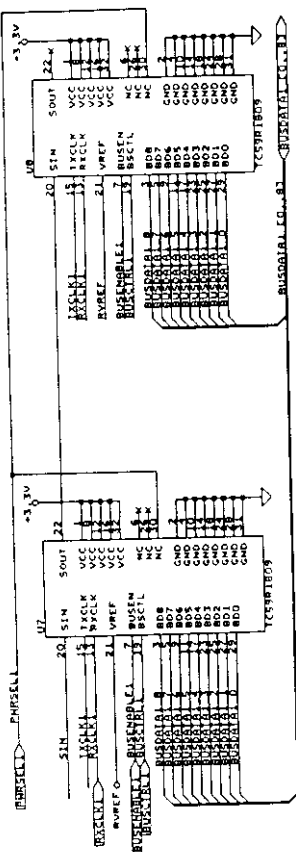
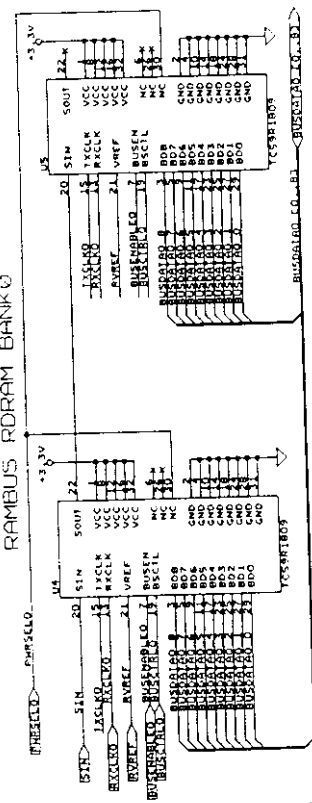




MPACT M2

RAMBUS RDRAM BANK 0

[illegible]

3.3V PLACE CLOSE TO PIN 21 OF U'S

3.3V PLACE CLOSE TO PIN 21 OF U'S

3.3V PLACE CLOSE TO PIN 21 OF U'S

SEE 0.1μFY CORE BYPASS. C PLACEMENTS ARE ALONG THE WIDTH OF THE C

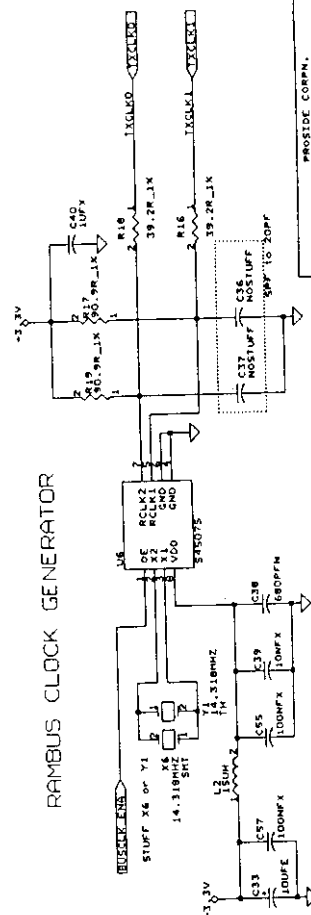
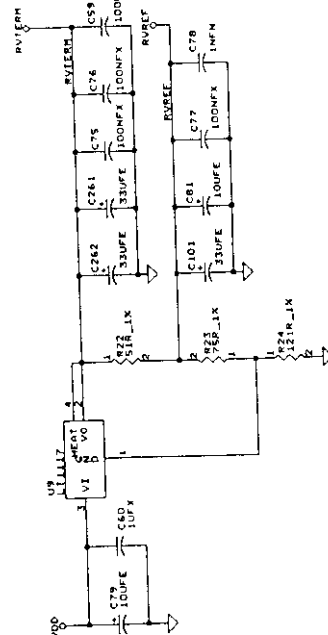
PLACE ONE SIDE OF TERM IN TERM ISLAND DECOUPLING TECHNIQUE AND

TERMINATION CHANNEL

RAMBUS CHANNEL TERMINATION

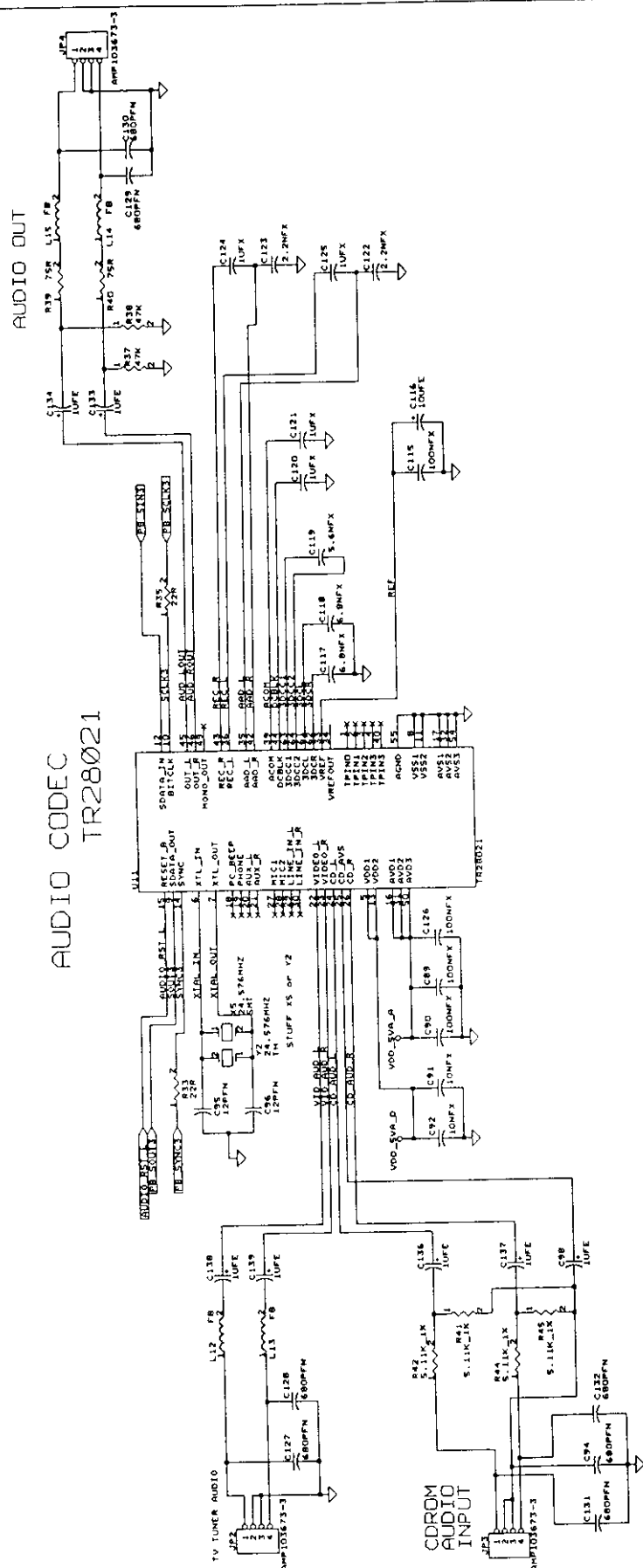
RAMBUS VTERM/VREF GENERATION

RAMB15 CLOCK GENERATOR

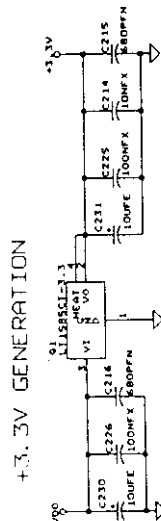
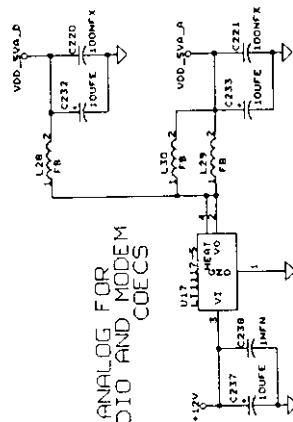


RAMBUS CLOCK MUST BE MATCHED
FOR TIME AND LENGTH, FRONT SIDE
TRACE ONLY. CONTROLLED IMPEDANCE

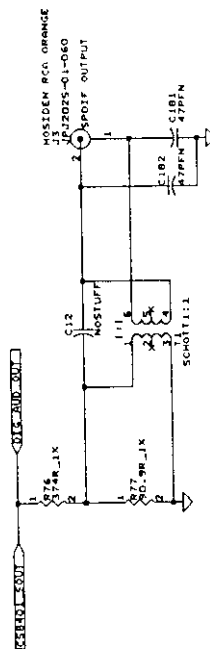
PROSIDE CORPN.	
Title	NZ IMPACT
Size	Document Number
8	
REV	1.0
2 of 6	



PROSIDE CORP.	
Title	N2 IMPACT
Size	Document Number
8	
Date:	March 3, 1988 Sheet 3 of 6
REV	1.0

[illegible]SV ANALOG FOR
AUDIO AND MODEM
COECS

ALYANATES: R76 = 205R-1X
R77 = 118R-1X
C3 = 100MF

SV GENERATION
FOR VIDEO CODECS