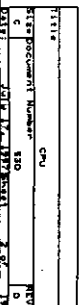


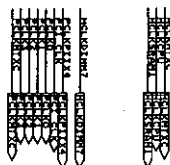
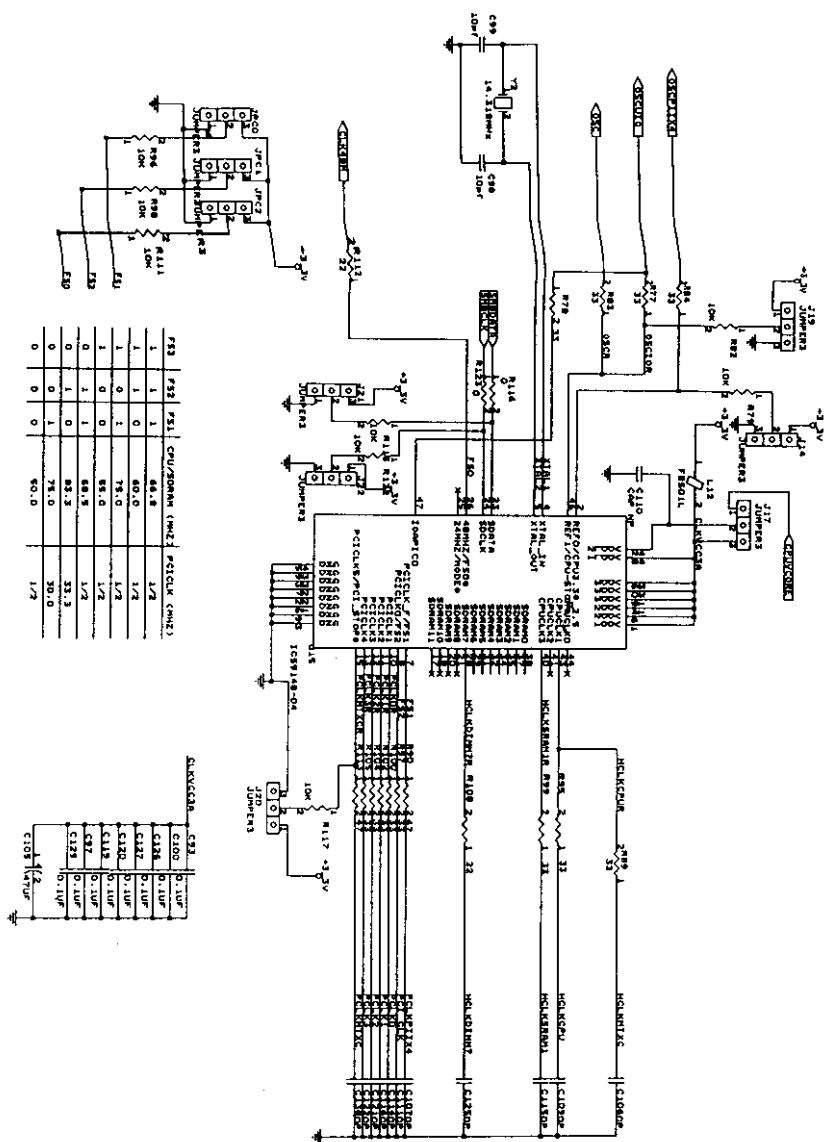
120

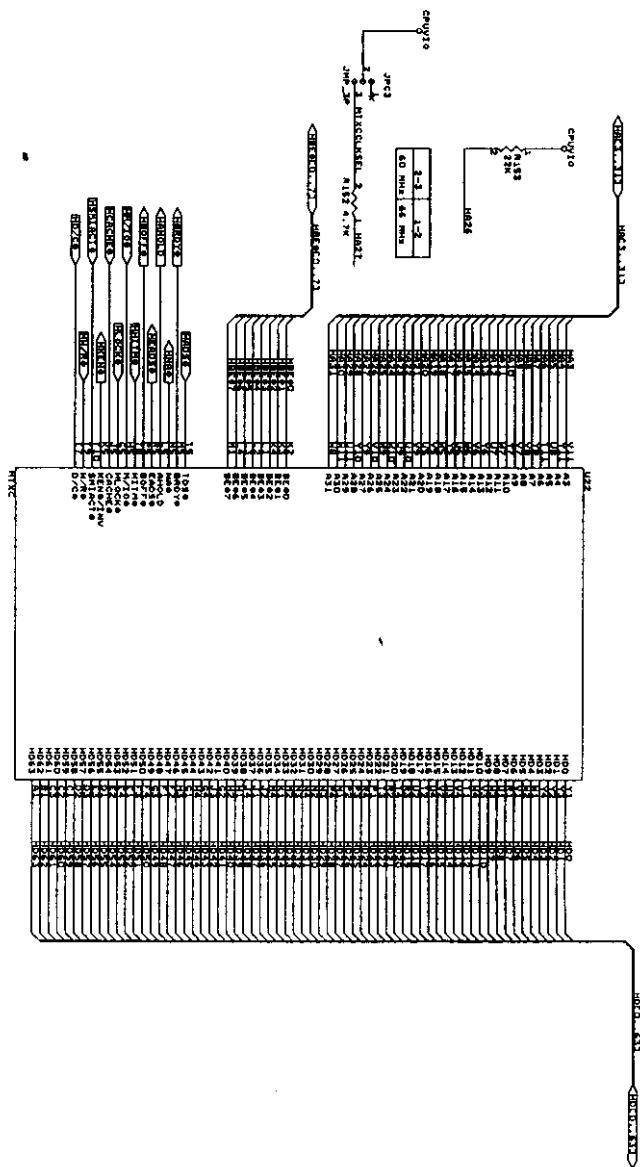
Platform : ATX
P54C,P55C,6X86,K5
SIMM : SOCKET 2 X 72 Pins
SIMM : 5V - EDO, FPM DRAM
CACHE : ON BOARD 256K/512K PBSRAM/M-CACHE
BIOS : 5V,12V FLASH ROM,2 Mbits FLASH ROM
USB : 2
ID : SMC 37C672
ULTRA DMA 33 IDE PORT X 2

ILINK
IP54C.SCH
ISYSCLK.SCH
IMTXC_A.SCH
IMTXC_B.SCH
ICACHE.SCH
ISIMM.SCH
IP1X4_A.SCH
IP1X4_B.SCH
IDEBUSB.SCH
ISUPER_IO.SCH
ICDM_PORT.SCH
IBIOS.SCH
IPOWER.SCH
IVOLT_REG.SCH
IDECUP.SCH
IPULLUP.SCH
IRTC_KBC.SCH
IT2_ISA16.SCH

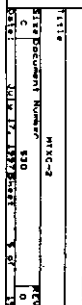
DATE	TIME	NO.	REV
0	0	0	0
NO. 11. 11711111 1.0.1			



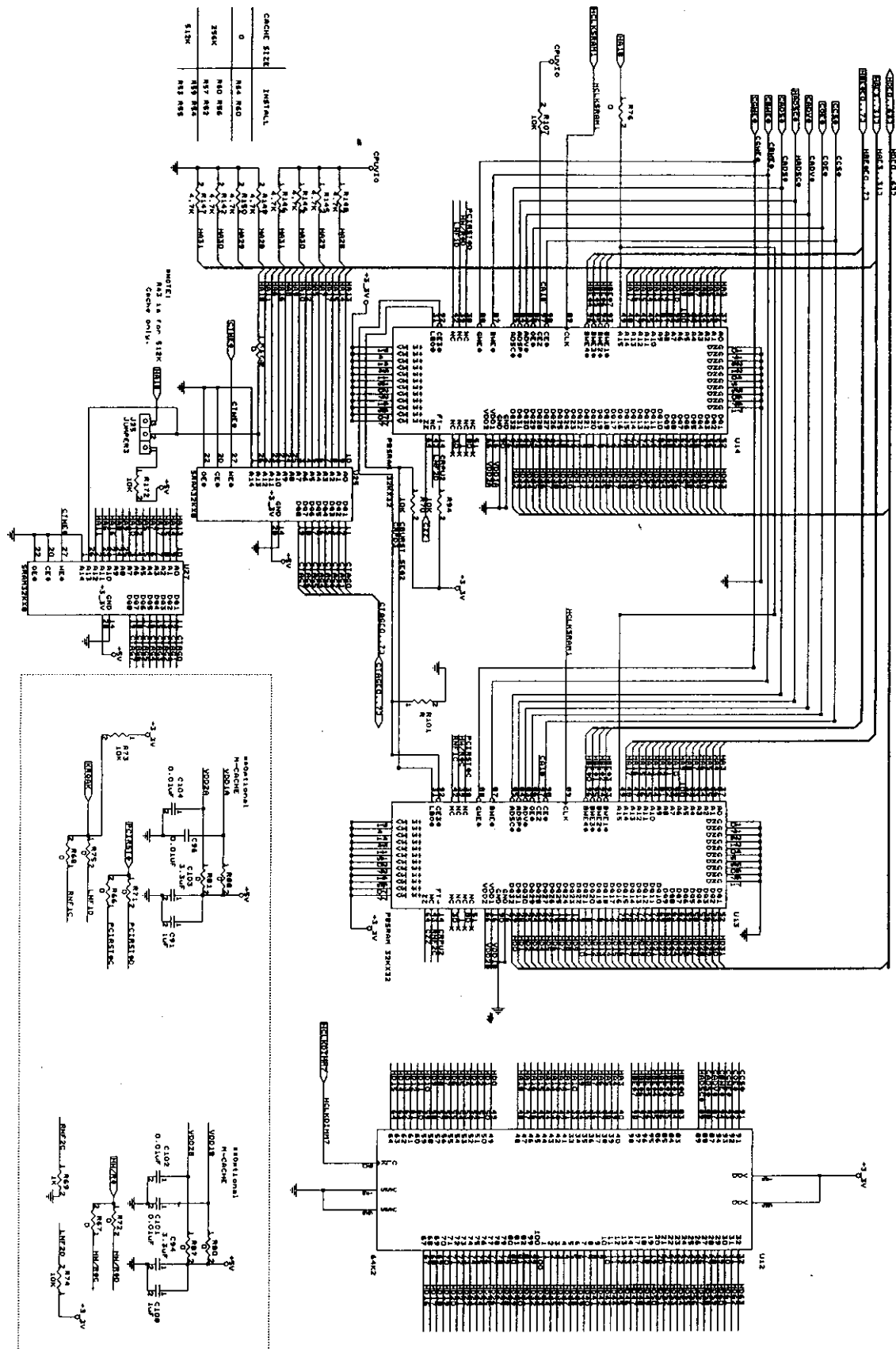




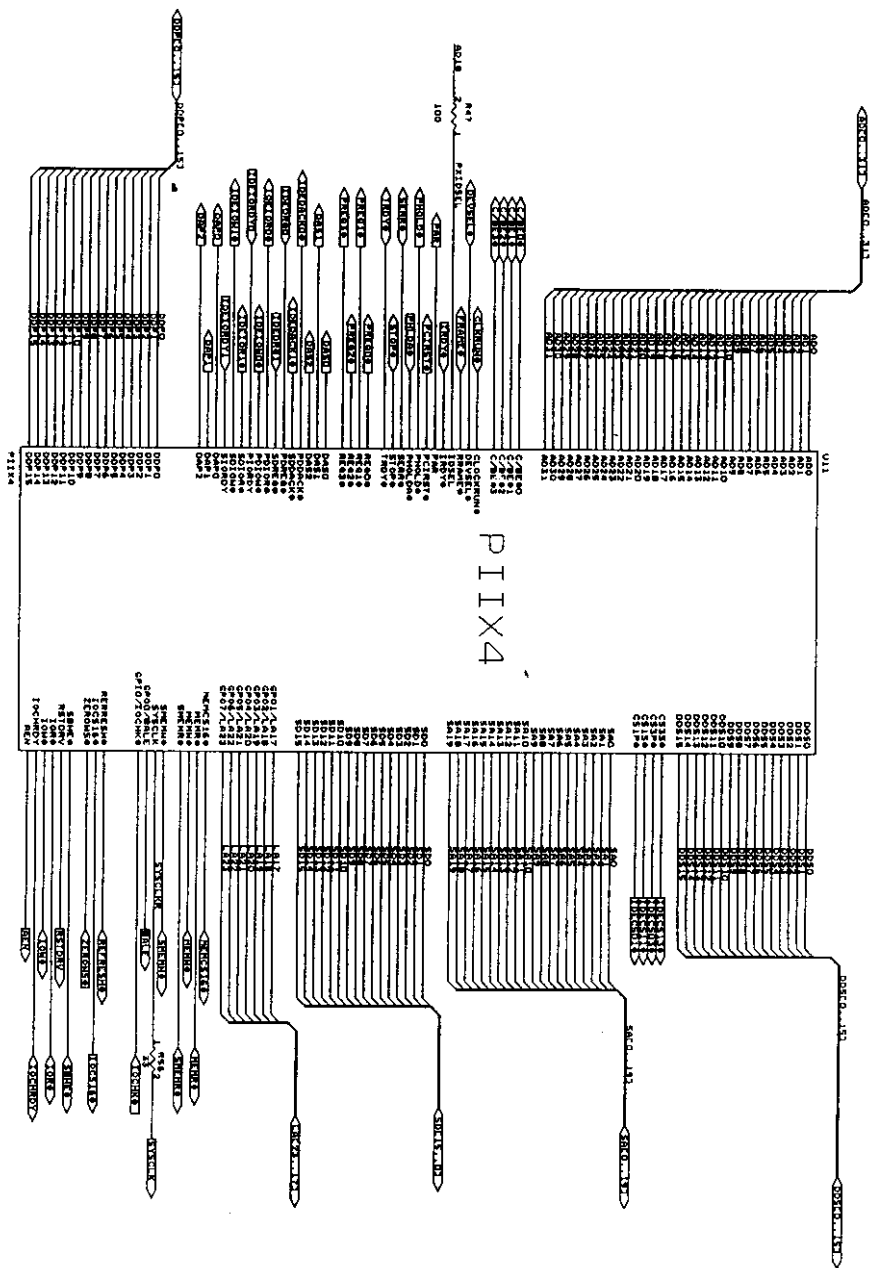
Title	
MTC-1	
Kit Assembly Number	880
DATE	2018-11-17/2018-11-17
REV	0

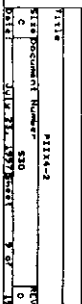


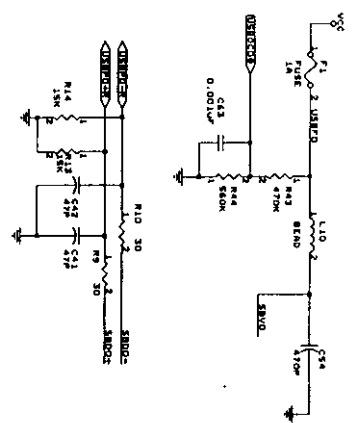
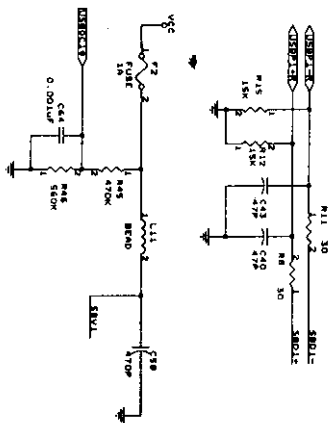
NOTE: PIN 16, 29, 42, 43 & 66 ARE DISCONNECTS FOR PG 8080



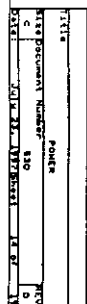
7118	CACHE
5118	PERMANENT ROM/WR
510	0
5118	DATE 24-11-1977
5118	BY 10

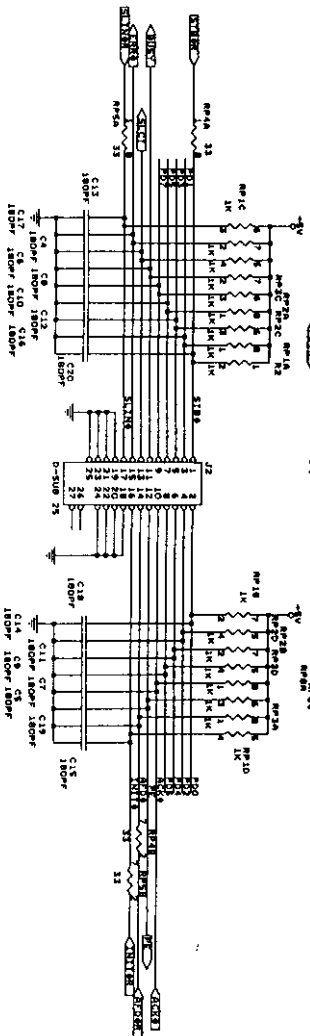
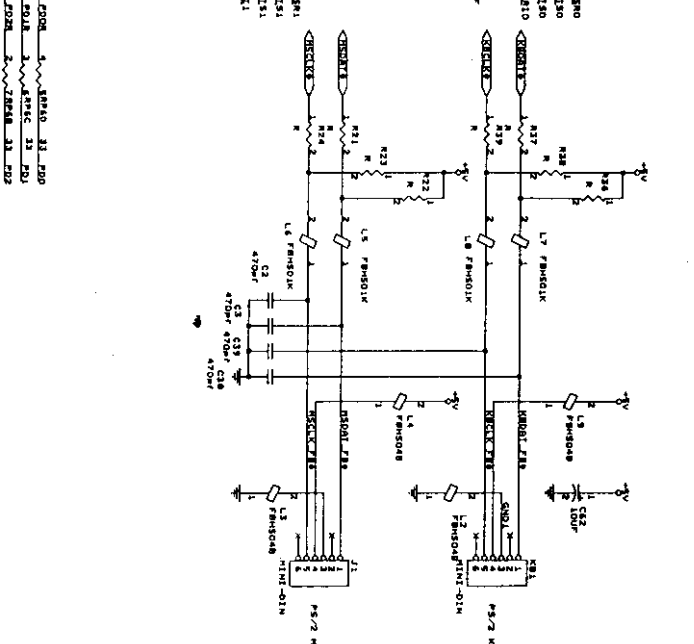
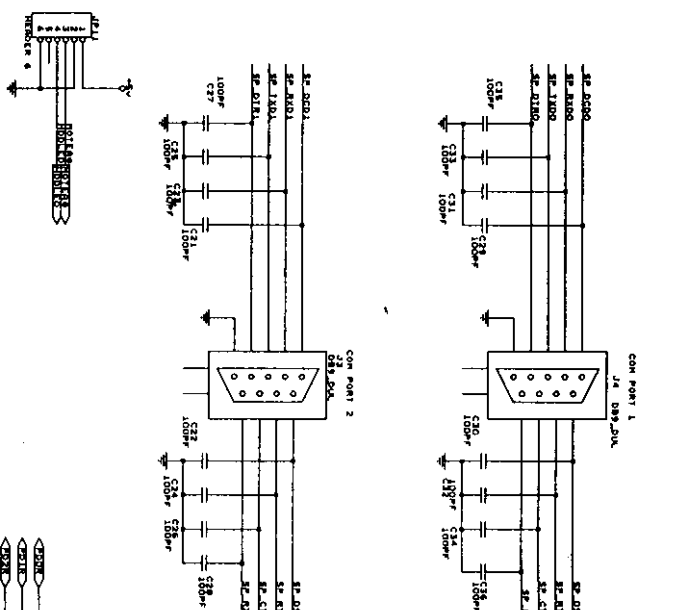
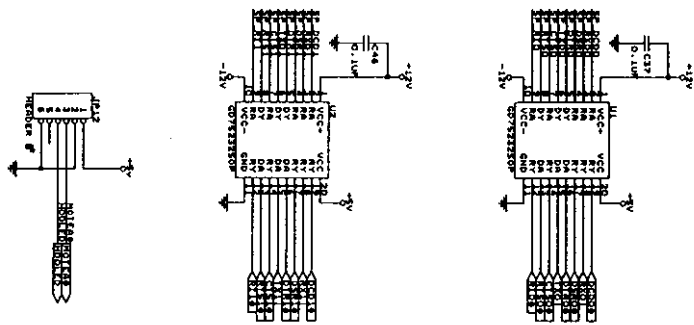




[illegible][illegible]

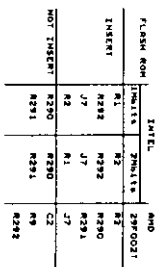
Title		10E & USB	
Size Document Number		450	
C			NY
Date:	JUN 27, 1987	Sheet	10 of 17

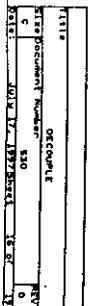


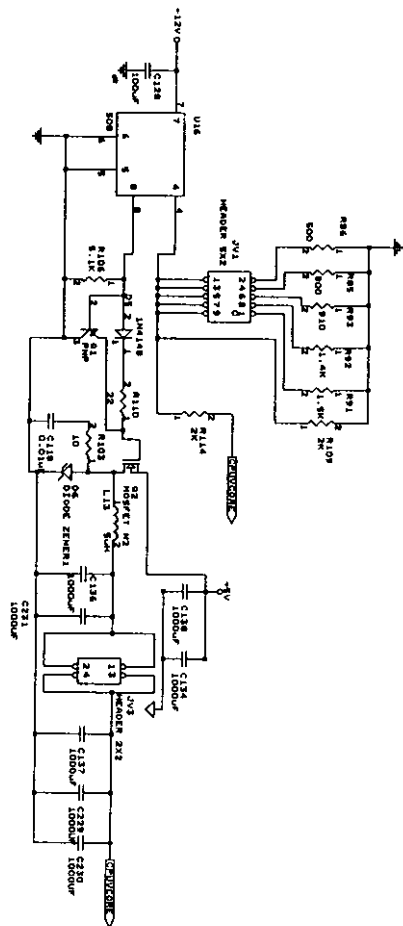
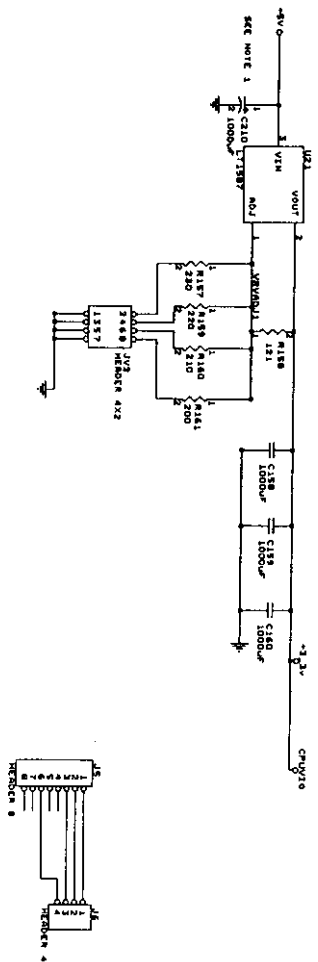


PARALLEL PORT

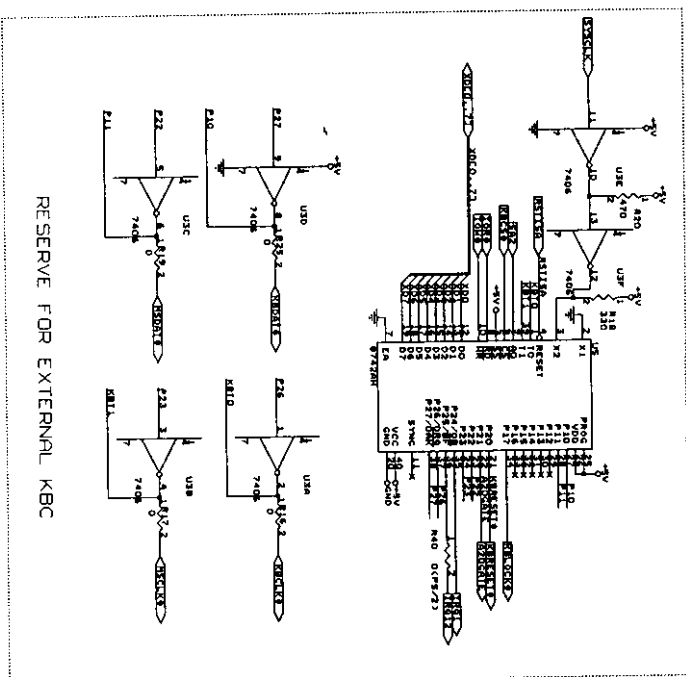
1118	CON-LAT PORT
1119	1119
1120	1120
1121	1121
1122	1122
1123	1123
1124	1124
1125	1125
1126	1126
1127	1127
1128	1128
1129	1129
1130	1130
1131	1131
1132	1132
1133	1133
1134	1134
1135	1135
1136	1136
1137	1137
1138	1138
1139	1139
1140	1140
1141	1141
1142	1142
1143	1143
1144	1144
1145	1145
1146	1146
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1173	1173
1174	1174
1175	1175
1176	1176
1177	1177
1178	1178
1179	1179
1180	1180
1181	1181
1182	1182
1183	1183
1184	1184
1185	1185
1186	1186
1187	1187
1188	1188
1189	1189
1190	1190
1191	1191
1192	1192
1193	1193
1194	1194
1195	1195
1196	1196
1197	1197
1198	1198
1199	1199
1200	1200







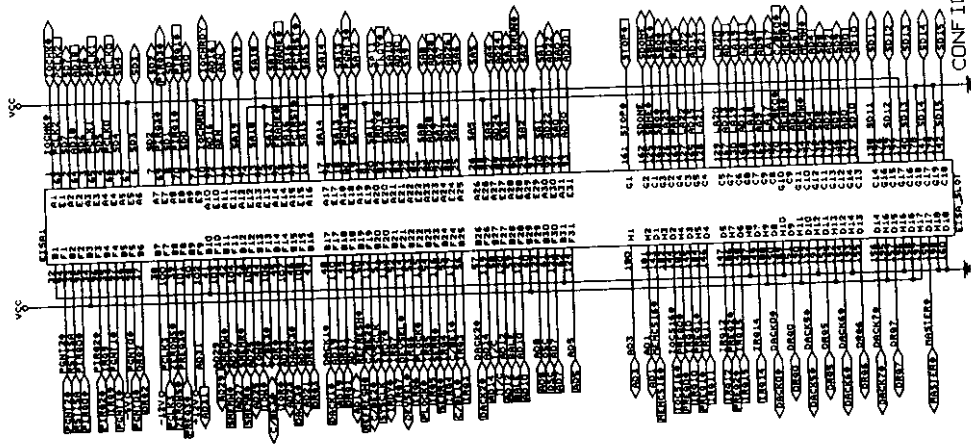
Resistors are implemented as capacitor traces on Inter 1 Supr.
 For J88, Connect.
 Note 1: Trace values are 50 mls, 1.24" long.
 Note 2: Trace values are 50 mls, 0.82" long.



RESERVE FOR EXTERNAL KBC

DO NOT REPRODUCE

ISA SLOTS 3&4



INTEL SECRET
CONFIDENTIAL INFORMATION