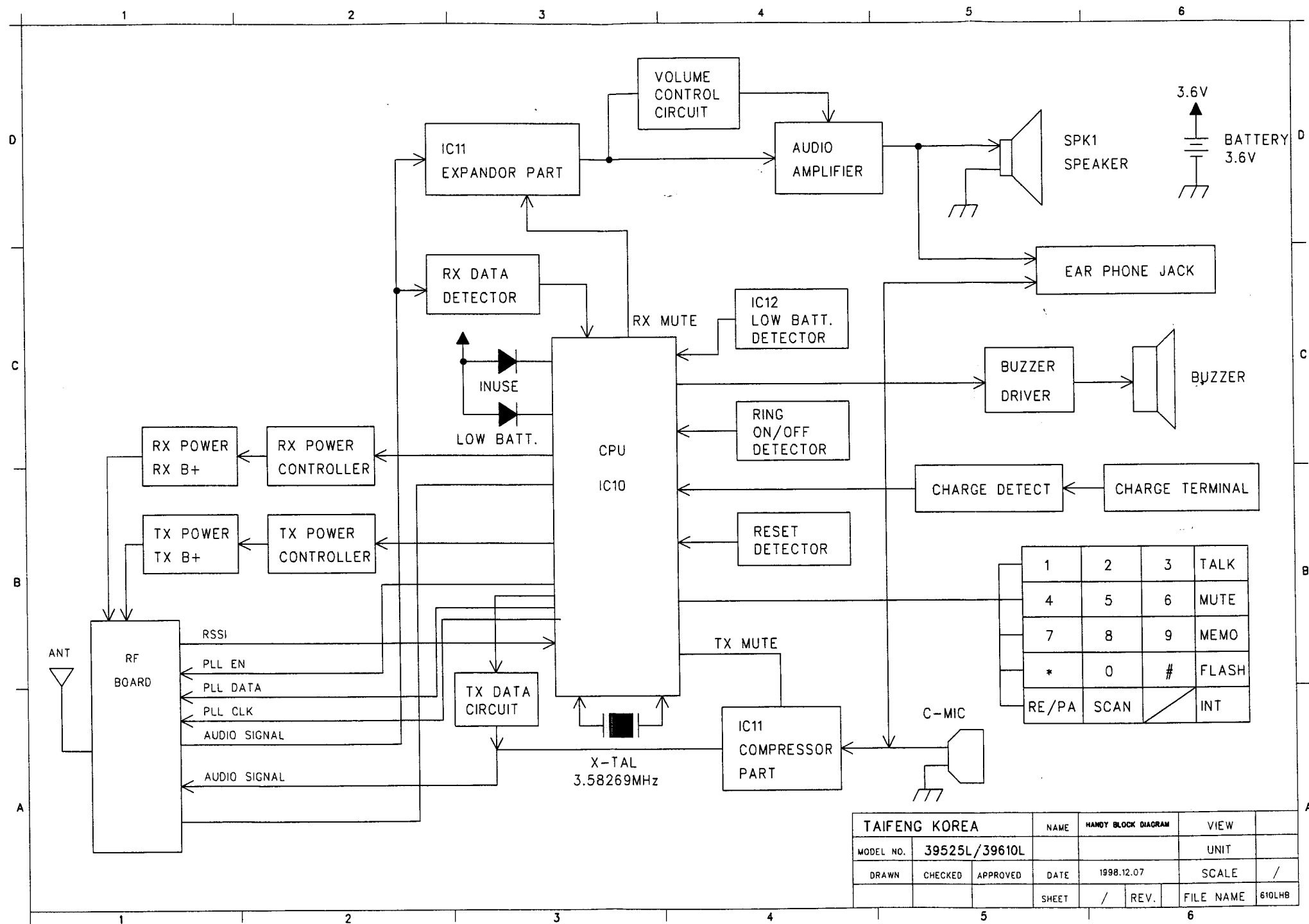
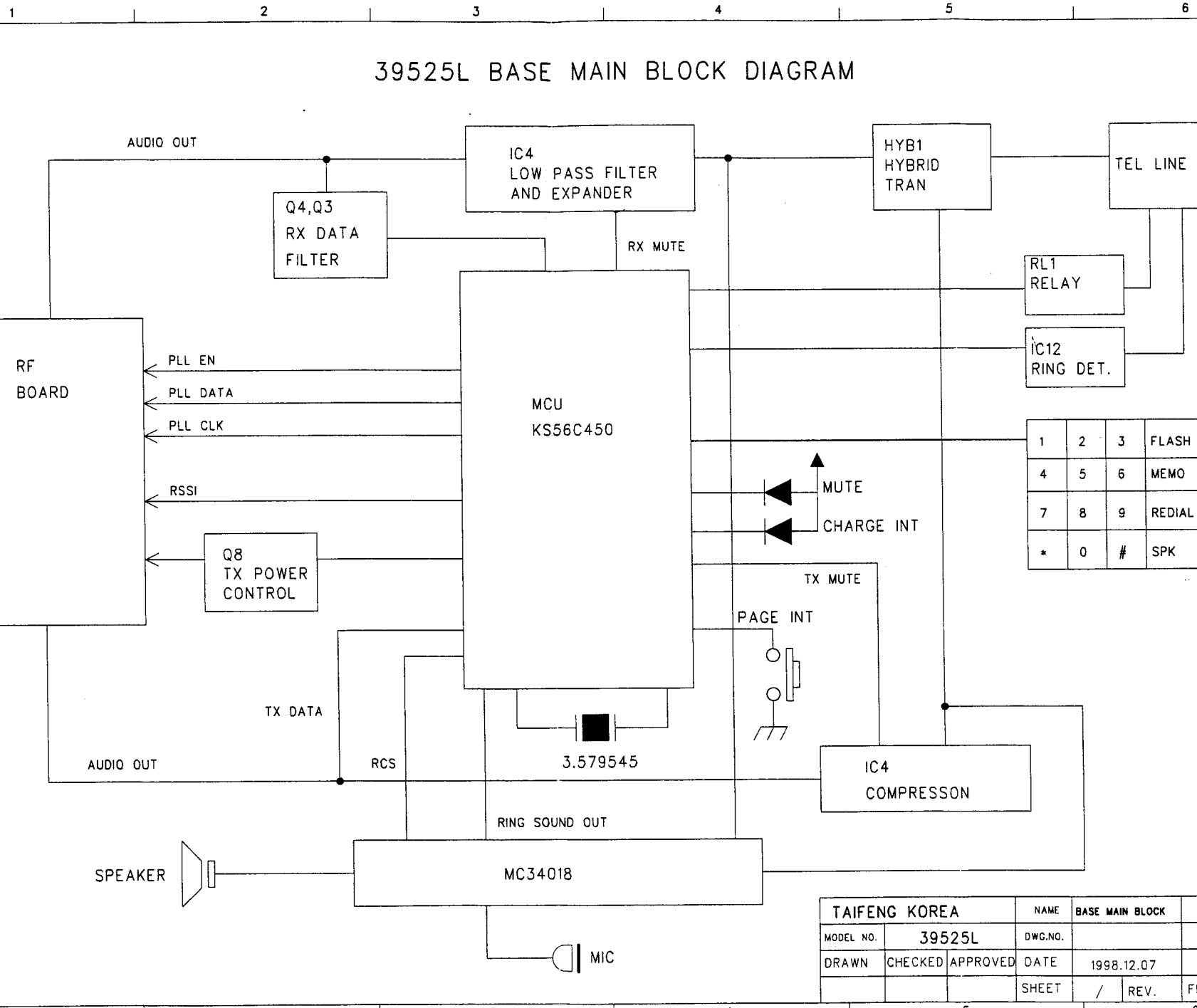






RF BOARD BLOCK DIAGRAM

The diagram illustrates the functional blocks and signal paths of an RF board. The components and their interconnections are as follows:

- Antenna (ANT)** is connected to a **BPF** (Band Pass Filter).
- The signal from the BPF goes to the **RF AMP** (Radio Frequency Amplifier).
- The output of the RF AMP is connected to **RX MIXER1**.
- The output of RX MIXER1 is connected to **CF1** (Coupling Filter).
- The output of CF1 is connected to **RX MIXER2**.
- The output of RX MIXER2 is connected to **CF2 450KHz**.
- The output of CF2 is connected to the **DEMODULATOR**.
- The output of the DEMODULATOR is connected to the **AUDIO AMPLIFIER**.
- The output of the AUDIO AMPLIFIER is connected to the **CONN1** connector.
- The **CONN1** connector has the following pins: RSSI, AF OUT, S/Q, PLL EN, PLL DATA, PLL CLK, RX B+, RX GND, TX B+, TX GND, and AF/DATA.
- The **TX VCO** (Transmit Voltage Controlled Oscillator) is connected to the **TX BUFFER AMP** and the **TX POWER AMP**.
- The output of the TX POWER AMP is connected to a **BPF**.
- The output of the BPF is connected to the **ANT**.
- The **TX VCO** is also connected to the **IC2** (Integrated Circuit 2) via **PD2**.
- The **IC2** has pins **FIN1**, **FIN2**, **PD1**, **XOUT**, and **XIN**.
- The **IC2** is connected to the **RX VCO** (Receive Voltage Controlled Oscillator) via **PD1**.
- The **IC2** is also connected to the **DEMODULATOR** via **XIN**.
- The **IC2** is connected to the **TX VCO** via **XOUT**.
- The **IC2** is connected to the **CONN1** connector via **AF/DATA**.
- The **IC2** is connected to the **TX B+** and **TX GND** pins of the **CONN1** connector.
- The **IC2** is connected to the **RX B+** and **RX GND** pins of the **CONN1** connector.
- The **IC2** is connected to the **PLL EN**, **PLL DATA**, and **PLL CLK** pins of the **CONN1** connector.
- The **IC2** is connected to the **AF OUT** and **S/Q** pins of the **CONN1** connector.
- The **IC2** is connected to the **RSSI** pin of the **CONN1** connector.
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- The **IC2** is connected to the **PLL CLK** pin of the <

TAIFENG KOREA			NAME	RF BLOCK DIAGRAM			VIEW	
MODEL NO.	39525L/39610L		DWG.NO.				UNIT	
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