

Model-1000 GPIO ASSIGNMENT

GP10	DOCKSM1#	FOR DOCKING	ACTIVE LOW
GP11	PIIX4 GP11#	FOR ACPI	ACTIVE LOW
GP12	FANFAULT#	FOR W83L784	ACTIVE LOW
GP13	CARDIOC#	FOR CARD BUS	ACTIVE LOW
GP14	AD_PRESENT#	FOR RSM	ACTIVE HIGH
GP15	CHARGERIN#	FOR RSM	ACTIVE LOW
GP16	BAT_PRESENT#	FOR RSM	ACTIVE LOW
GP17	D/P	DOCK/PORTICLE SWITCH	0 : PORTICLE 1 : DOCKING
GP18	784 SM1#	FOR W83L784	ACTIVE LOW
GP19	BATLOW#	FOR PME (W83L784)	ACTIVE LOW
GP110	L1DM	FOR RSM RES	ACTIVE LOW
GP111	SHALERT#	FOR ACPI (RING)	ACTIVE LOW
GP112	WAKE UP#	FOR RSM	ACTIVE LOW
GP113	GP113	FOR AUDIO	ACTIVE LOW
GP114	CHT DNB		ACTIVE LOW
GP115	M1D0		ACTIVE HIGH
GP116	M1D1		ACTIVE HIGH
GP117	M1D2		ACTIVE HIGH
GP118	M1D3		ACTIVE HIGH
GP119	VGA_BUSY#	FOR RSM	ACTIVE LOW
GP120	CARD_LED#	FOR RSM	ACTIVE LOW
GP121	VRCHANGE	RESERVED	
GP122	****	*****	*****
GP00	SENSOR_PD#	FOR TOUCH PANEL	ACTIVE LOW
GP01	PDW#	FOR PRIMARY HDD	ACTIVE HIGH
GP02	BAV_PWR	FOR SECONDARY CD-ROM	ACTIVE HIGH
GP03	INH1B		ACTIVE HIGH
GP04	DRAMENA		ACTIVE HIGH
GP05	DRAMENB		ACTIVE HIGH
GP06	USB/SW	DOCK/PORTICLE SWITCH	1 : CDD USB 0 : DOCKING USB
GP07	PIIX4_EVEN#	CARDBUS ZVPORT ENABLE	ACTIVE LOW
GP08	POSSBETH	FOR HDD RESET	ACTIVE LOW
GP09	2RTX_PWR	FULL OR 2/3 PWR	
GP10	X		
GP11	X		
GP12	SOREBETH	FOR CD-ROM RESET	ACTIVE LOW
GP13	IO_CFW	FOR SERIALS PORT	ACTIVE LOW
GP14	B10SNPD		ACTIVE LOW
GP15	S10SB#		ACTIVE LOW
GP16	S10SC#		ACTIVE LOW
GP17	CPU_STP#		ACTIVE LOW
GP18	PCI_STP#		ACTIVE LOW
GP19	CDD OFF	FOR CDD	ACTIVE HIGH
GP20	SUS_STAT1#	FOR 440BX	ACTIVE LOW
GP21	SUS_STAT2#	FOR VGA_STP_ADP#	ACTIVE LOW
GP22	784_PWDON#	FOR W83L784	ACTIVE LOW
GP23	GP201#	FOR VGA_ADP_SUS#	ACTIVE LOW
GP24	CDD_PD	FOR RSM	ACTIVE HIGH
GP25	RCM12V		ACTIVE HIGH
GP26	KBC#		ACTIVE LOW
GP27	PIIX4_GP027	FOR DOCKING	ACTIVE HIGH
GP28	PMINI-IN	DOCKING MINIPCI IN	ACTIVE HIGH
GP29	X		
GP30	OP_PD#	FOR AUDIO	ACTIVE LOW

PAGE 1 : SPARE PARTS

PAGE 2 : VR_ON & PIIX4 SIDEBAND PULL UP
ISA PULL UP/DOWN & MEM I/F DAMPING
PCIREQ#[0:3] MAP TO PCIREQ#[A:C]
RESUME LOGIC POWER

PAGE 3 : DIMM BANK 0/1,2/3 & PCI PULL UP/DOWN
MEM DATA BUS DAMPING

PAGE 4 : PIIX4

PAGE 5 : H8/3437F & PS/2 PORT & SCARD READER PORT
POWER SUS/RSM , RESET BUTTON

PAGE 6 : CLK GEN & BIOS & PWRGD

PAGE 7 : RJ45,RJ11,BATT,BACKUP BATT,LED/MIC CONN
STR & RING IN & POWER/HDD LED

PAGE 8 : W83877ATF & FIR

PAGE 9 : 1394 : TI TSB12LV22 & TSB41LV03

PAGE 10 : PCI 1250A (CARD BUS CTRL)

PAGE 11 : PC CARD SOCKETS

PAGE 12 : PC CARD & CCD ZV BUFFER

PAGE 13 : SAA7111A

PAGE 14 : ESS 1980-M3 & ES1921 & CD IN & LINE_IN

PAGE 15 : LM4863,SPK_OUT & MICRO-PHONE

PAGE 16 : DOCKING CONN & MINIPCI CONN & CCD CONN

PAGE 17 : DIMM PD CIRCUIT & PRIMARY HDD,CD-ROM CONN & USB

PAGE 18 : SMI LYNX3DM

PAGE 19 : LVDS INTERFACE & LCD CONN

PAGE 21 : W83L784R & FAN CONN

PAGE 22 : CHARGER CKT.

PAGE 23 : UPGA1 CONN CIRCUIT 1

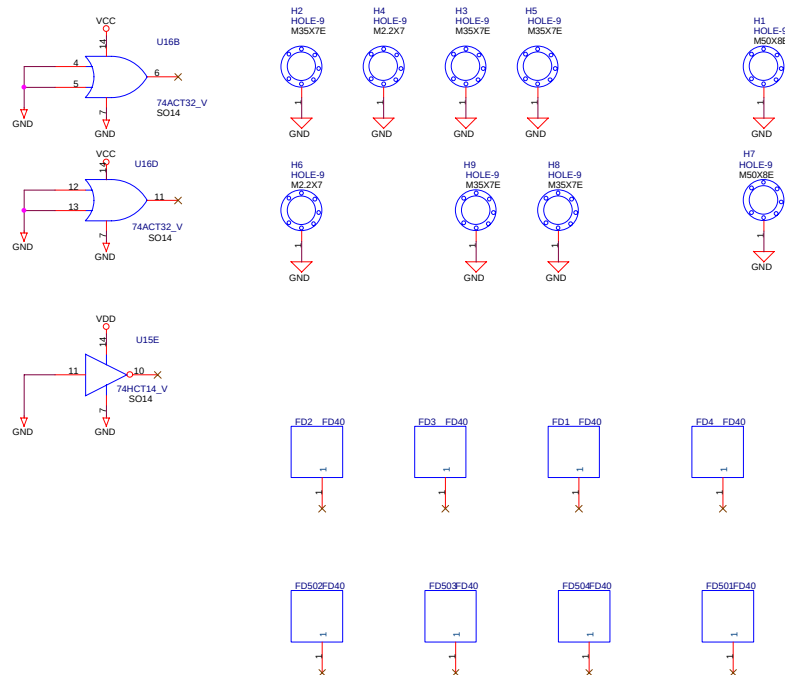
PAGE 24 : UPGA1 CONN CIRCUIT 2

PAGE 25 : 440BX CIRCUIT 1

PAGE 26 : 440BX CIRCUIT 2

PAGE 27 : UPGA1 CPU POWER

PAGE 28 : TOUCH PANEL CIRCUIT

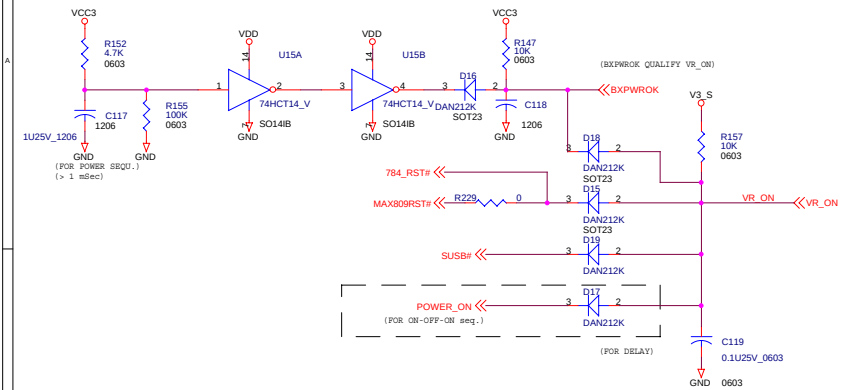


	PCINT	440BX REQ/INT	PIIX4 REQ	REMARK
	A B C D	0 1 2 3 4	A B C D	
PIIX4	X X X X		X X X X	
TONGA		X X X X X		
1394	X		X	
CARDBUS	X X		X	
AUDIO	X X	X	X	
MINIPCI	X X	X X	X	
DOCKING	X X X X	X X	X	
VGA	X		XX	ADP_REQ/INT; ADP_BUSY -> PCIRBQ#

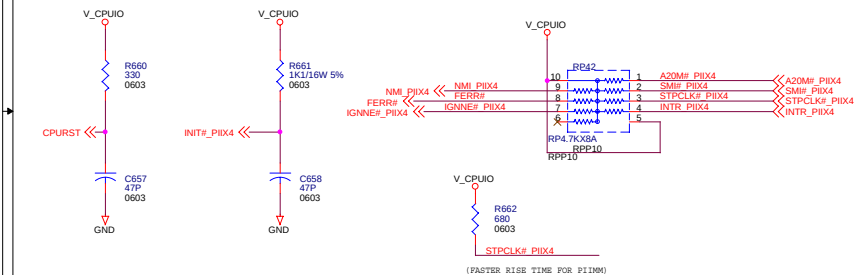
PCI IDSEL ASSIGNMENT

PIIX4	AD18
121V22	AD18
T11250A	AD14
B81980-M3	AD24
W83L784	AD13
VGA-SM721	AD16
DOCK MINIPCI	AD20

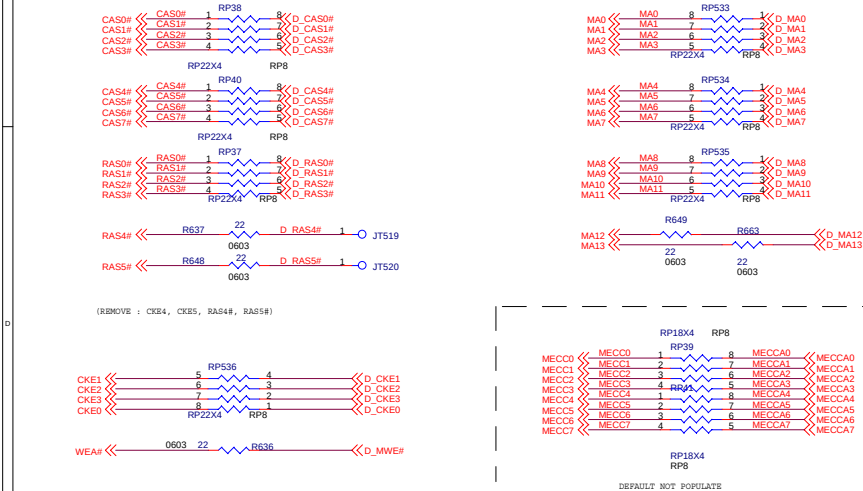
VR ON



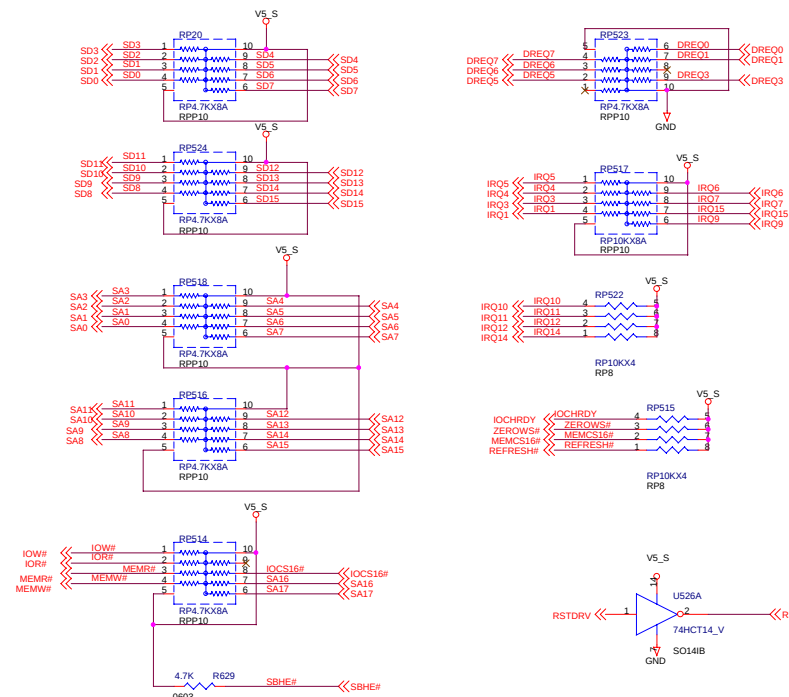
PIIX4 <--> CPU SIDEBAND SIGNAL



MEM I/F DAMPING

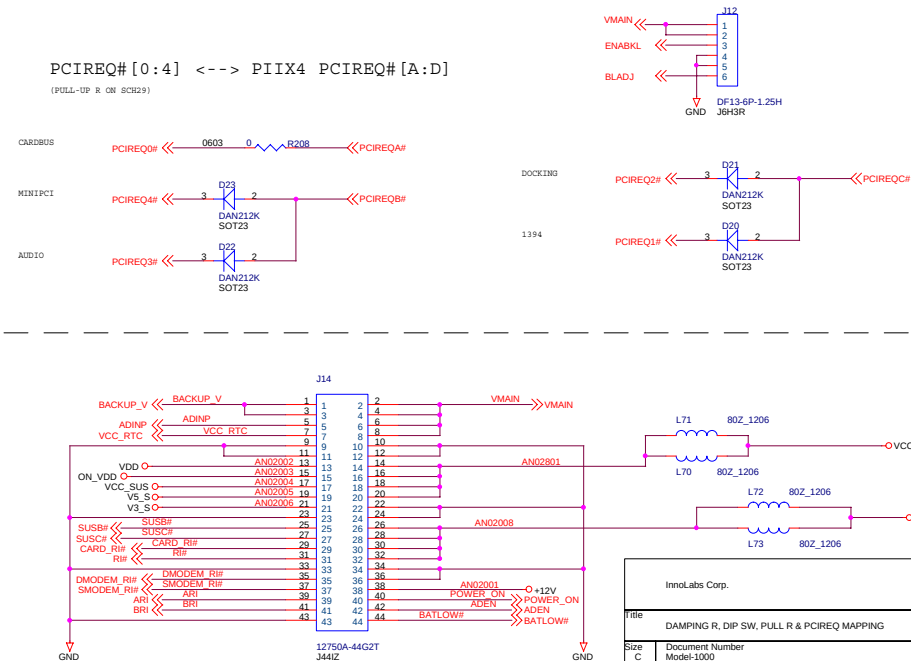


ISA PULL UP/DOWN



PCIREQ# [0:4] <--> PIIX4 PCIREQ# [A:D]

(PULL-UP R ON SCH29)



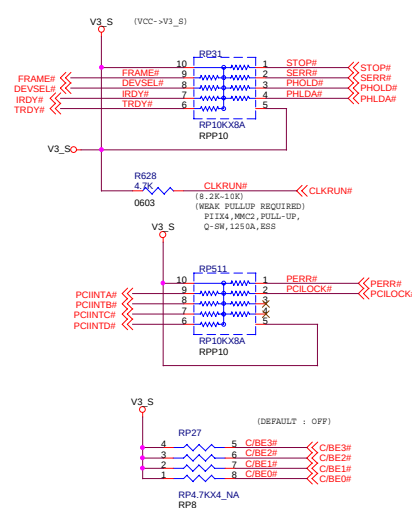
(MDT 18 GNM / R-PACK)

XMD[63] of << XMD[63] of
RP18X4 RP8
MDP52
MDT 18 GNM / R-PACK
MDP52
<< XMD[63] of

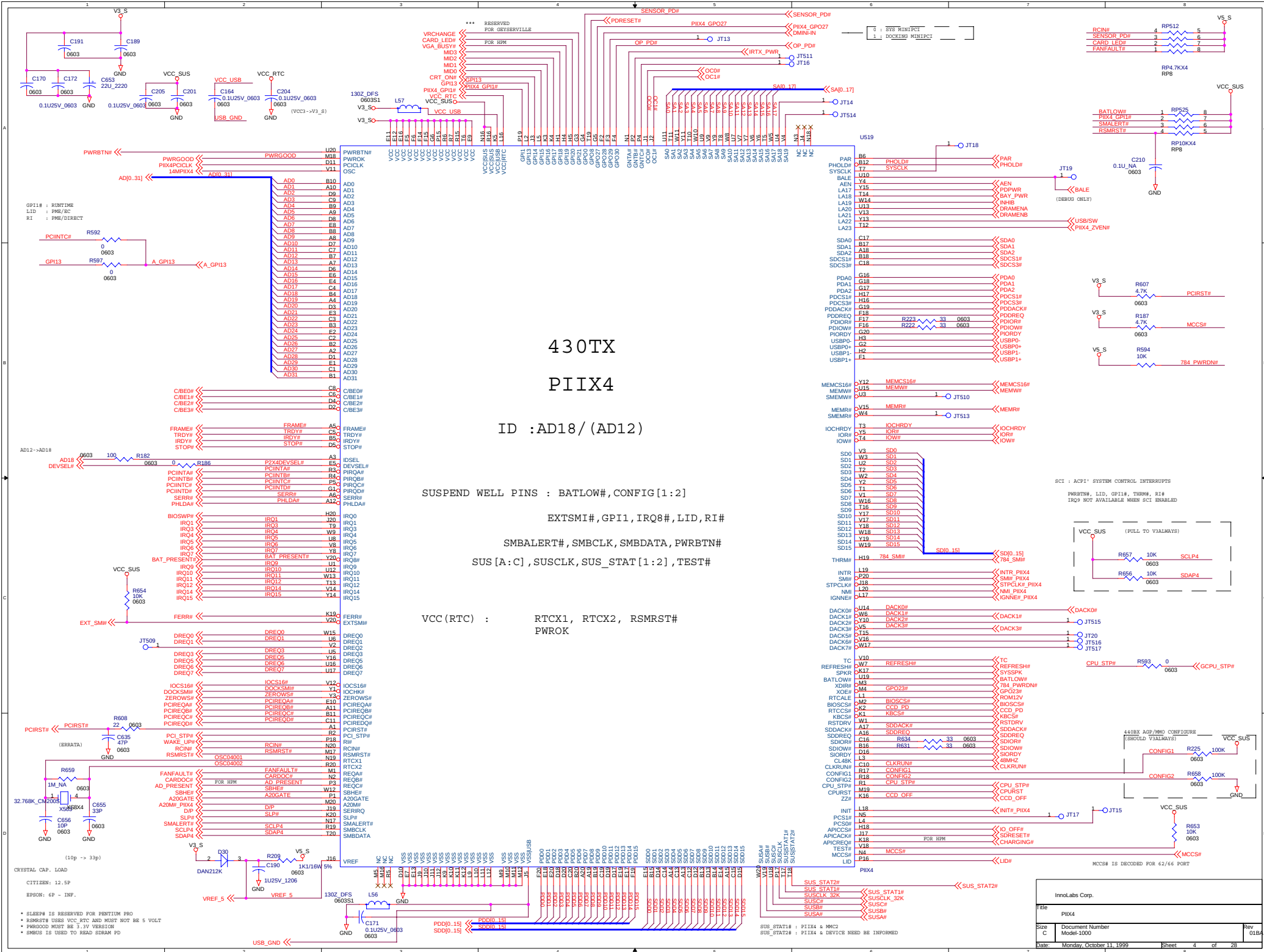
XMD0	5		4	MD0
XMD1	6		3	MD1
XMD2	7		2	MD2
XMD3	5		1	MD3
XMD4	5	RP51	1	MD4
XMD5	6		3	MD5
XMD6	7		2	MD6
XMD7	8	RP50	1	MD7
XMD8	5		4	MD8
XMD9	6		3	MD9
XMD10	7		2	MD10
XMD11	8	RP49	1	MD11
XMD12	5		4	MD12
XMD13	6		3	MD13
XMD14	7		2	MD14
XMD15	8	RP48	1	MD15
XMD16	5		4	MD16
XMD17	6		3	MD17
XMD18	7		2	MD18
XMD19	8	RP47	1	MD19
XMD20	5		4	MD20
XMD21	6		3	MD21
XMD22	7		2	MD22
XMD23	8	RP46	1	MD23
XMD24	5		4	MD24
XMD25	6		3	MD25
XMD26	7		2	MD26
XMD27	8	RP45	1	MD27
XMD28	5		4	MD28
XMD29	6		3	MD29
XMD30	7		2	MD30
XMD31	8	RP44	1	MD31
XMD32	8		1	MD32
XMD33	7		2	MD33
XMD34	6		3	MD34
XMD35	5	RP43	4	MD35
XMD36	8		1	MD36
XMD37	7		2	MD37
XMD38	6		3	MD38
XMD39	5	RP42	4	MD39
XMD40	8		1	MD40
XMD41	7		2	MD41
XMD42	6		3	MD42
XMD43	5	RP41	4	MD43
XMD44	8		1	MD44
XMD45	7		2	MD45
XMD46	6		3	MD46
XMD47	5	RP40	4	MD47
XMD48	8		1	MD48
XMD49	7		2	MD49
XMD50	6		3	MD50
XMD51	5	RP39	4	MD51
XMD52	8		1	MD52
XMD53	7		2	MD53
XMD54	6		3	MD54
XMD55	5	RP38	4	MD55
XMD56	8		1	MD56
XMD57	7		2	MD57
XMD58	6		3	MD58
XMD59	5	RP37	4	MD59
XMD60	8		1	MD60
XMD61	7		2	MD61
XMD62	6		3	MD62
XMD63	5		4	MD63

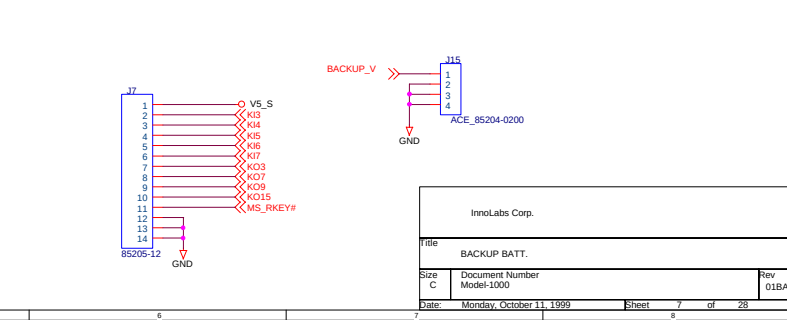
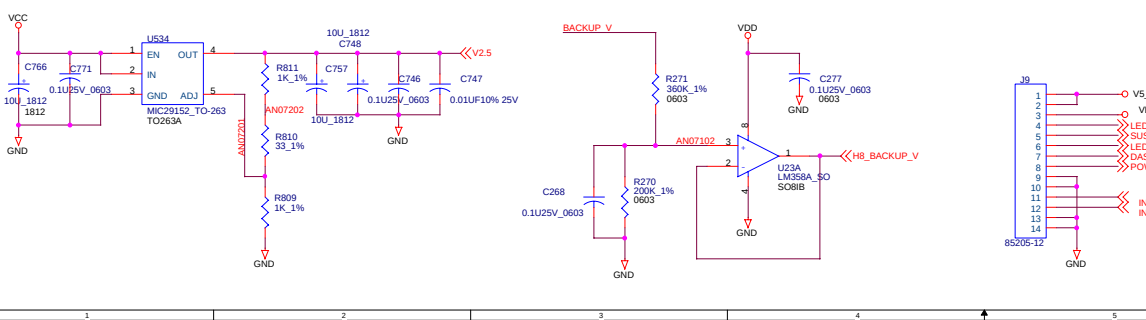
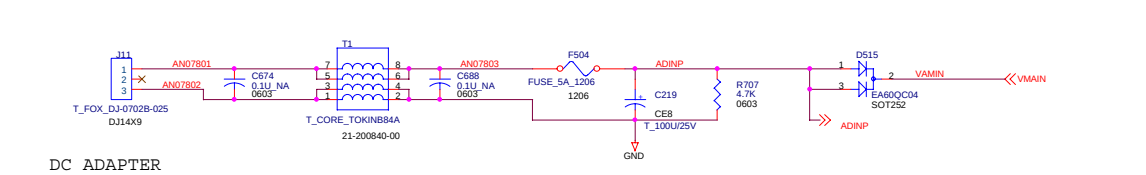
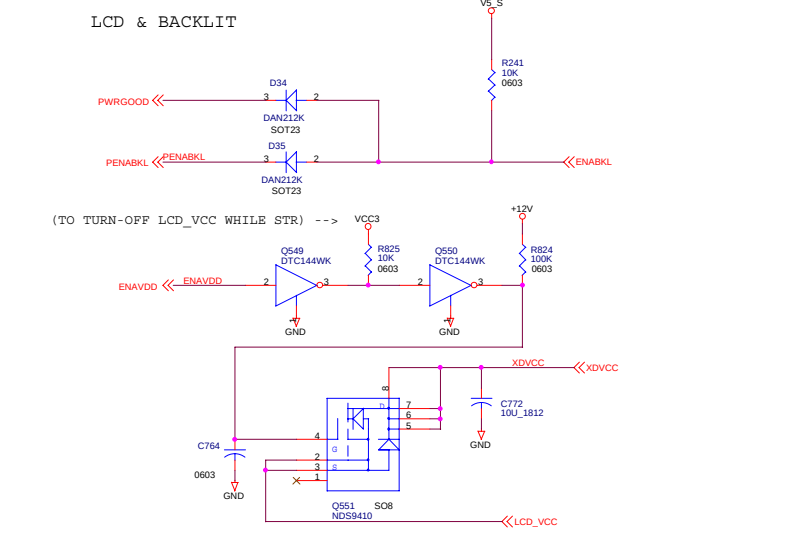
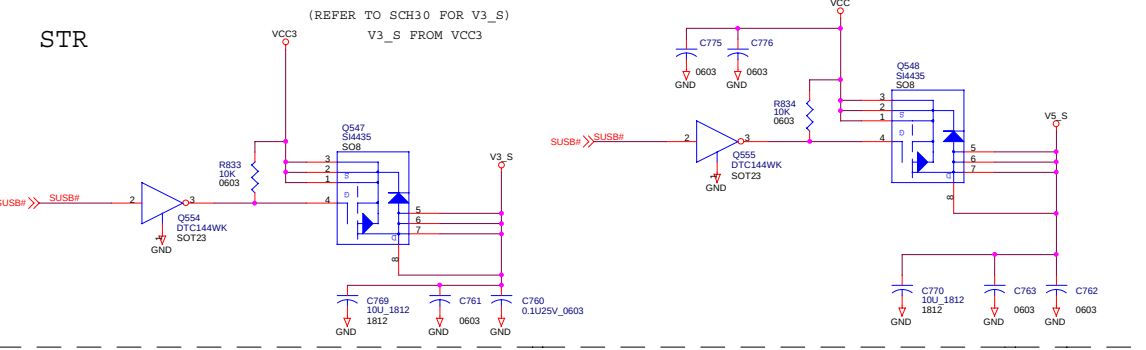
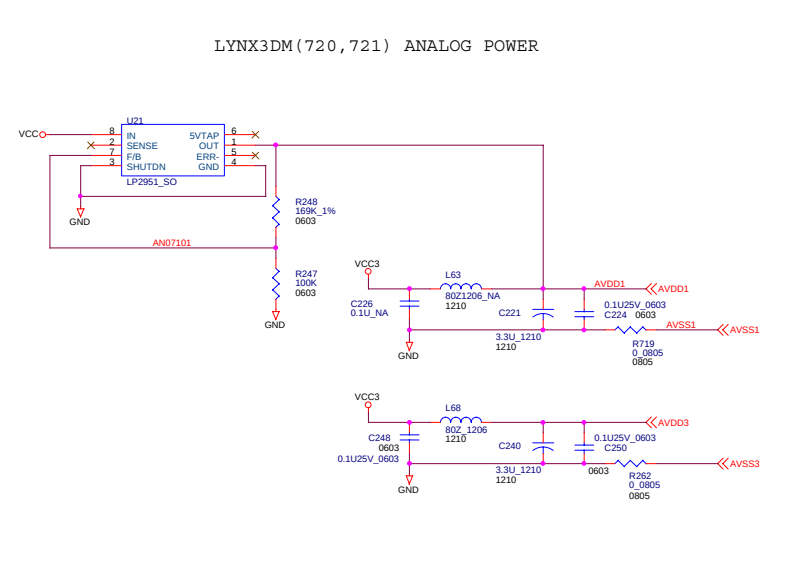
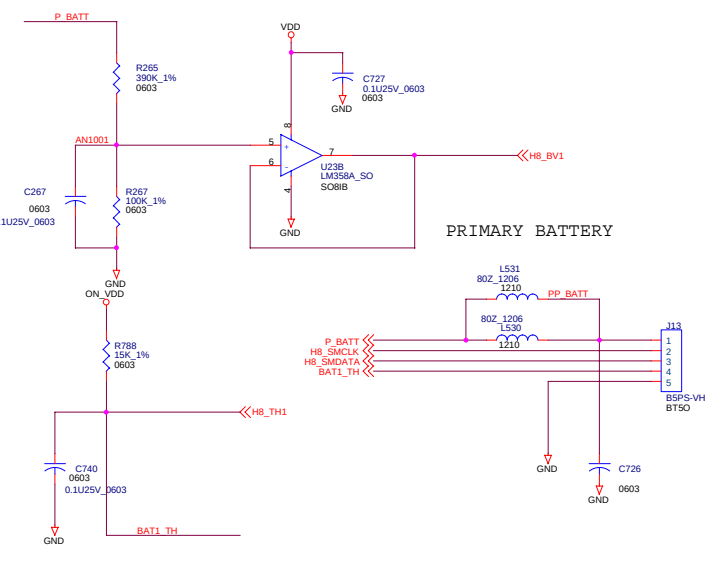
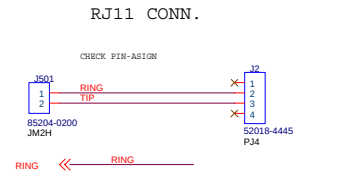
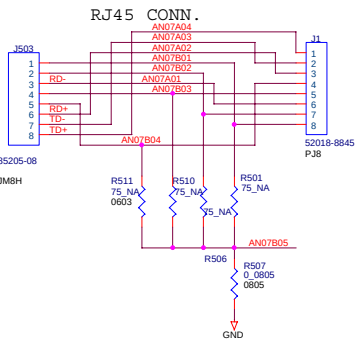
Figure 10 shows the schematic diagram for the PCIE4#0~3, GNT4#0~3, MID0~3, and PCIE4#0~3 signals. Each signal group is connected to a 10K resistor and a 1000pF capacitor, which are connected to V3. The signals are also connected to the RP10KX4 RPS and RP100KX4 RPS.

- PCIE4#0~3:** PCIE4#0, PCIE4#1, PCIE4#2, PCIE4#3. Connected to RP10KX4 RPS.
- GNT4#0~3:** GNT4#0, GNT4#1, GNT4#2, GNT4#3. Connected to RP10KX4 RPS.
- MID0~3:** MID0, MID1, MID2, MID3. Connected to RP100KX4 RPS.
- PCIE4#0~3:** PCIE4#0, PCIE4#1, PCIE4#2, PCIE4#3. Connected to RP100KX4 RPS.

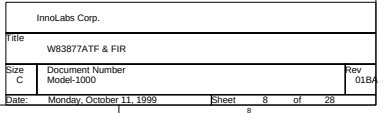


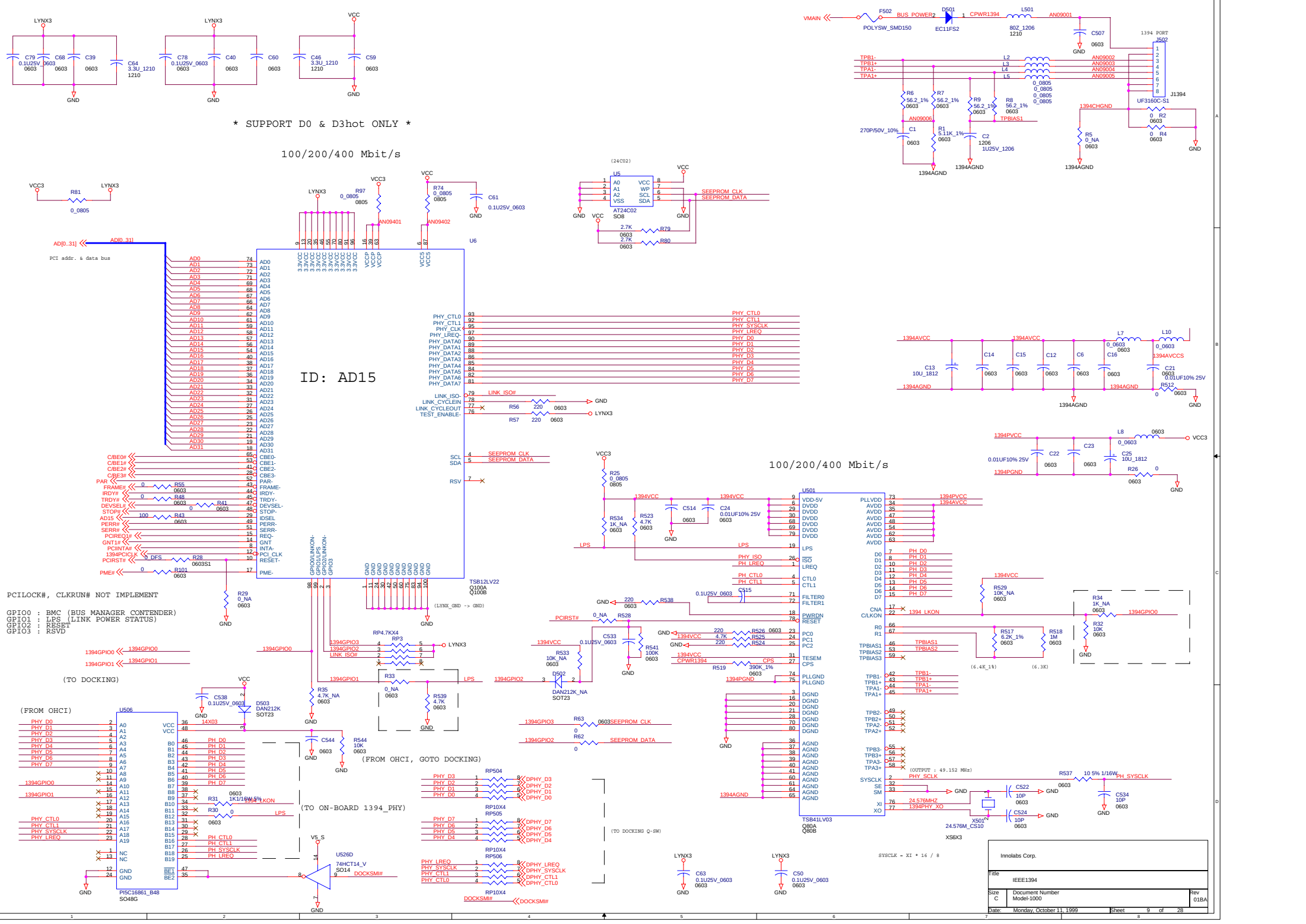
InnoLabs Corp.			
Title: DIMM SOCKETS (BANK0-5)			
Size C	Document Number Model-1000		Rev 01B/
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InnoLabs Corp.		
File	BACKUP BATT.	
Size	Document Number	Rev
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* SUPPORT D0 & D3hot ONLY *

100/200/400 Mbit/s

ID: AD15

100/200/400 Mbit/s

PCLOCK#, CLKRUN# NOT IMPLEMENT
GPIO0 : BMC (BUS MANAGER CONTENDER)
GPIO1 : LPS (LINK POWER STATUS)
GPIO2 : RESET
GPIO3 : RSVD

(TO DOCKING)

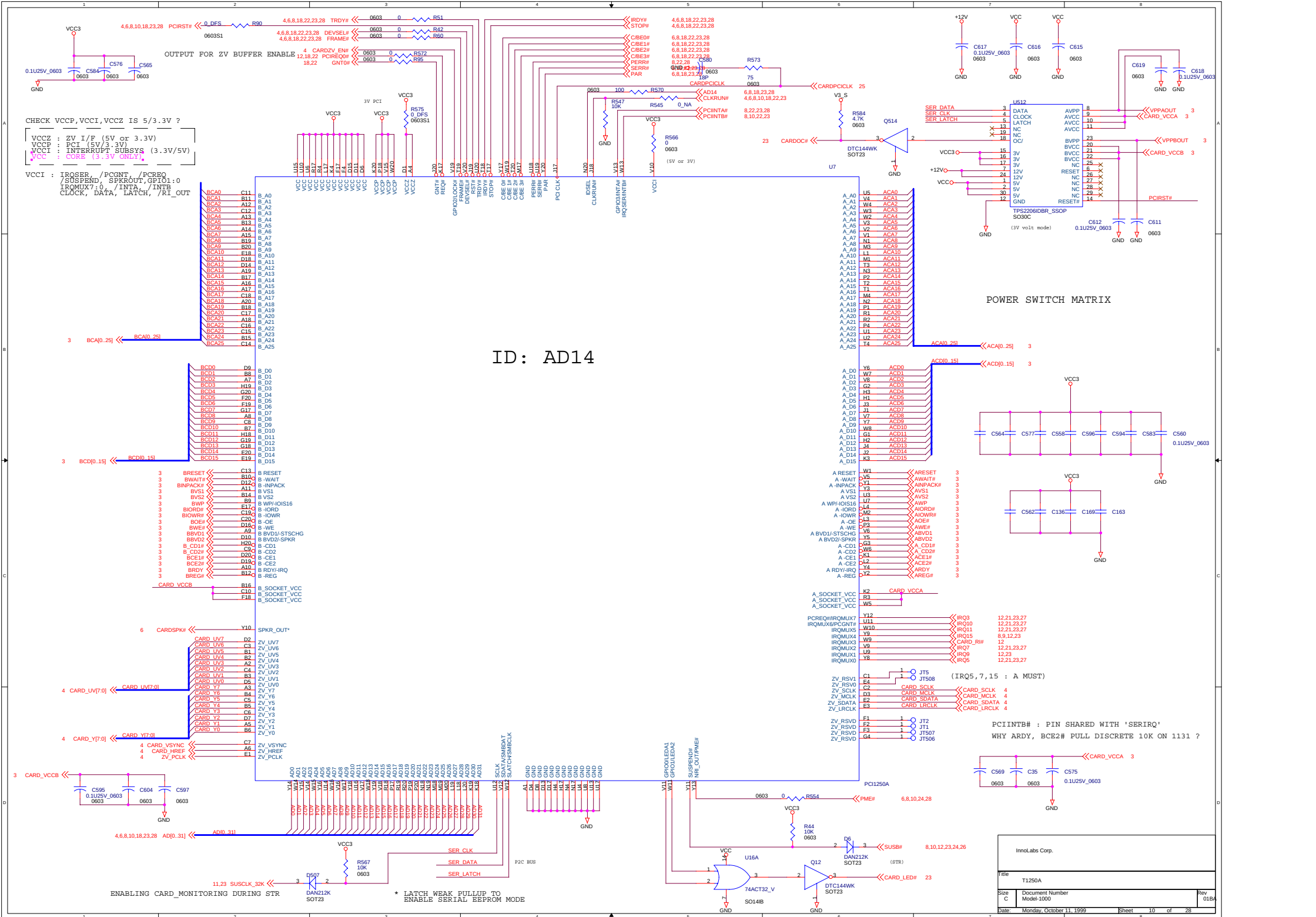
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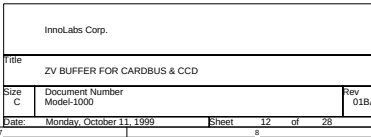
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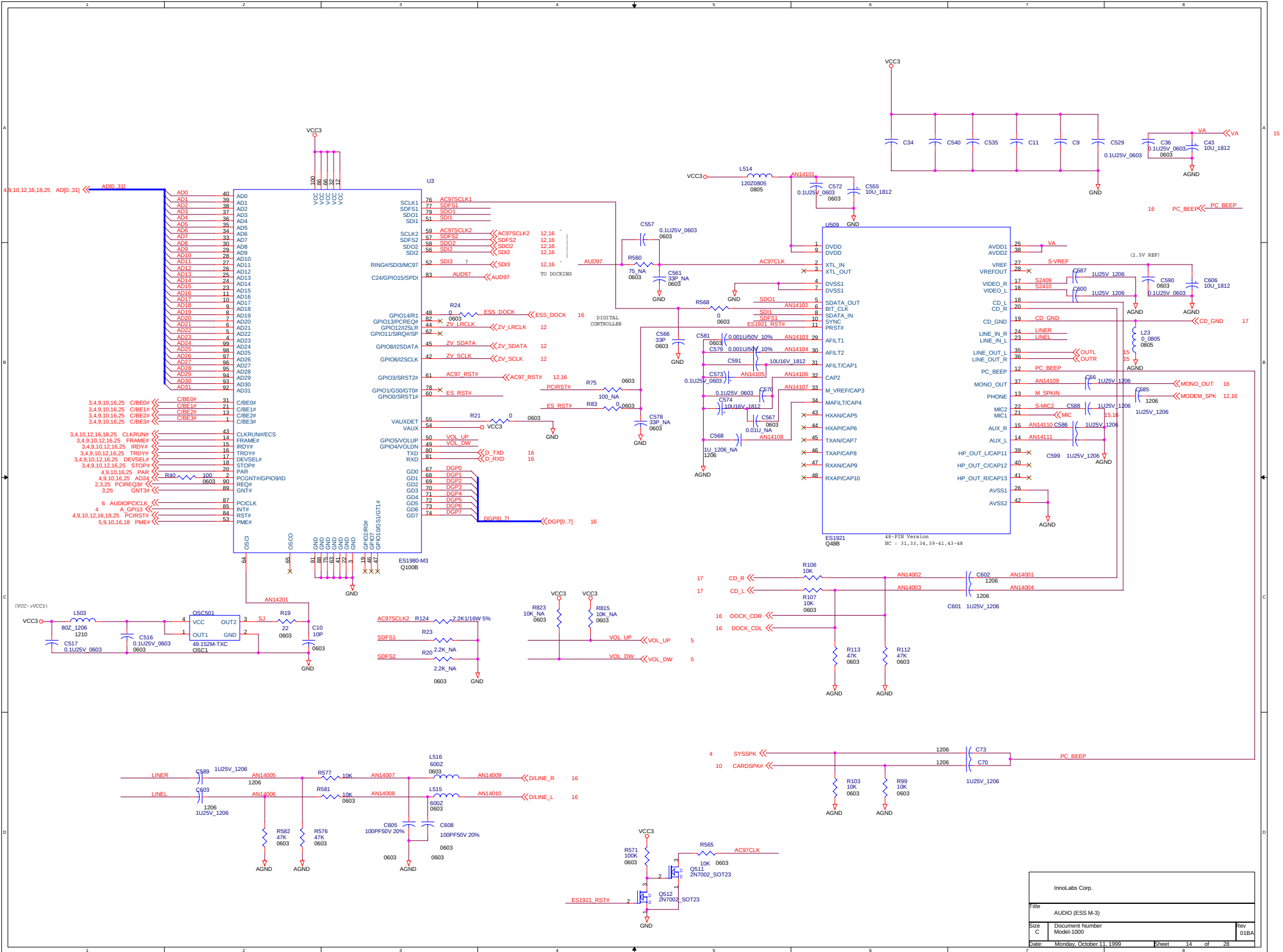
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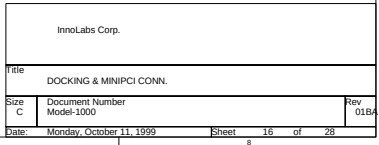
(TO DOCKING Q-SW)

Innolabs Corp.			
Title: IEEE1394			
Size: C	Document Number: Model-1000	Rev: 01BA	
Date: Monday, October 11, 1999	Sheet: 9	of 28	

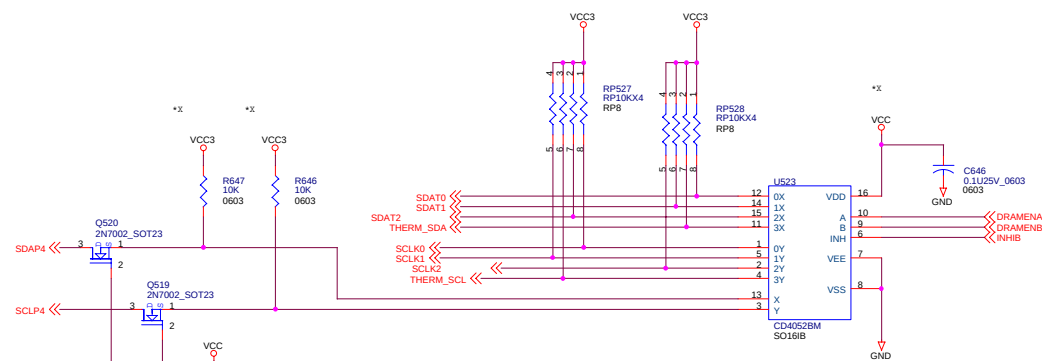


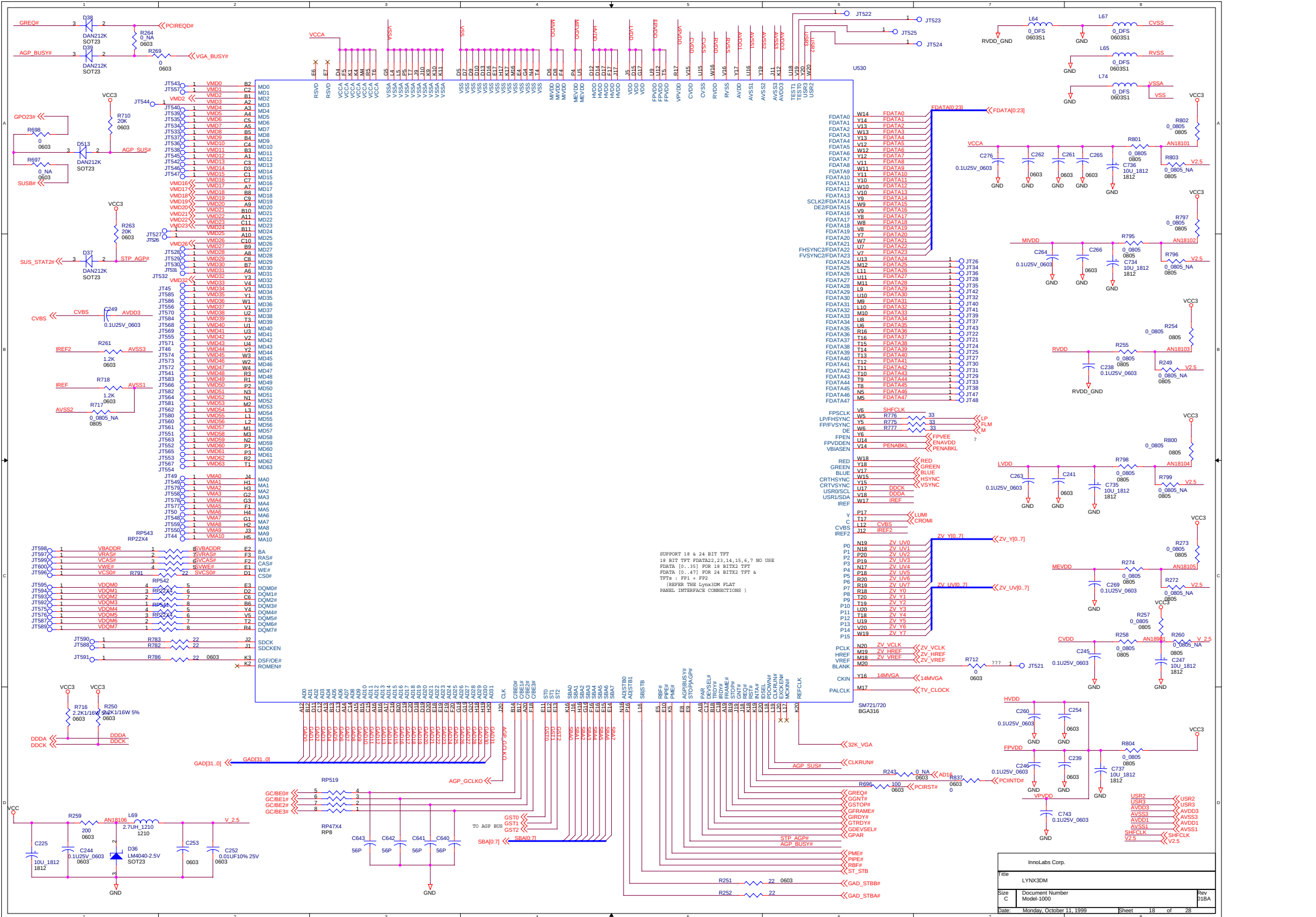


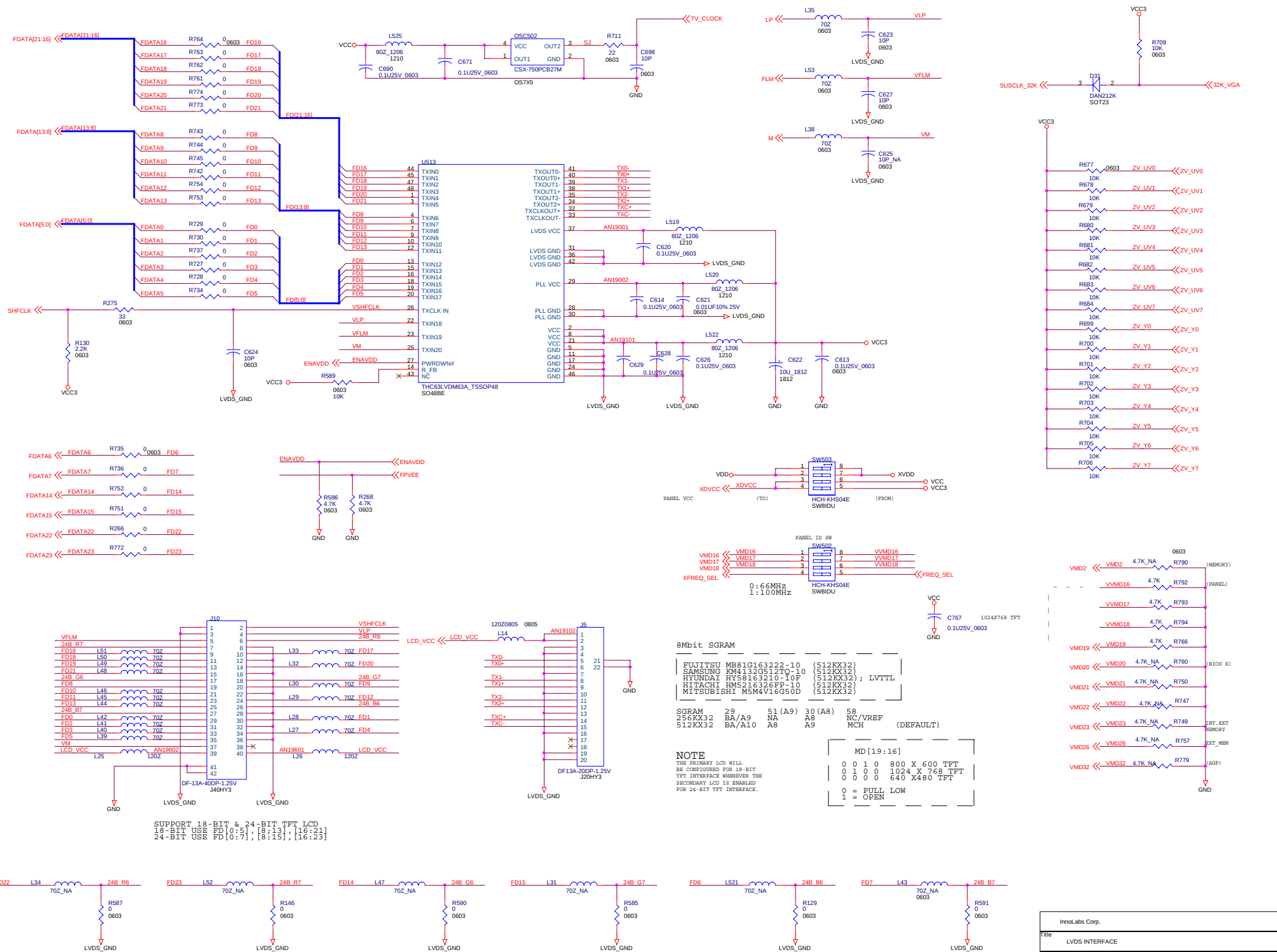




DIMM PD CIRCUITS



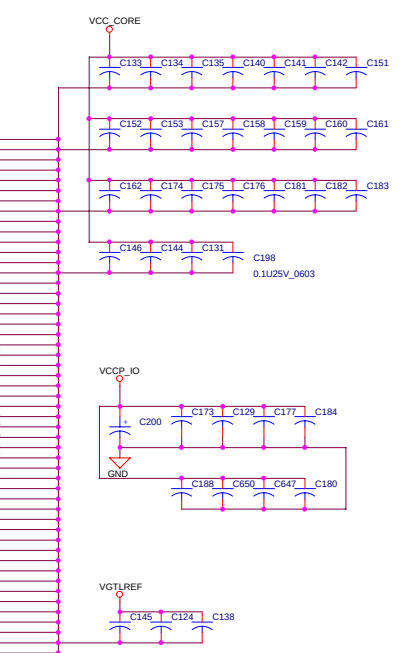
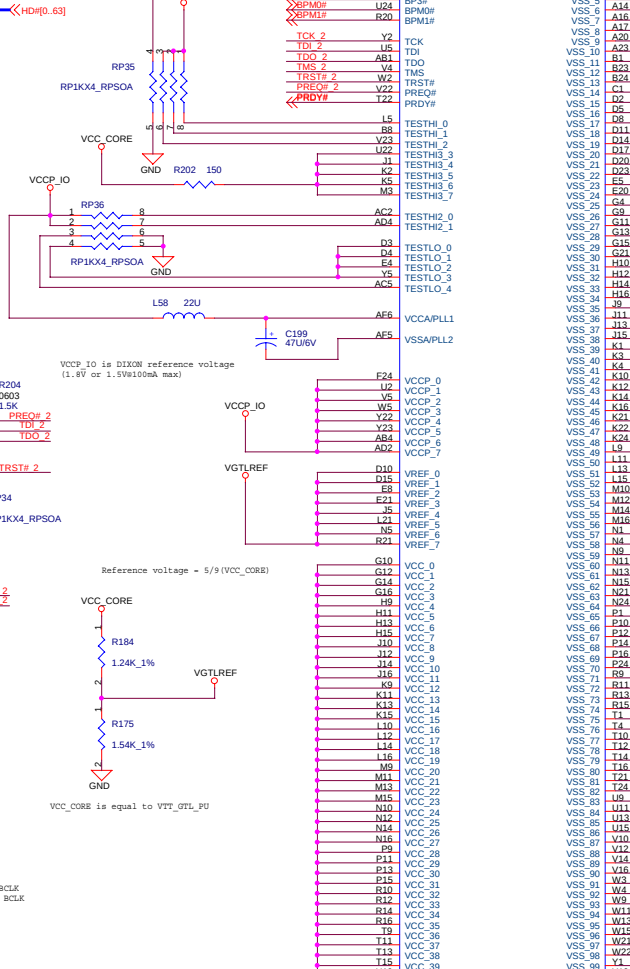
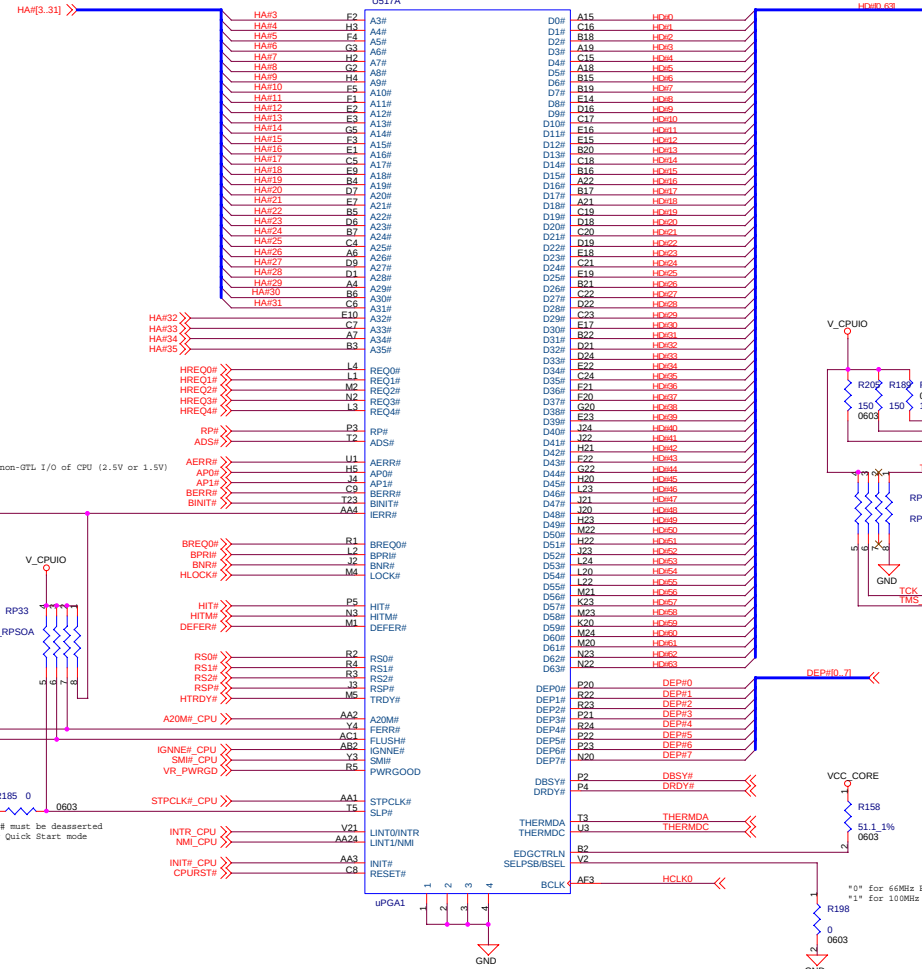
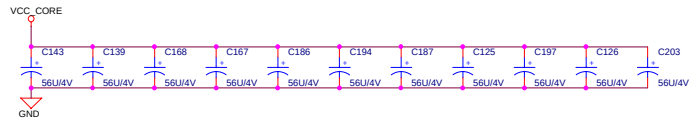






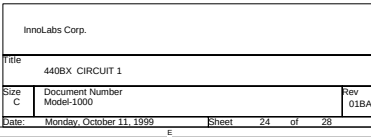
The following signals are not supported by BX but must be terminated since they are GTL signals

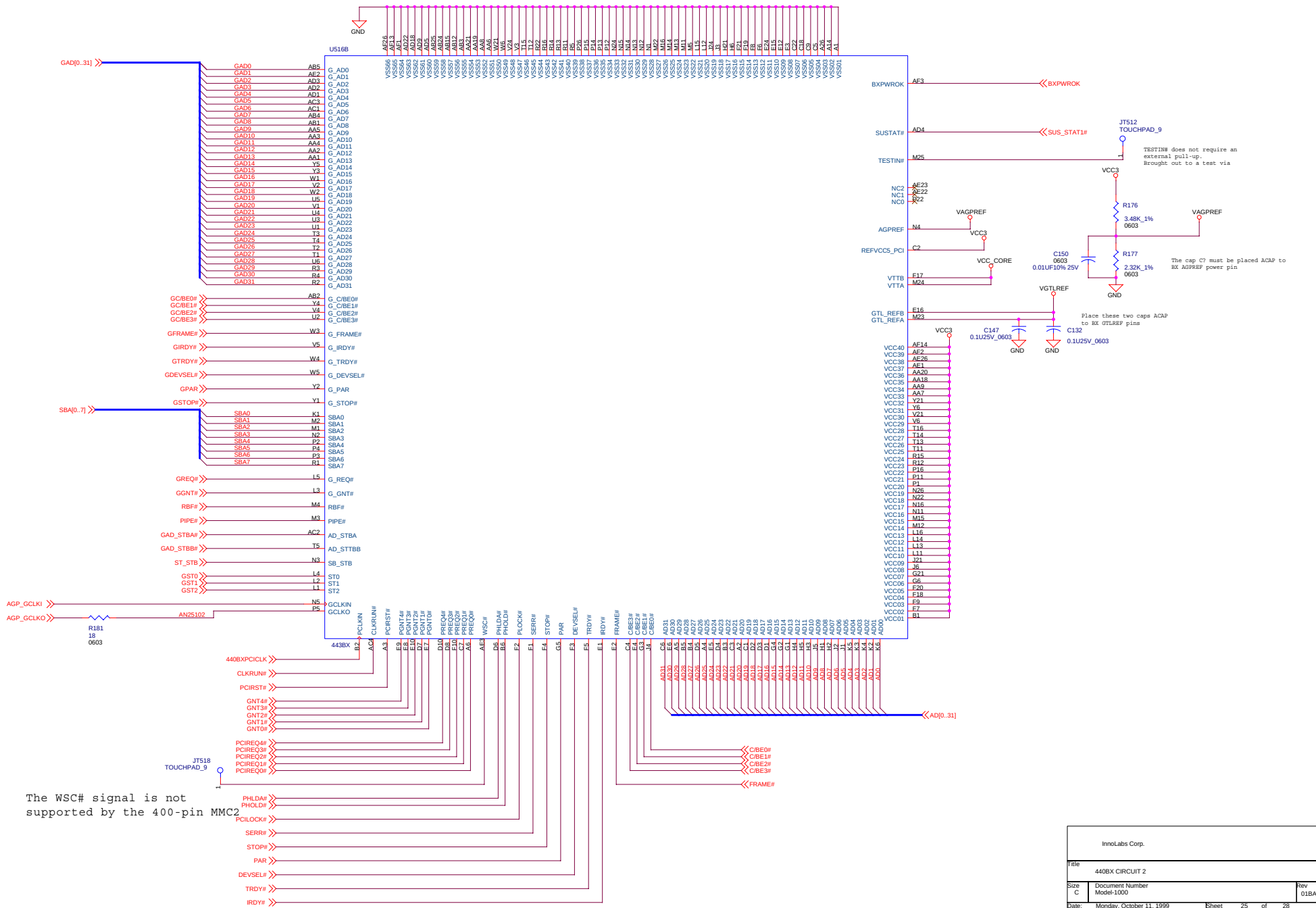
HA[32..35]#
RP#
ABRR#
AP#
AP1#
BERR#
BINT#
RSP#
DEP[0..7]#

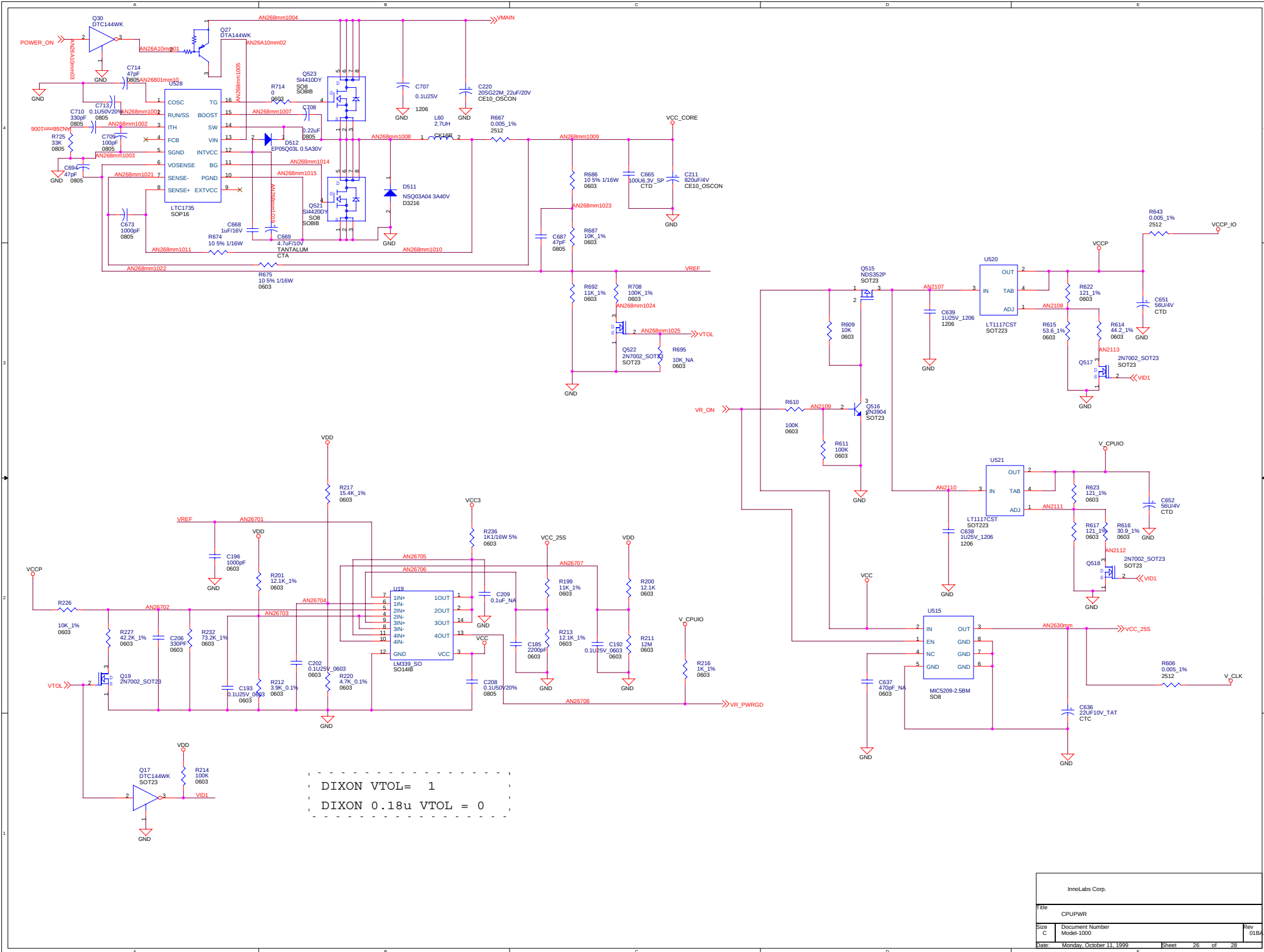


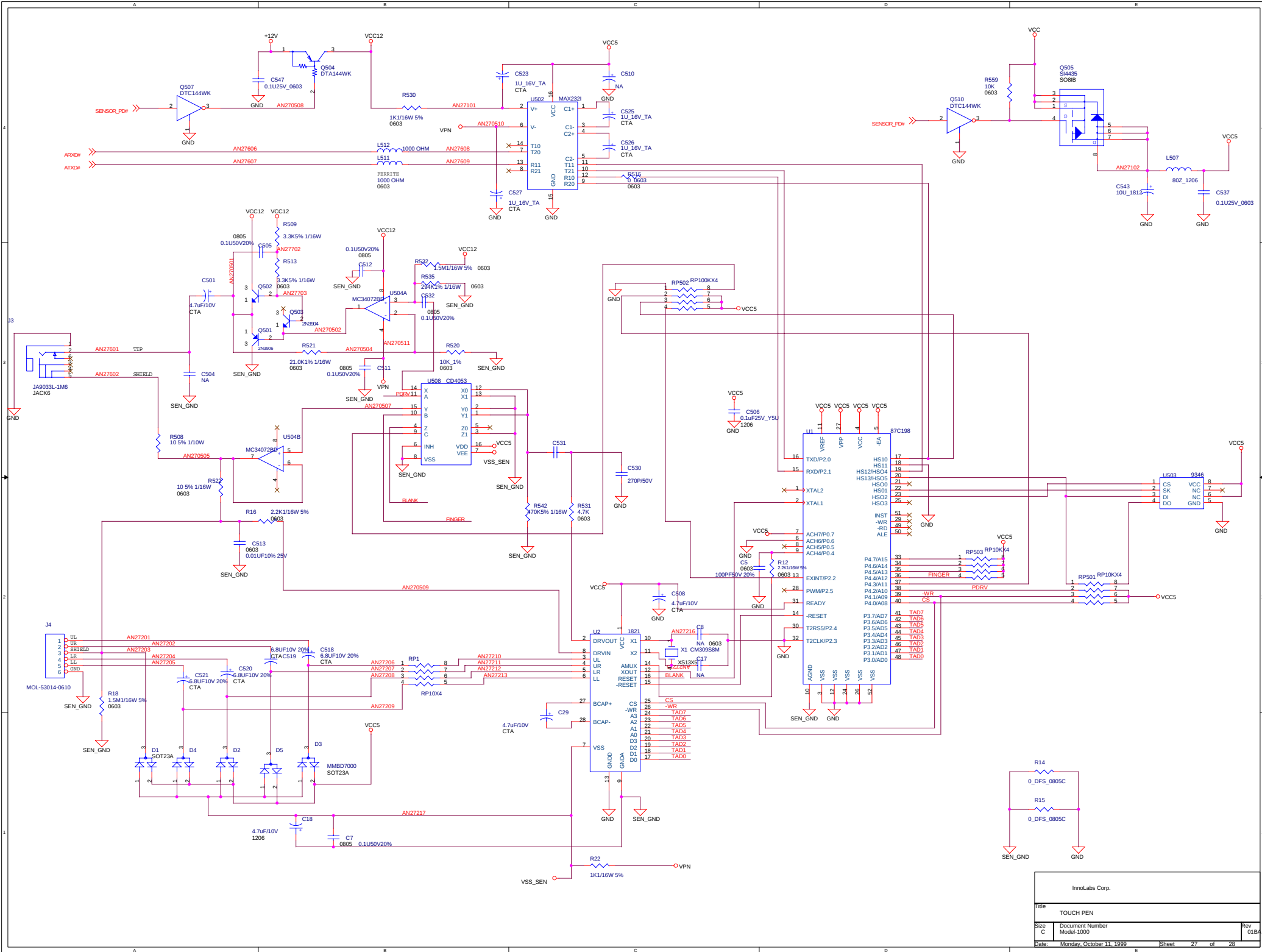
Power Plane

VCC_CORE CPU core power: 1.6V/ Dixon or 1.5V/Dixon-.18u (1.3V/non-0)
V_CPUIO CPU IO power plane: 2.5V/Dixon or 1.5V/Dixon-.18u
VCCP_IO Dixon reference voltage: 1.5V or 1.8V (100mA max)
VGTREF 5/9 NP11 core voltage (VTT_OTL_PU same as core voltage)

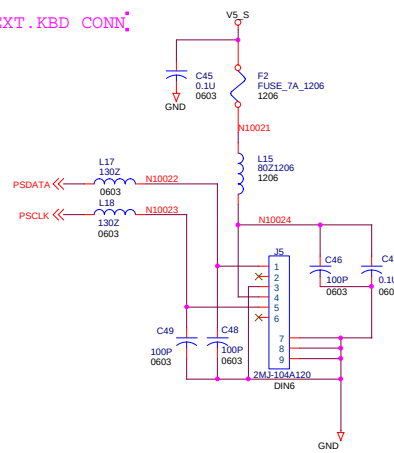




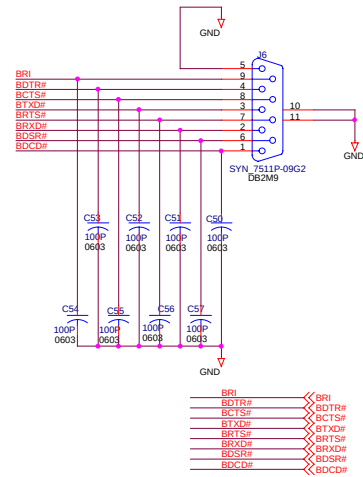




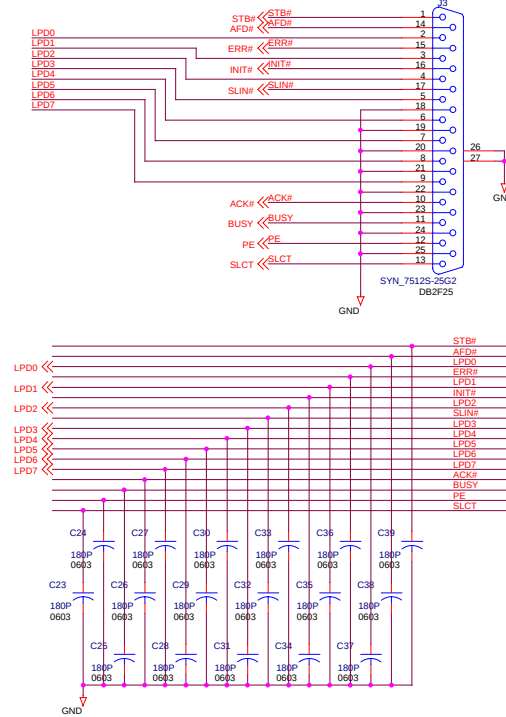
EXT. KBD CONN



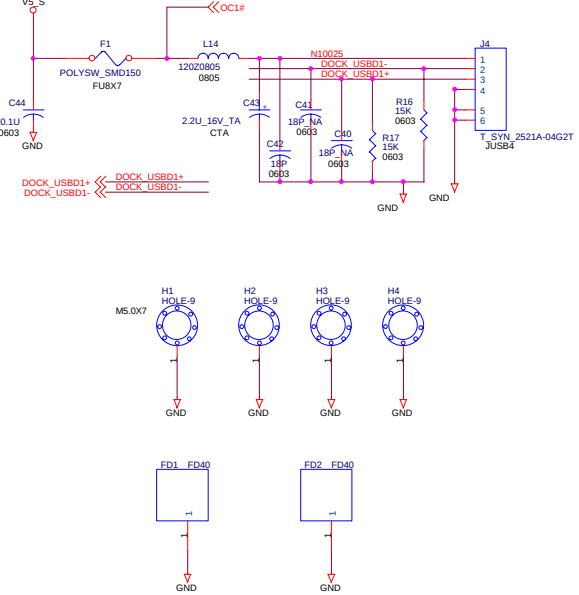
EXP.RS232/SIO



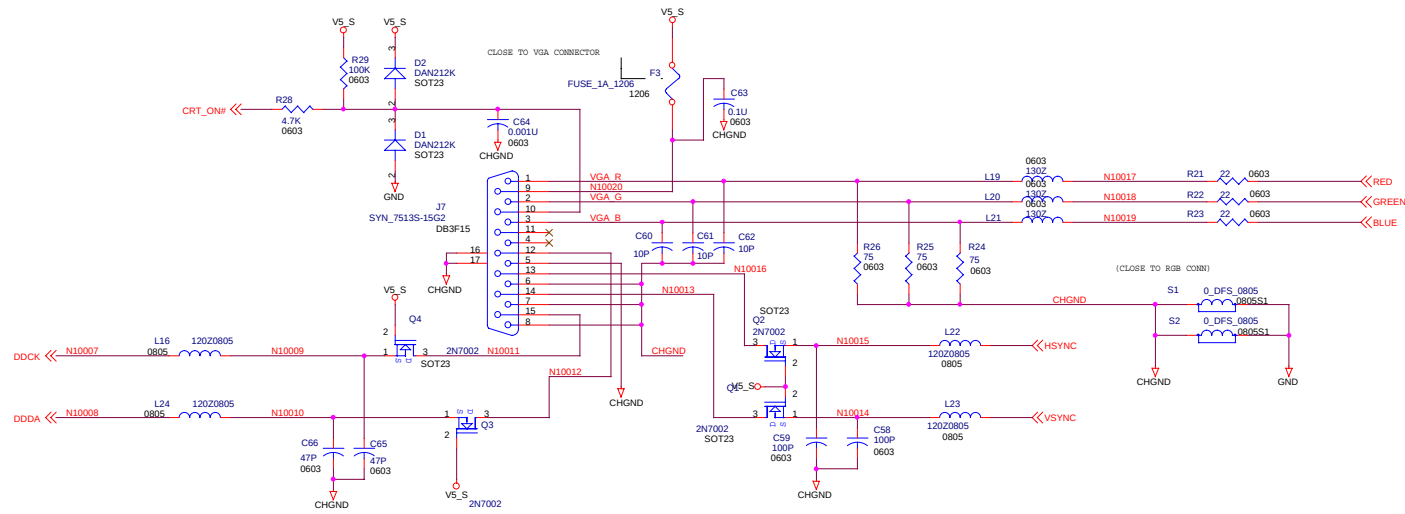
EXP. PIO



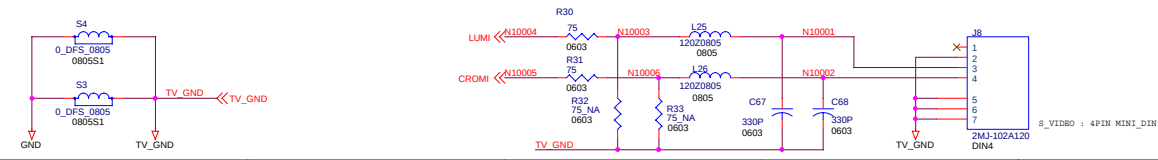
EXP. USB



EXP. CRT



EXP.TV OUT



EXP.GP CONN

