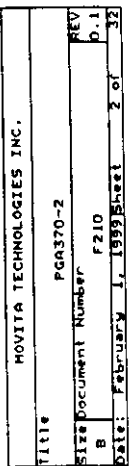
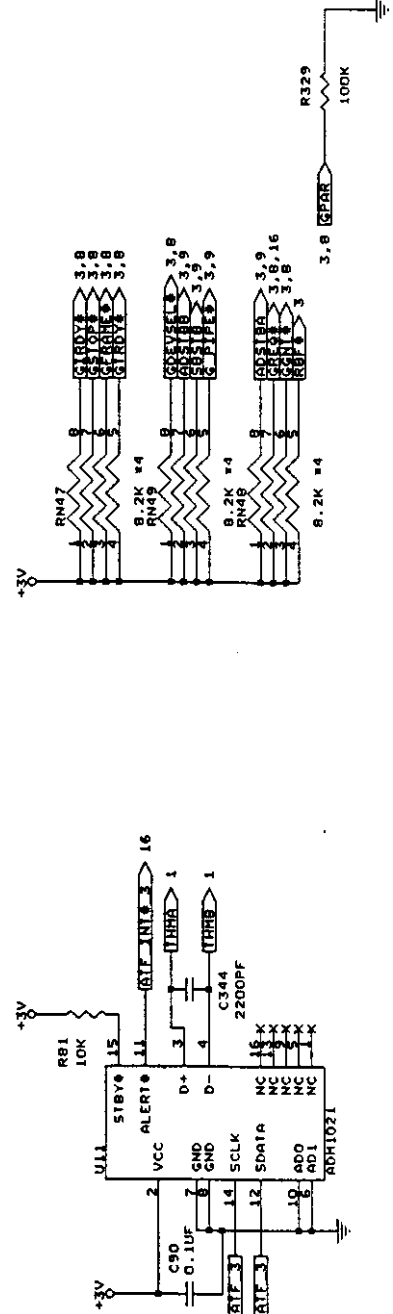
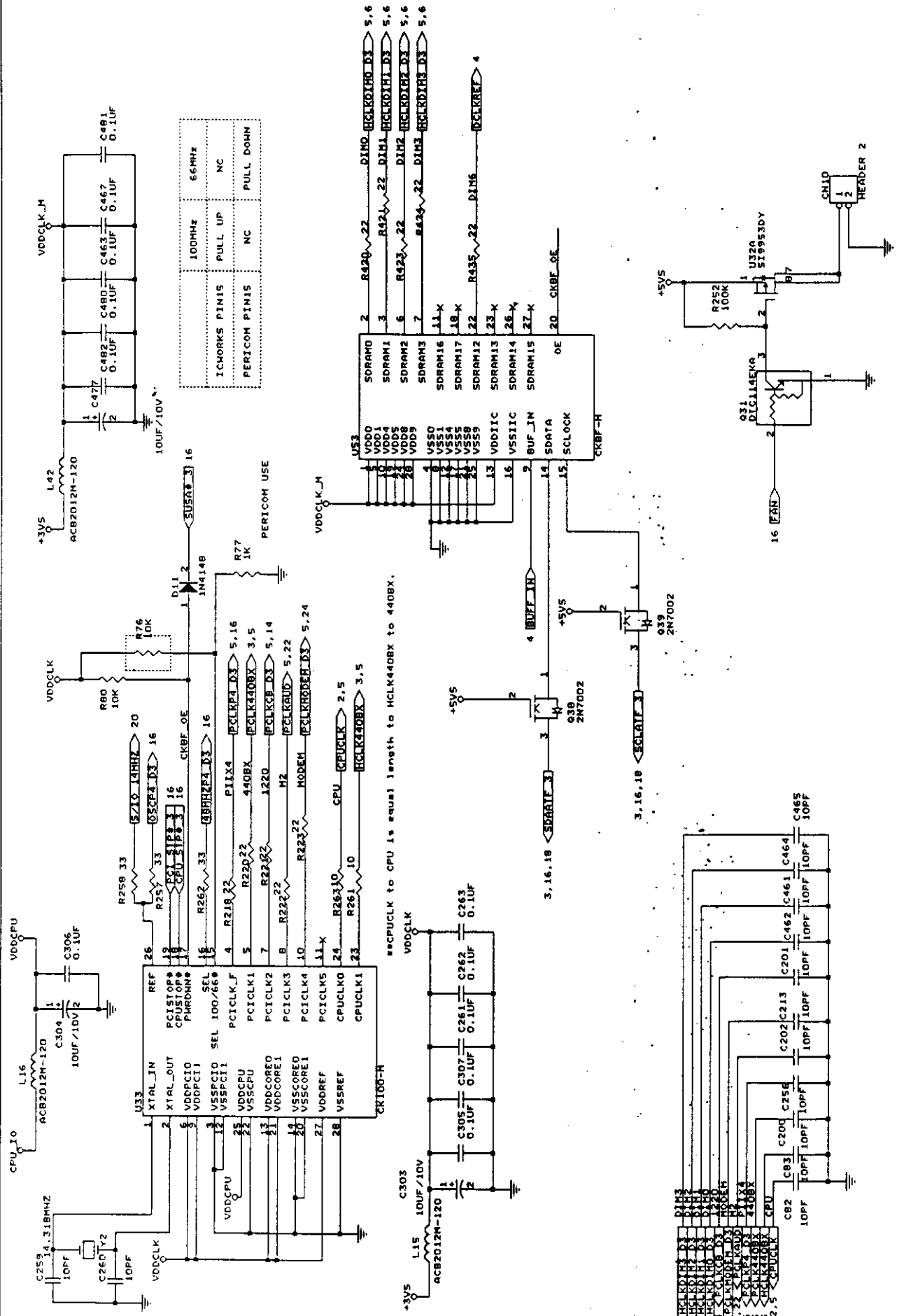
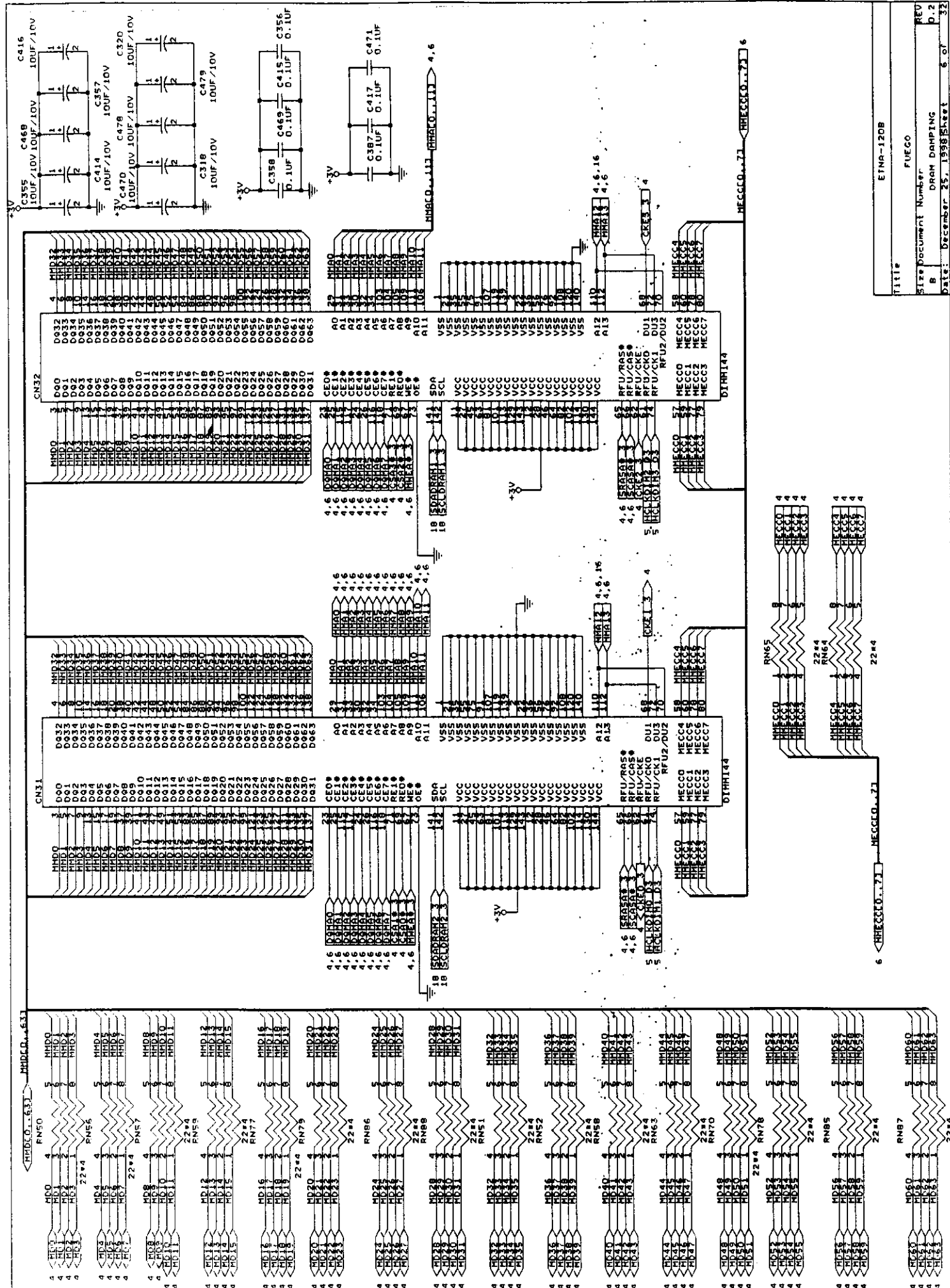


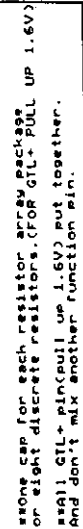
CLOCK	WHT LIMIT	INTR/L3INT	IGNNE#	220MHz
66MHz (100MHz)	L	L	H	H
1/5 333MHz (500MHz)	L	H	L	H
2/9 300MHz (450MHz)	L	L	L	H
1/4 266MHz (400MHz)	H	L	L	H
2/7 233MHz (350MHz)	H	L	L	H
1/3 200MHz (300MHz)	H	L	H	H



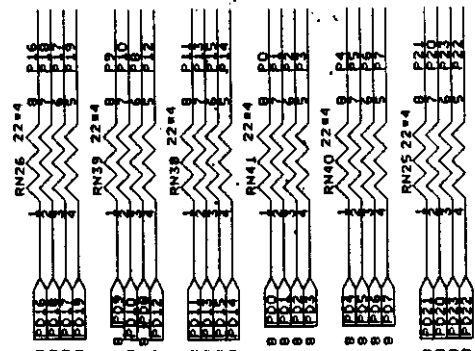
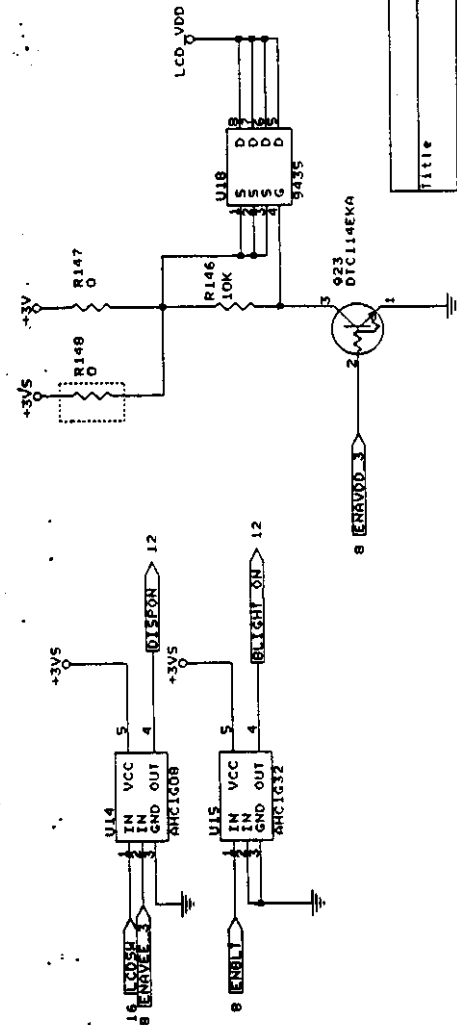
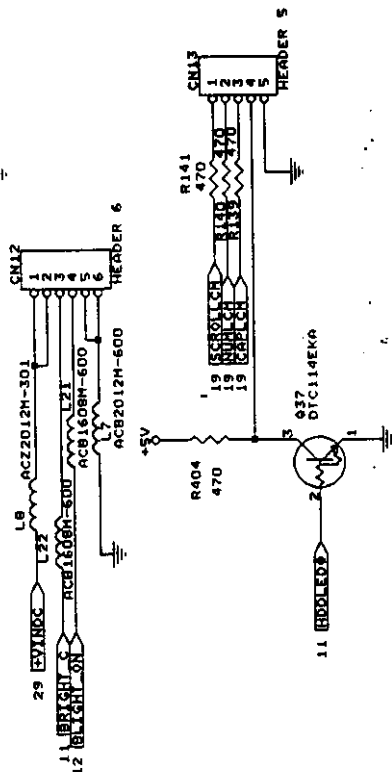
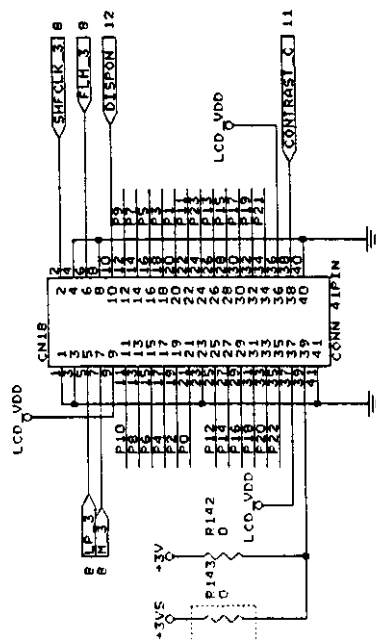


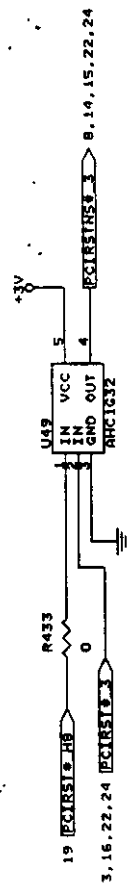
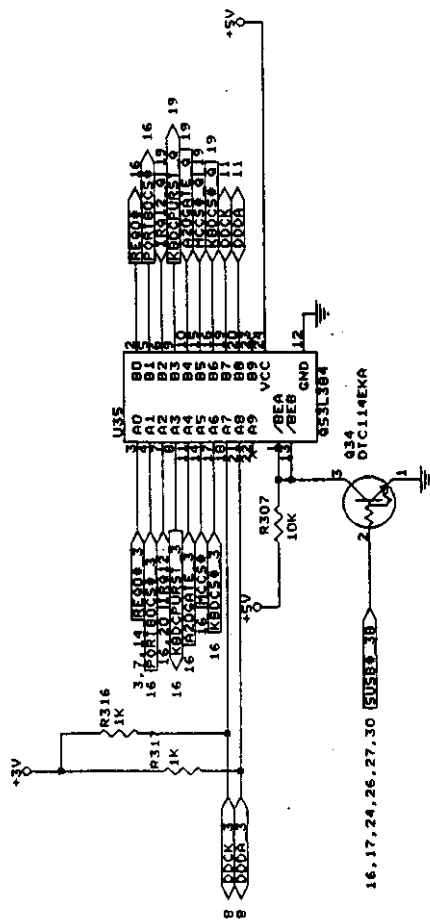




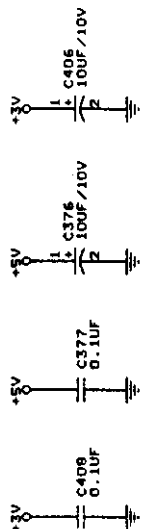
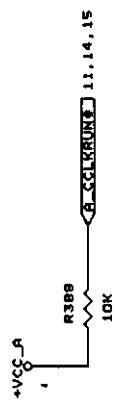


MOVITA		REV
FUEGO		0.2
Title	Size Document Number	32
	B CPU & BY PULL UP	
Date: February 2, 1953	Sheet	7 of

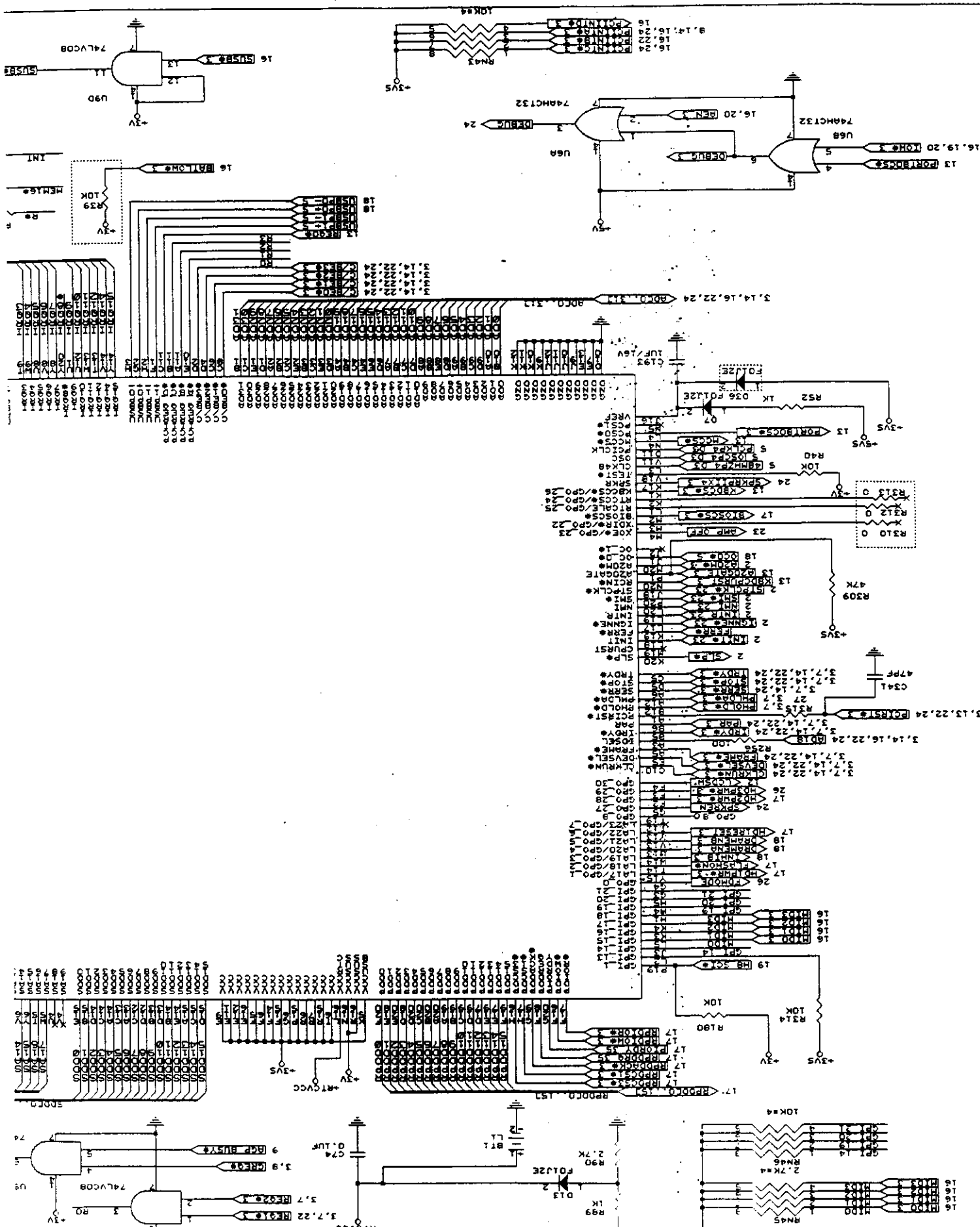


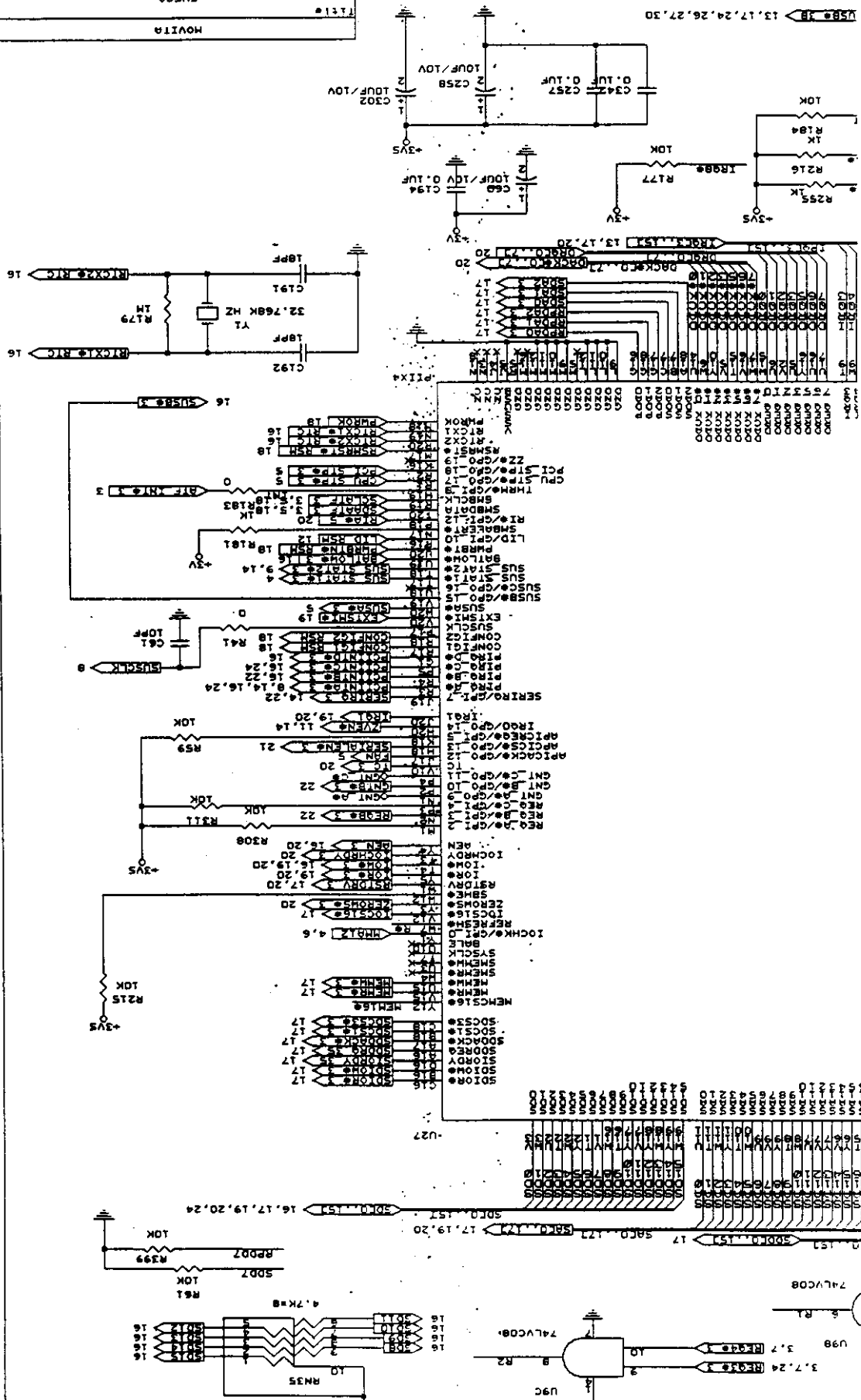


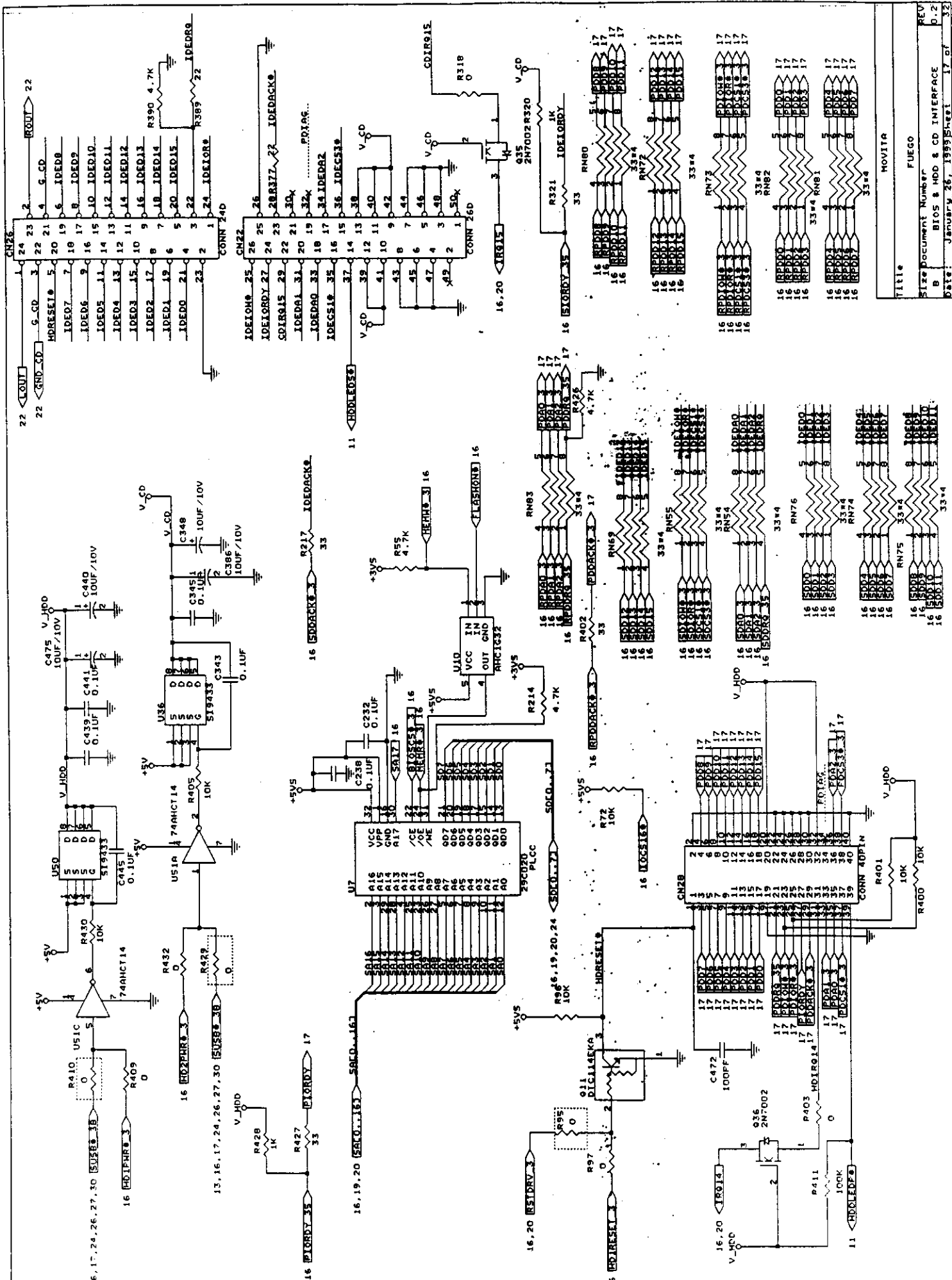
Title	MOVITA
Size	FUEGO
Document Number	
REV	0.1
Date	January 12, 1999
Sheet	13 of 32



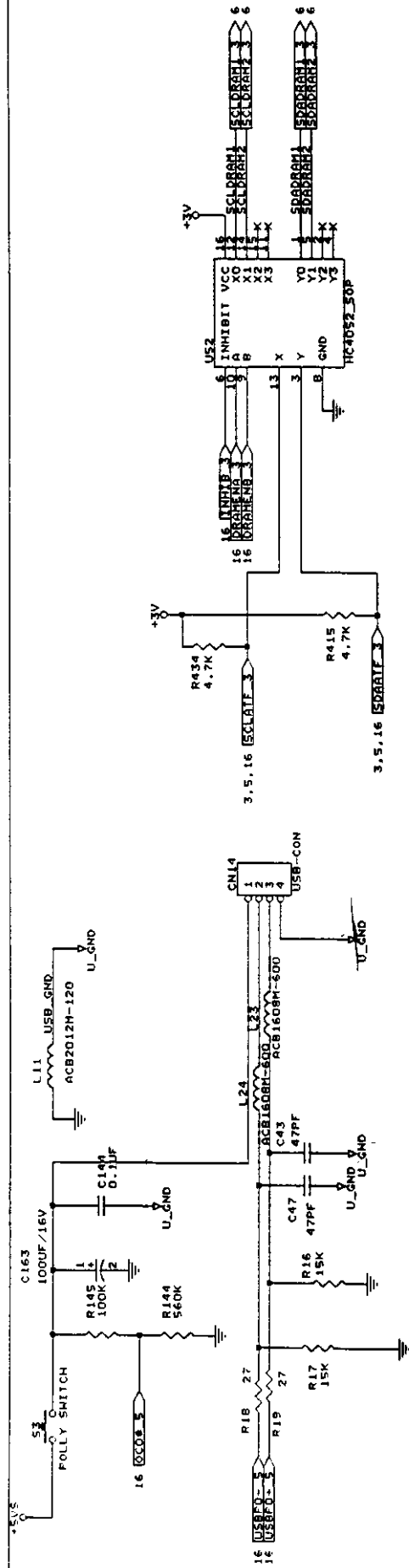
REV	0.2
Size	CARDBUS SLOTS
Document Number	8
FUECO	
MOVITA	
Title	





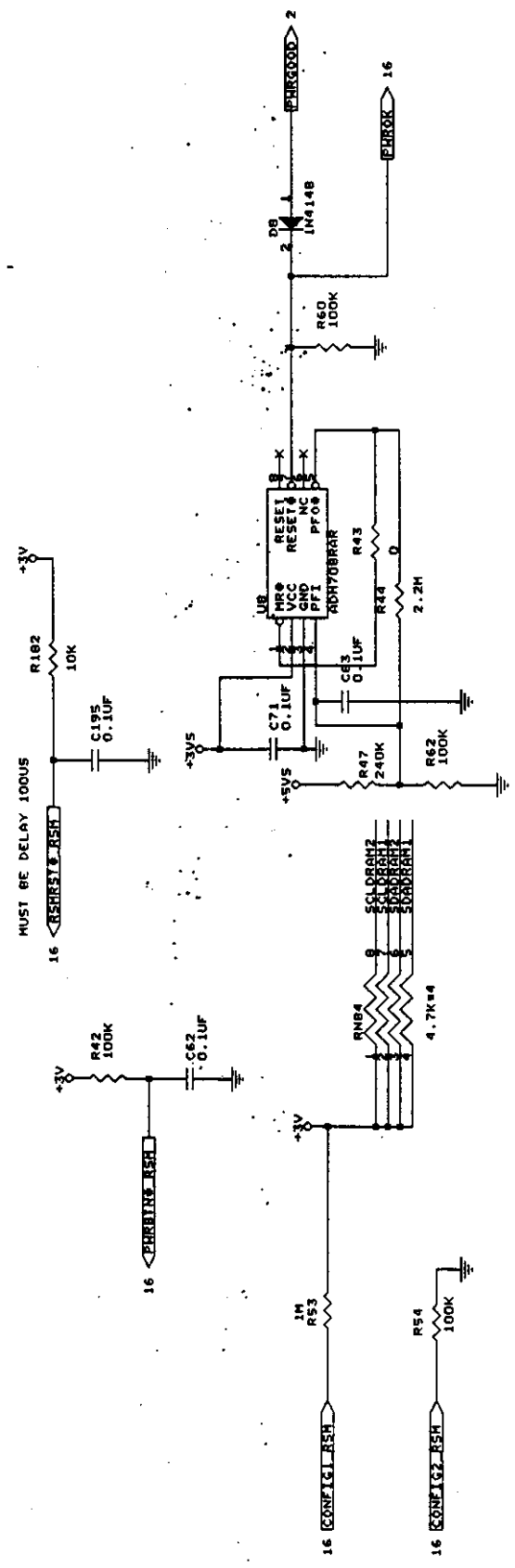


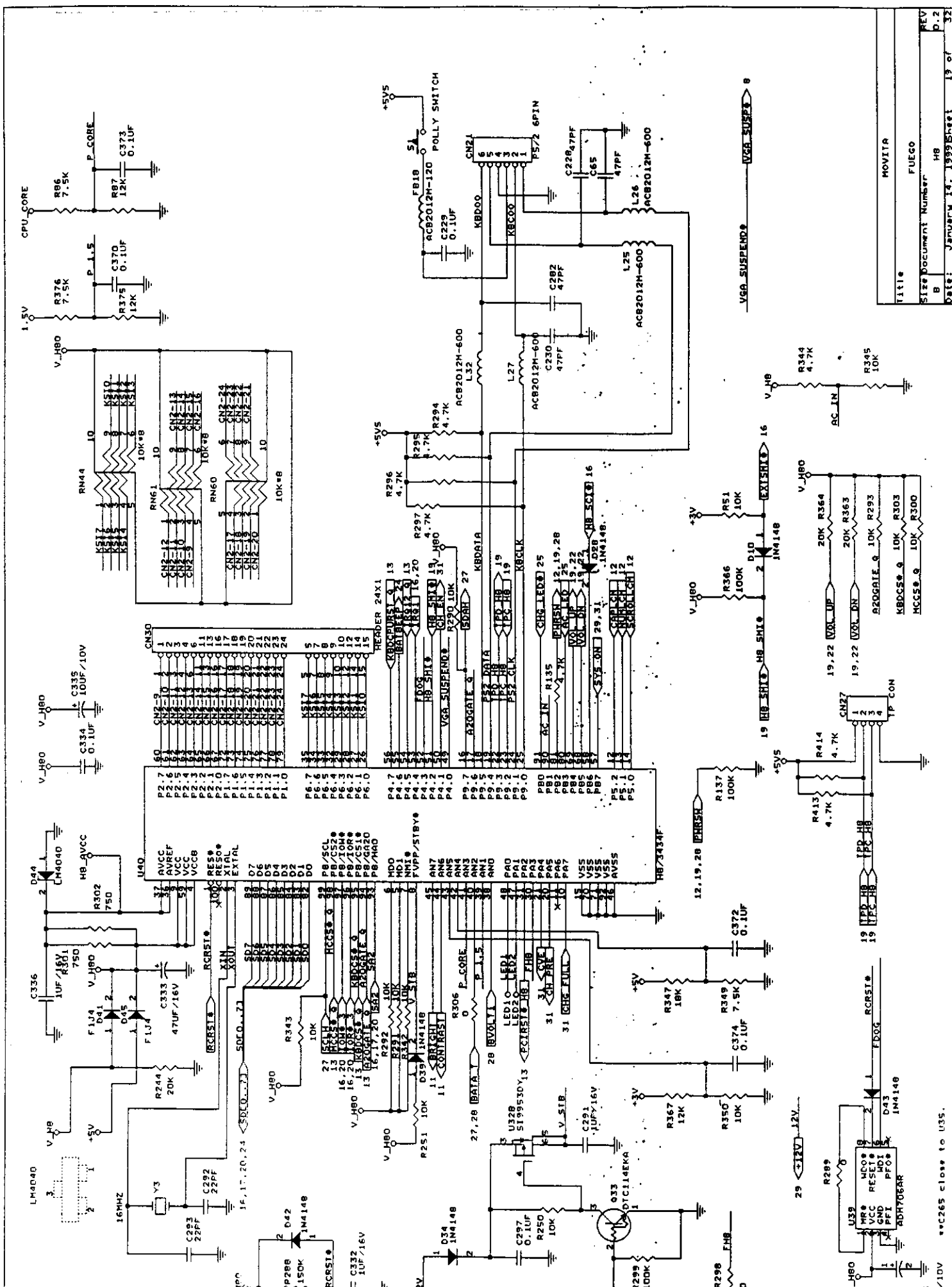
REV	0.2
Size	BIOS & HDD & CD INTERFACE
Date	January 26, 1993
Page	17 of 32

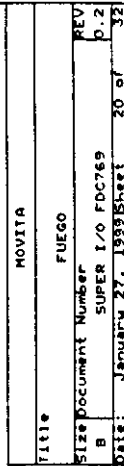


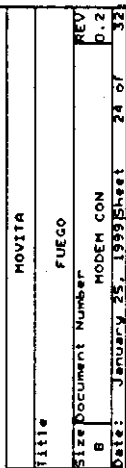
I2C ADDRESS

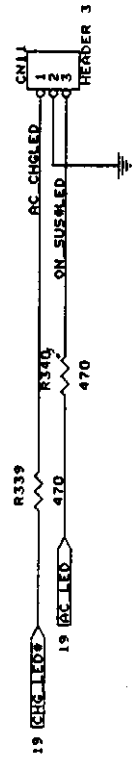
HEX	BINARY
00	0000 0000
01	0000 0001
02	0000 0010
03	0000 0011
04	0000 0100
05	0000 0101
06	0000 0110
07	0000 0111
08	0000 1000
09	0000 1001
0A	0000 1010
0B	0000 1011
0C	0000 1100
0D	0000 1101
0E	0000 1110
0F	0000 1111



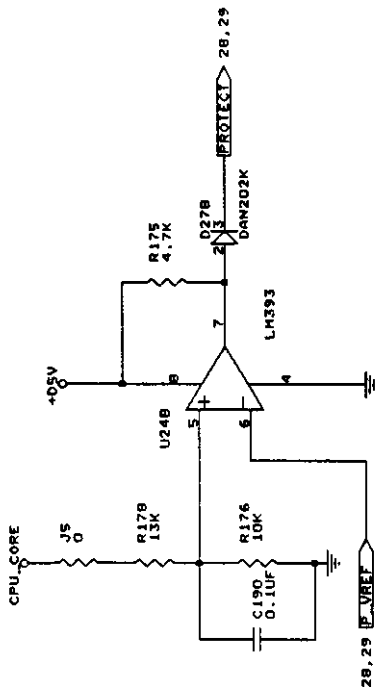




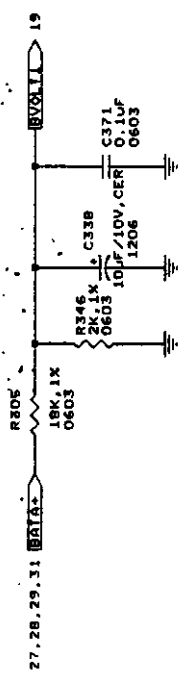
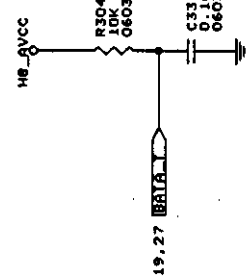
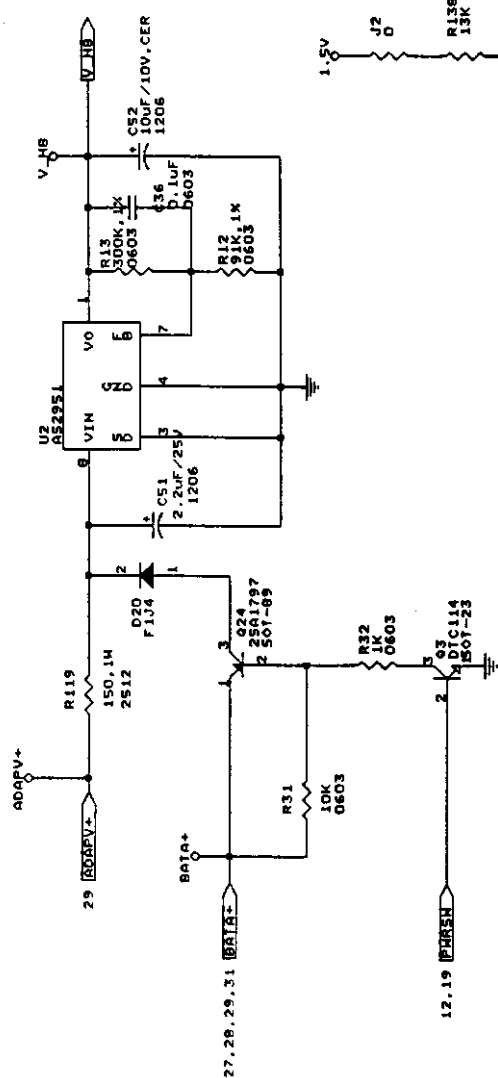
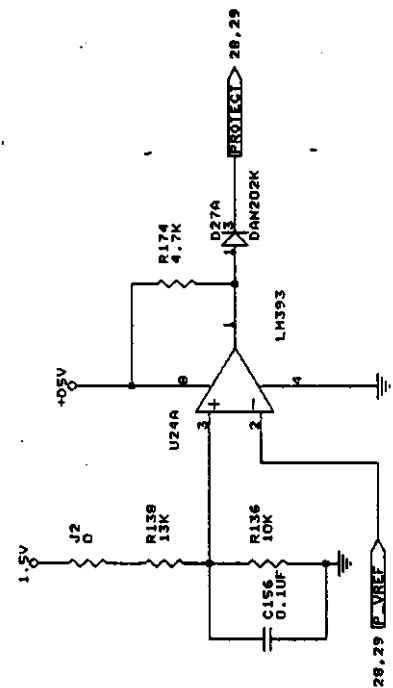


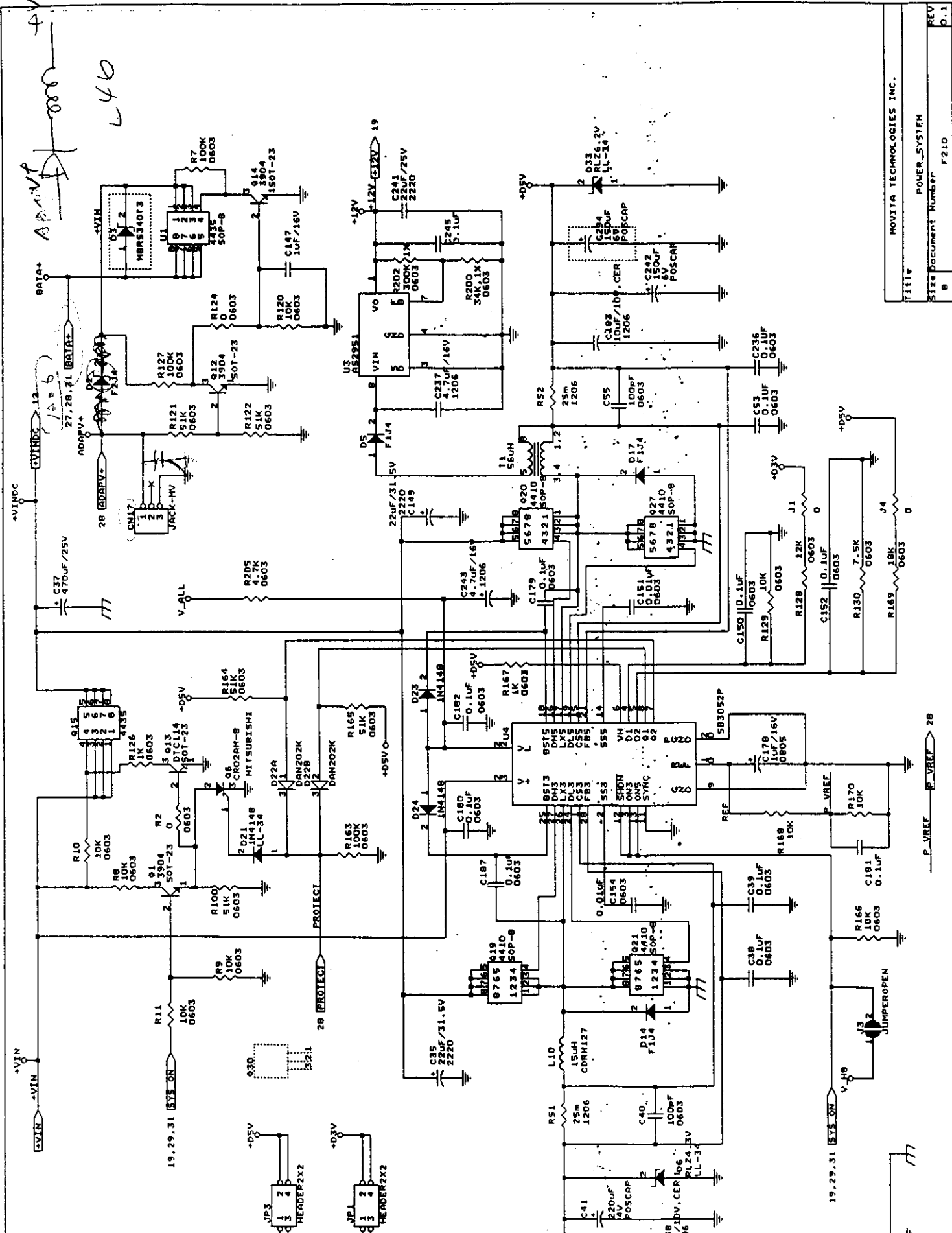


Title		MOVITA	
Size		FUEGO	
Document Number		DOCK	
REV		0.2	
Date		January 27, 1999	
Sheet		25 of 32	

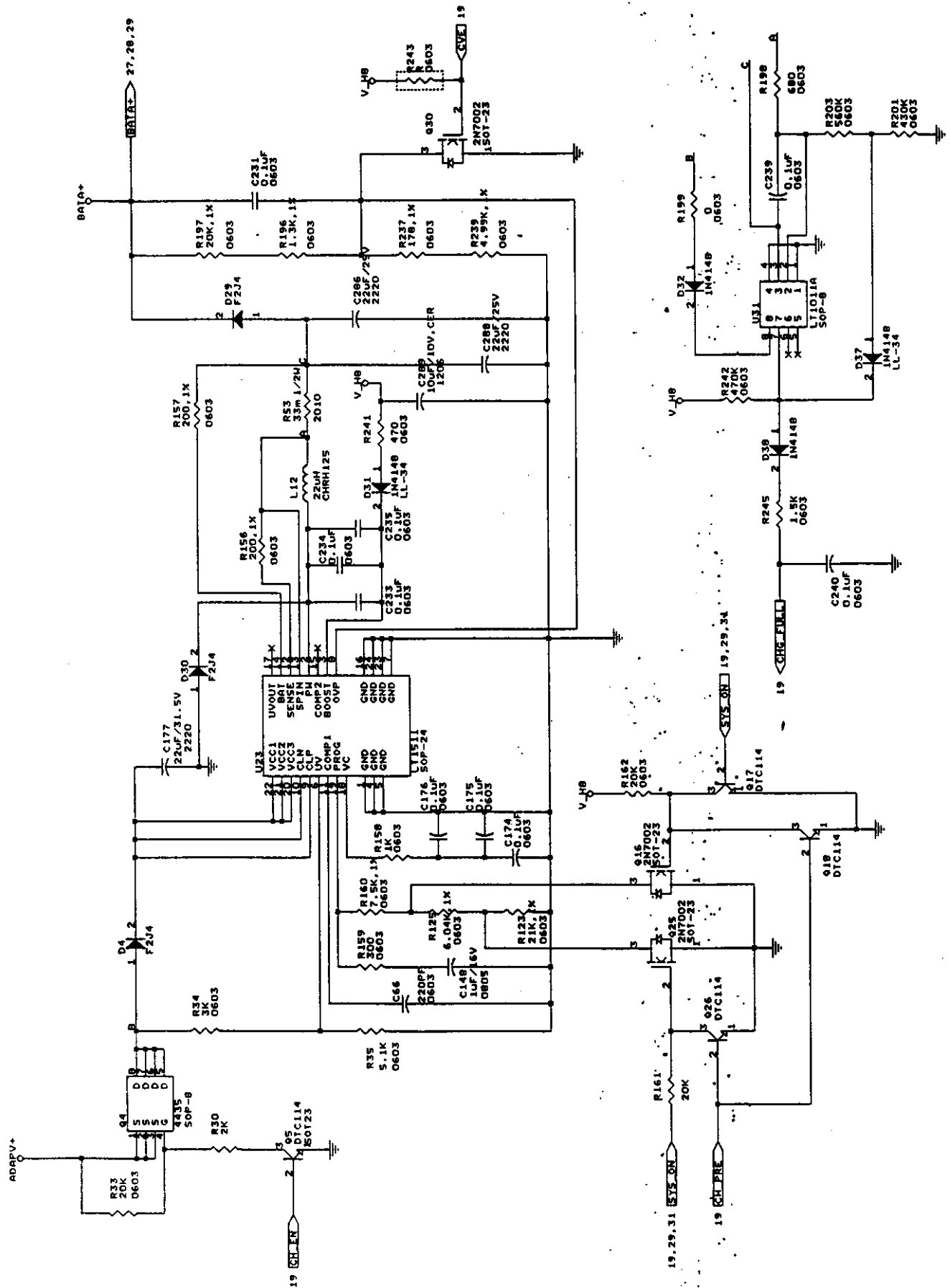


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 IF370-30.SCH
 IF370-31.SCH
 IF370-32.SCH





P_VREF B_VREF 20



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