

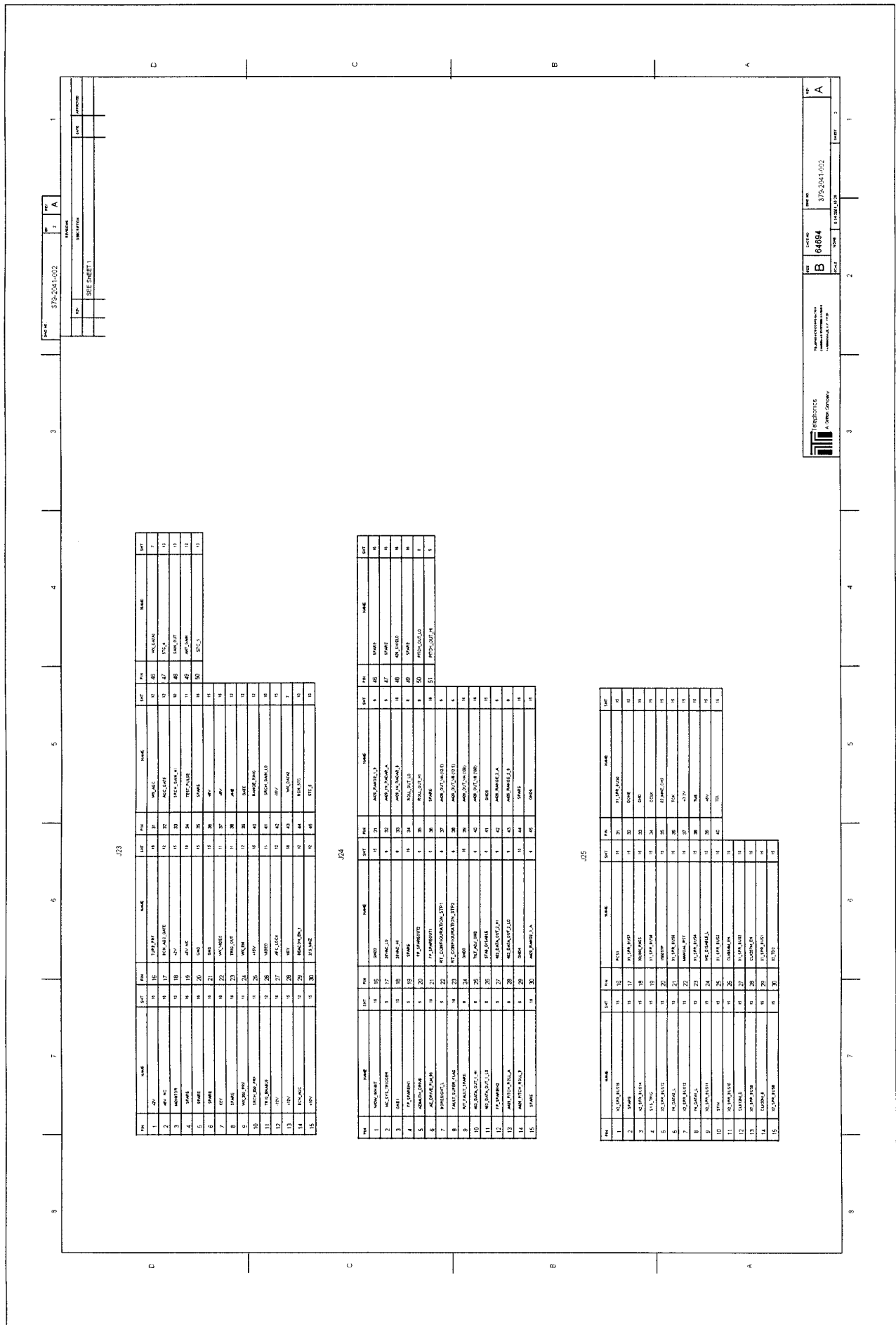


FIG 22 MODE DECODE BOARD SCHEMATIC 2 of 17