

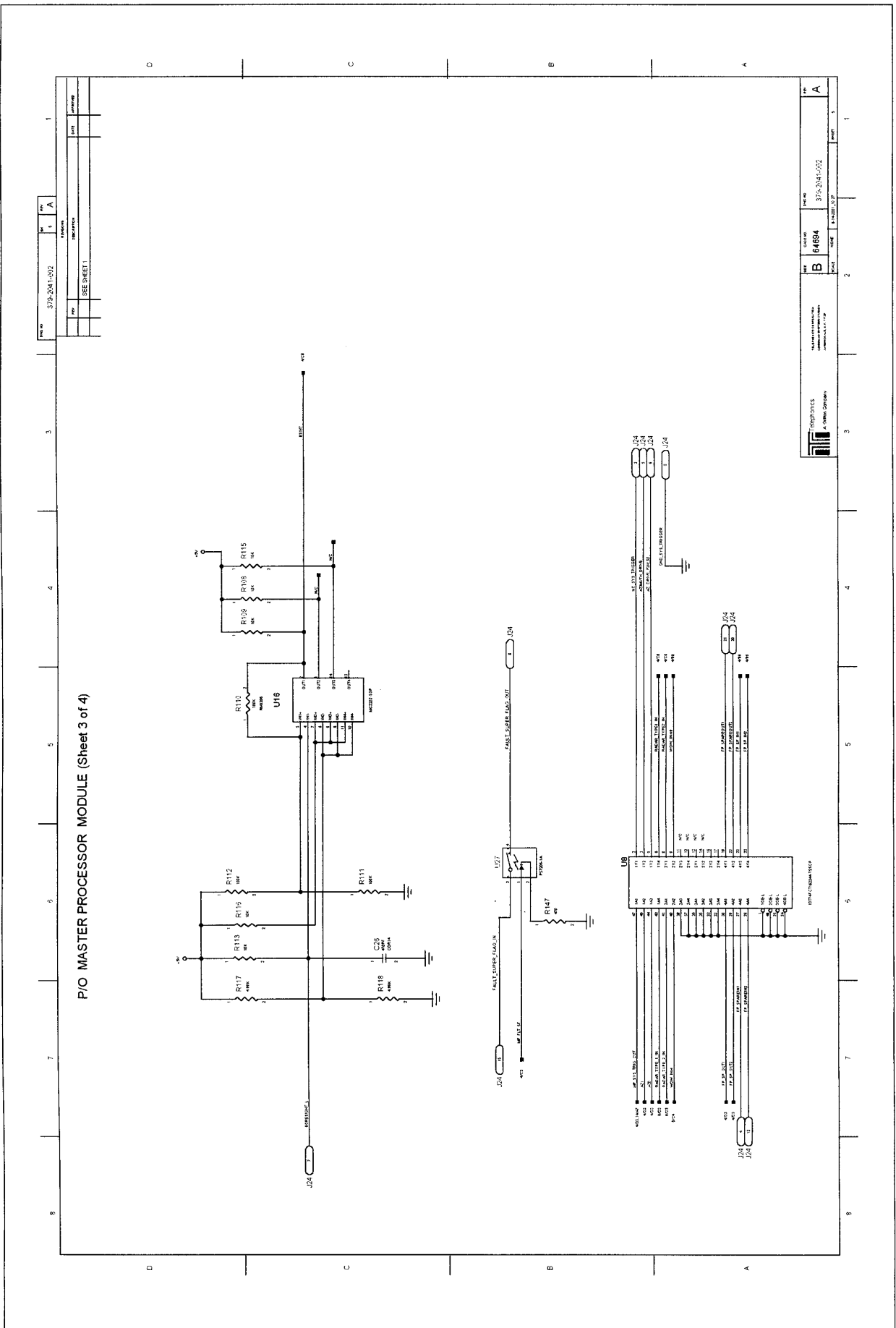


FIG 25 MODE DECODE BOARD SCHEMATIC 5 of 17