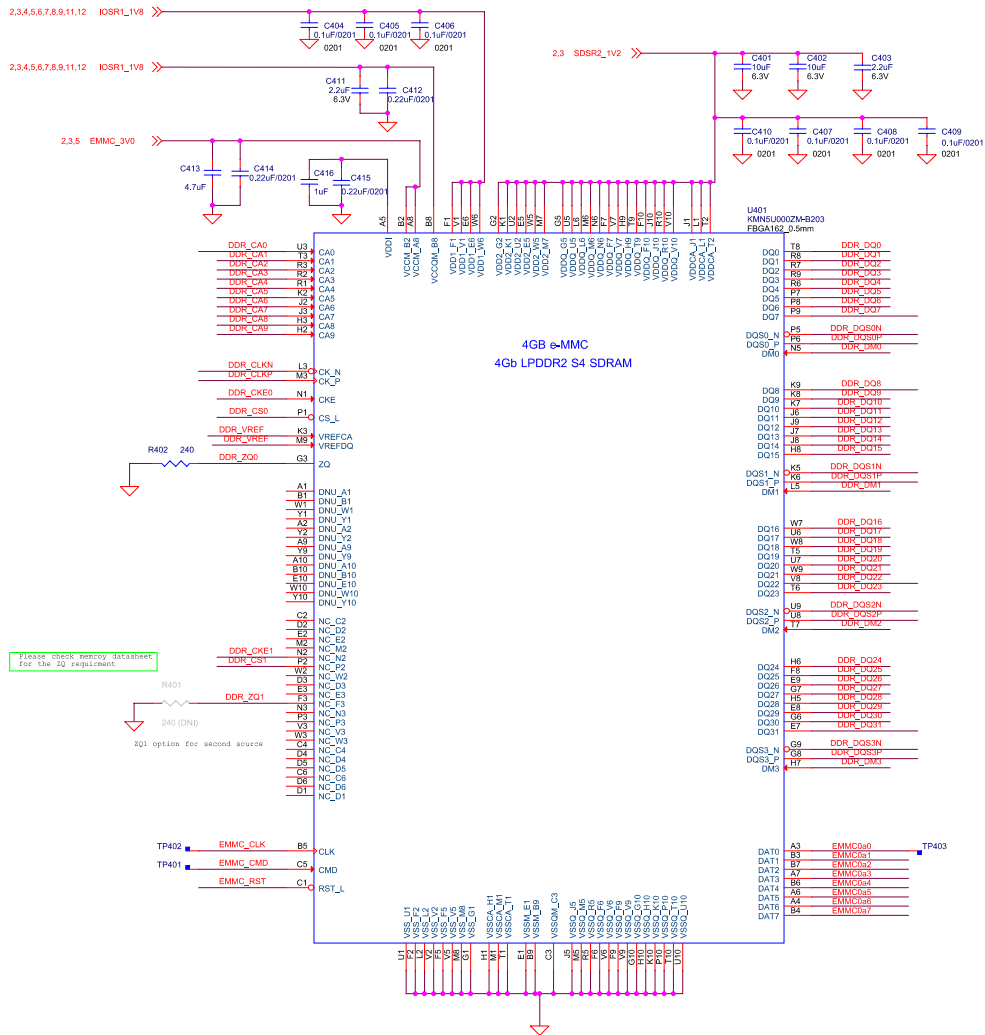
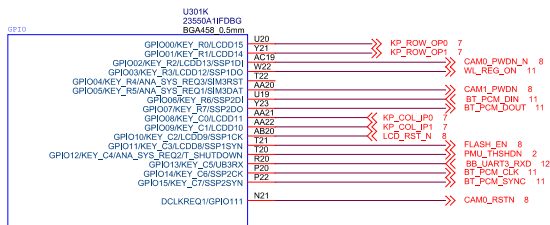
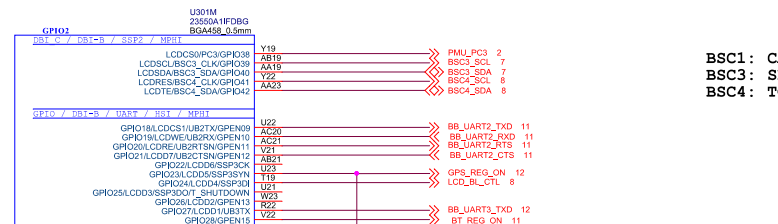




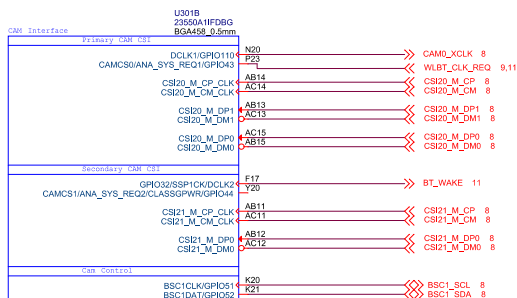
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LPT082		MEMORY		ENERGY	
DDR_DQ31	U2	DDR_DQ31	U2	DDR_DQ31	U2
DDR_DQ30	R3	DDR_DQ30	R3	DDR_DQ30	R3
DDR_DQ29	U1	DDR_DQ29	U1	DDR_DQ29	U1
DDR_DQ28	Y3	DDR_DQ28	Y3	DDR_DQ28	Y3
DDR_DQ27	Y1	DDR_DQ27	Y1	DDR_DQ27	Y1
DDR_DQ26	Y4	DDR_DQ26	Y4	DDR_DQ26	Y4
DDR_DQ25	R4	DDR_DQ25	R4	DDR_DQ25	R4
DDR_DQ24	R4	DDR_DQ24	R4	DDR_DQ24	R4
DDR_DQ23	H1	DDR_DQ23	H1	DDR_DQ23	H1
DDR_DQ22	G2	DDR_DQ22	G2	DDR_DQ22	G2
DDR_DQ21	F1	DDR_DQ21	F1	DDR_DQ21	F1
DDR_DQ20	G1	DDR_DQ20	G1	DDR_DQ20	G1
DDR_DQ19	H3	DDR_DQ19	H3	DDR_DQ19	H3
DDR_DQ18	F3	DDR_DQ18	F3	DDR_DQ18	F3
DDR_DQ17	G3	DDR_DQ17	G3	DDR_DQ17	G3
DDR_DQ16	F2	DDR_DQ16	F2	DDR_DQ16	F2
DDR_DQ15	P1	DDR_DQ15	P1	DDR_DQ15	P1
DDR_DQ14	L4	DDR_DQ14	L4	DDR_DQ14	L4
DDR_DQ13	M1	DDR_DQ13	M1	DDR_DQ13	M1
DDR_DQ12	N3	DDR_DQ12	N3	DDR_DQ12	N3
DDR_DQ11	P2	DDR_DQ11	P2	DDR_DQ11	P2
DDR_DQ10	M2	DDR_DQ10	M2	DDR_DQ10	M2
DDR_DQ09	N4	DDR_DQ09	N4	DDR_DQ09	N4
DDR_DQ08	M4	DDR_DQ08	M4	DDR_DQ08	M4
DDR_DQ07	J4	DDR_DQ07	J4	DDR_DQ07	J4
DDR_DQ06	K2	DDR_DQ06	K2	DDR_DQ06	K2
DDR_DQ05	K3	DDR_DQ05	K3	DDR_DQ05	K3
DDR_DQ04	J3	DDR_DQ04	J3	DDR_DQ04	J3
DDR_DQ03	J6	DDR_DQ03	J6	DDR_DQ03	J6
DDR_DQ02	L3	DDR_DQ02	L3	DDR_DQ02	L3
DDR_DQ01	H4	DDR_DQ01	H4	DDR_DQ01	H4
DDR_DQ00	J2	DDR_DQ00	J2	DDR_DQ00	J2
DDR_VREF	N5	DDR_VREF	N5	DDR_VREF	N5
DDR_ZQ0	K7	DDR_ZQ0	K7	DDR_ZQ0	K7
ENERGY		ENERGY		ENERGY	
EMMC0A7	V3	EMMC0A7	V3	EMMC0A7	V3
EMMC0A6	U7	EMMC0A6	U7	EMMC0A6	U7
EMMC0A5	V2	EMMC0A5	V2	EMMC0A5	V2
EMMC0A4	Y5	EMMC0A4	Y5	EMMC0A4	Y5
EMMC0A3	V1	EMMC0A3	V1	EMMC0A3	V1
EMMC0A2	V4	EMMC0A2	V4	EMMC0A2	V4
EMMC0A1	U4	EMMC0A1	U4	EMMC0A1	U4
EMMC0A0	W2	EMMC0A0	W2	EMMC0A0	W2
EMMC0A7	V3	EMMC0A7	V3	EMMC0A7	V3
EMMC0A6	U7	EMMC0A6	U7	EMMC0A6	U7
EMMC0A5	V2	EMMC0A5	V2	EMMC0A5	V2
EMMC0A4	Y5	EMMC0A4	Y5	EMMC0A4	Y5
EMMC0A3	V1	EMMC0A3	V1	EMMC0A3	V1
EMMC0A2	V4	EMMC0A2	V4	EMMC0A2	V4
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EMMC0A0	W2	EMMC0A0	W2	EMMC0A0	W2



GPIO



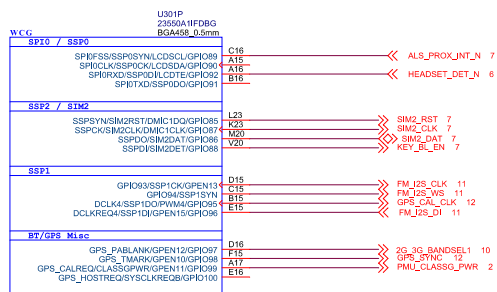
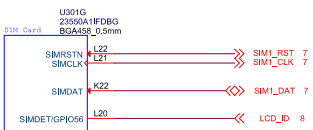
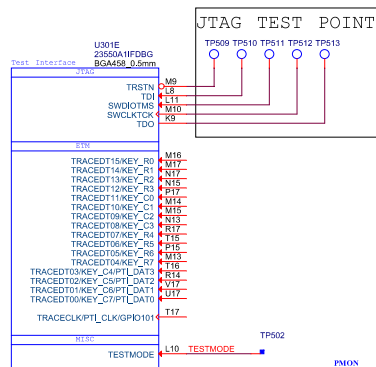
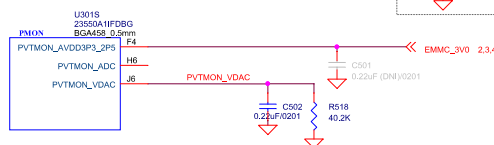
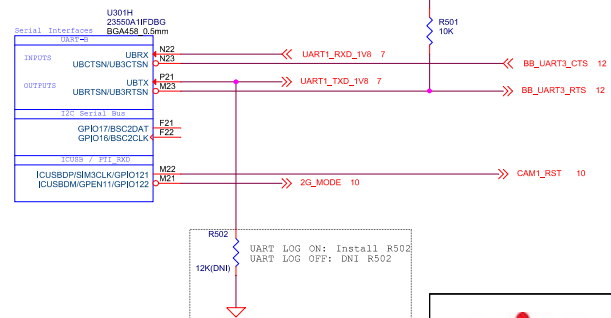
GPIO / BSC3-4



CAMERA

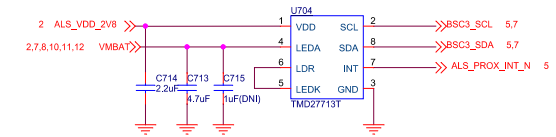


SDIO



				Broadcom Corporation 5300 California Ave Irvine, CA 92617 USA			
				Java A25 : BB GPIOs			
Size		DWG NO		Rev			
Custom		JAVA_A25_EDN010		01			
Thursday, July 03, 2014		Scale		Sheet		5 of 13	

ALS & PS Sensor



TAOS TMD 27713

ADDRESS 0x39

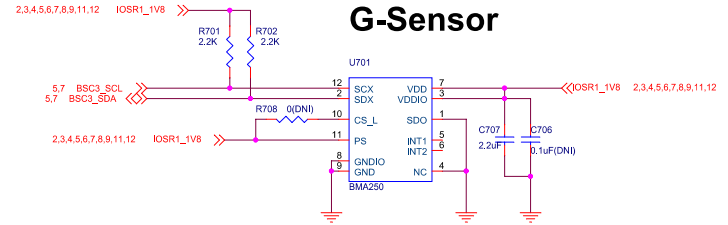
I2C Vbus = 1.8V interface

TAOS TMD 27713

ADDRESS 0x39

I2C Vbus = 1.8V interface

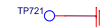
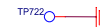
G-Sensor



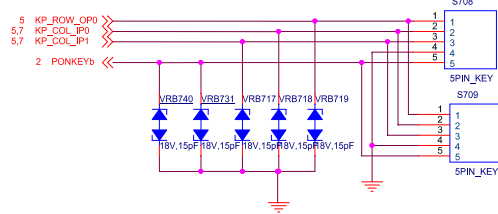
I2C ADDR = 0x18 BMA250

I2C ADDR = 0x0E KXTJ2-1009

FPC

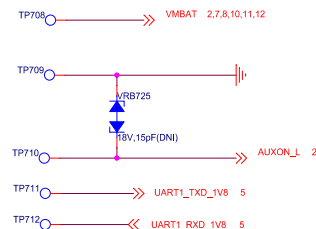


Volume Up Down,ponkey

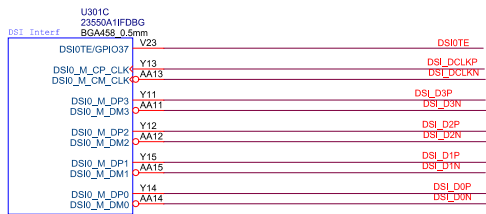


S1 Vol + : ROW_OP0 and COL_IP1
S2 Vol - : ROW_OP0 and COL_IP0

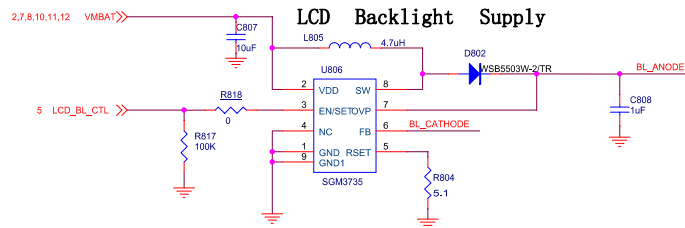
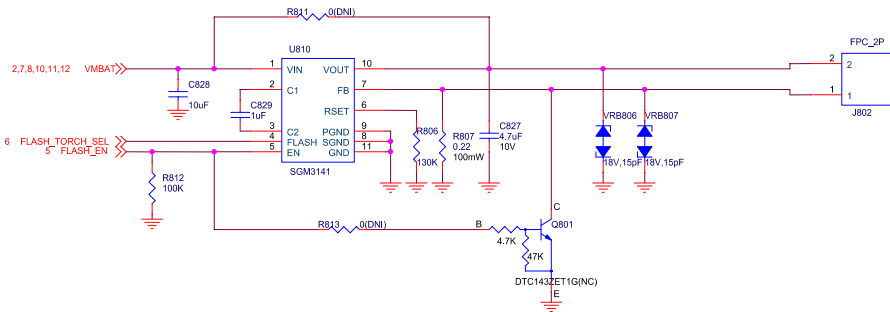
FACTORY TEST POINTS



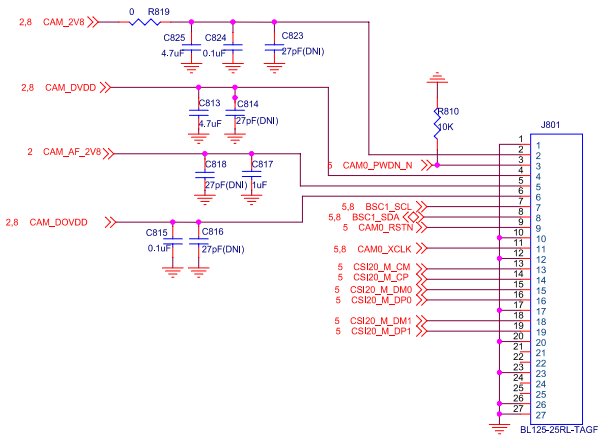
	Broadcom Corporation 5300 California Ave Irvine, CA 92617 USA		
	Java A25 : SIM, SD,USB,SENSOR,FPC		
	Size	DWG NO	Rev
	Custom	JAVA_A25_EDN010	01
Wednesday, February 12, 2014	Scale		Sheet 7 of 13



DSI SIGNALS TO BE ROUTED AS 100 OHMS DIFFERENTIAL PAIR



MAIN CAM

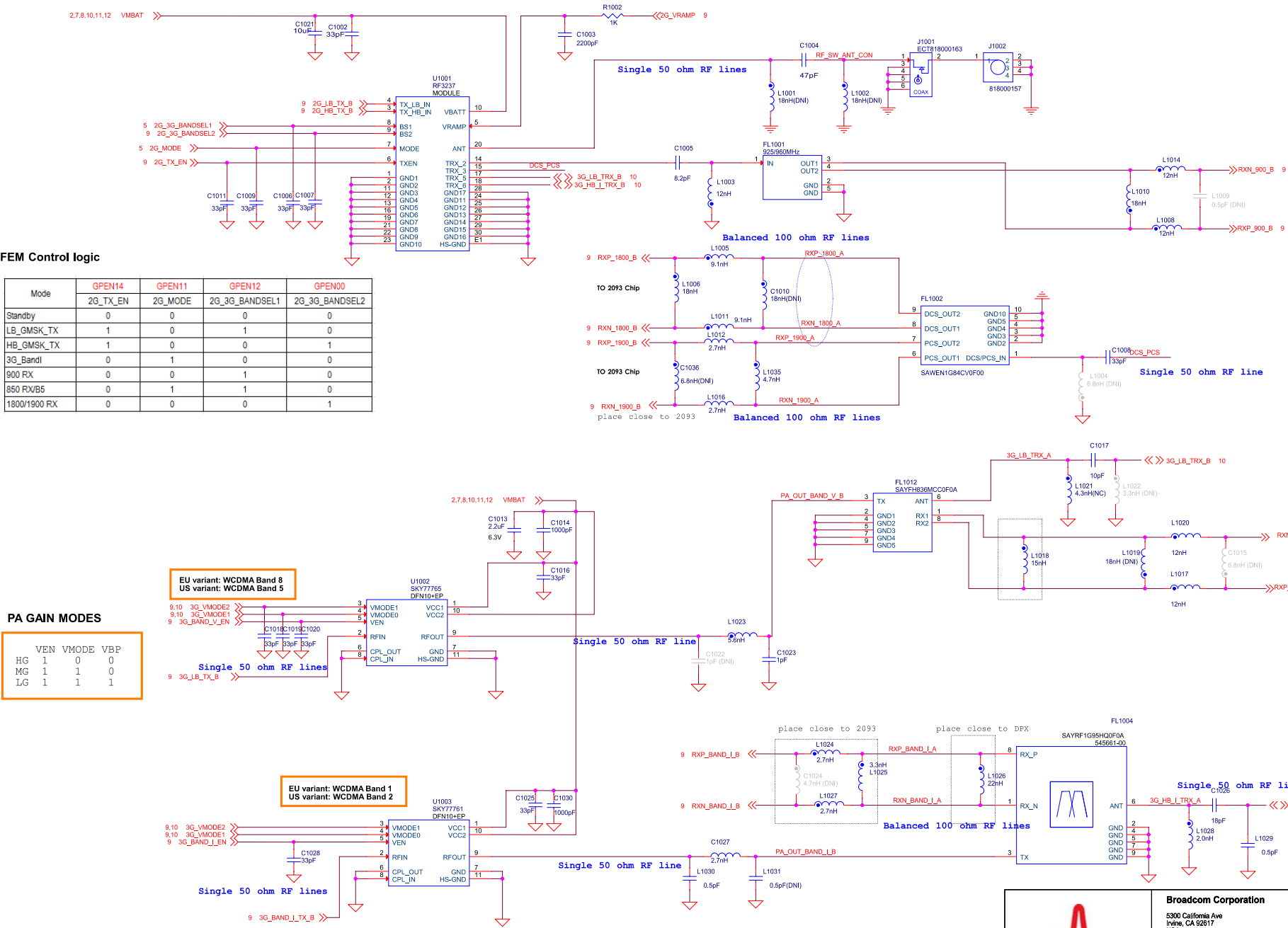


FEM Control logic

Mode	GPEN14	GPEN11	GPEN12	GPEN00
	2G_TX_EN	2G_MODE	2G_3G_BANDSEL1	2G_3G_BANDSEL2
Standby	0	0	0	0
LB_GMSK_TX	1	0	1	0
HB_GMSK_TX	1	0	0	1
3G_Band1	0	1	0	0
900 RX	0	0	1	0
850 RX/B5	0	1	1	0
1800/1900 RX	0	0	0	1

PA GAIN MODES

	VEN	VMODE	VBP
HG	1	0	0
MG	1	1	0
LG	1	1	1





Broadcom Corporation
5300 California Ave
Irvine, CA 92617
USA

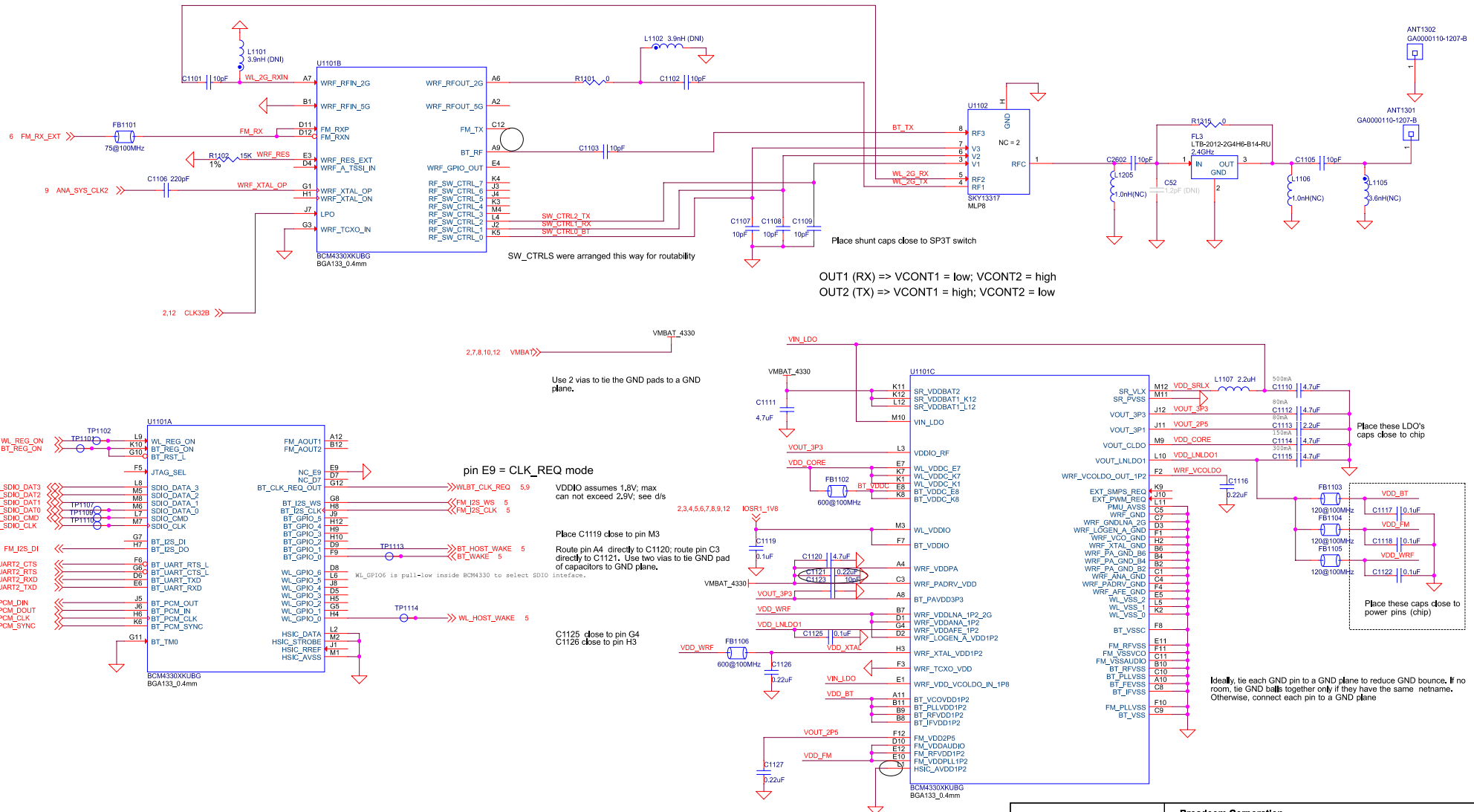
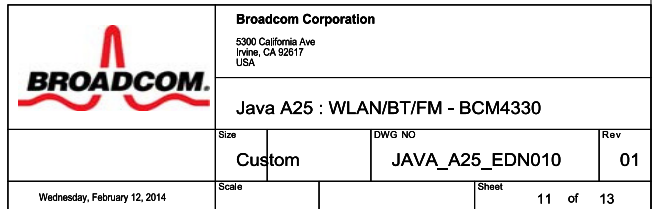
Java A25 : RF - 2/3G PA,FEM, ANT

Size	DWG NO	Rev
Custom	JAVA_A25_EDN010	01

Scale	Sheet
Wednesday, February 12, 2014	10 of 13

WLAN/BT/FM

Dual-Band, Single Antenna



Remove any GND planes (L2 - L whatever) below OSC. place OSC further from chip to reduce thermal noise couplings

