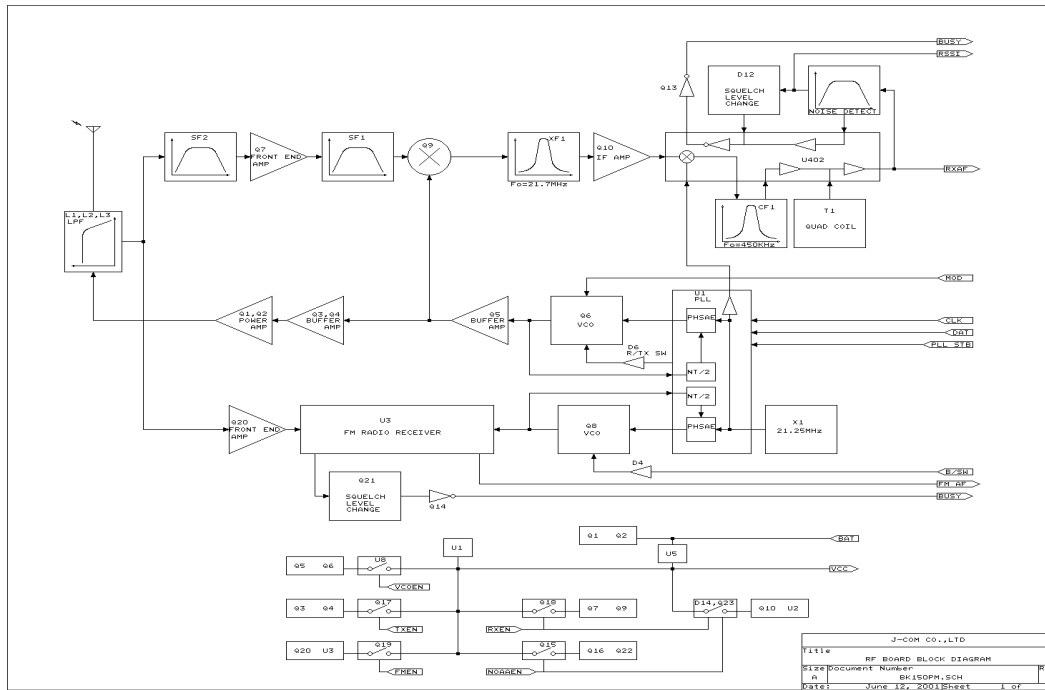


6.BLOCK DIAGRAM

6.1 RF Diagram



6.2 Logic Diagram

