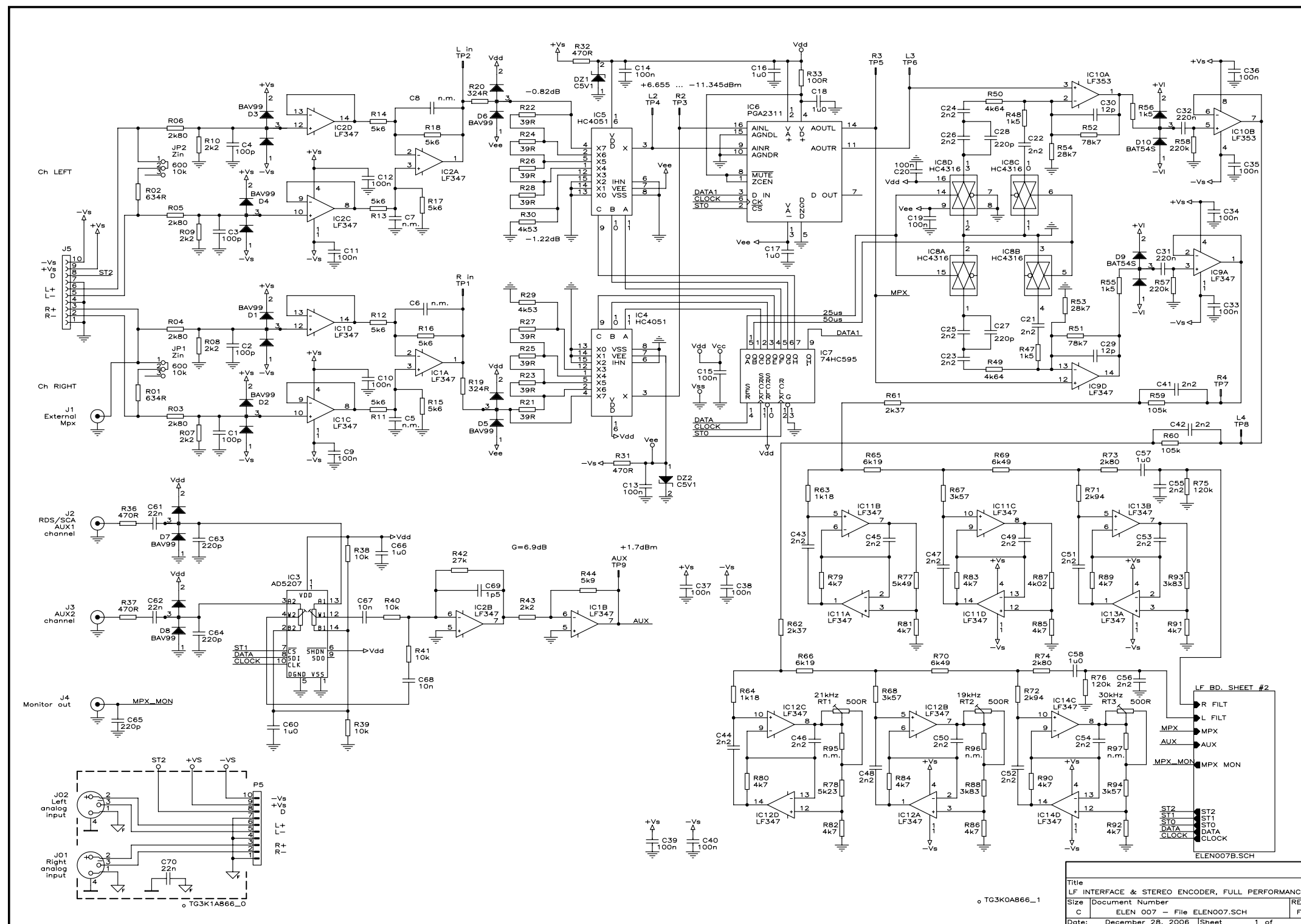
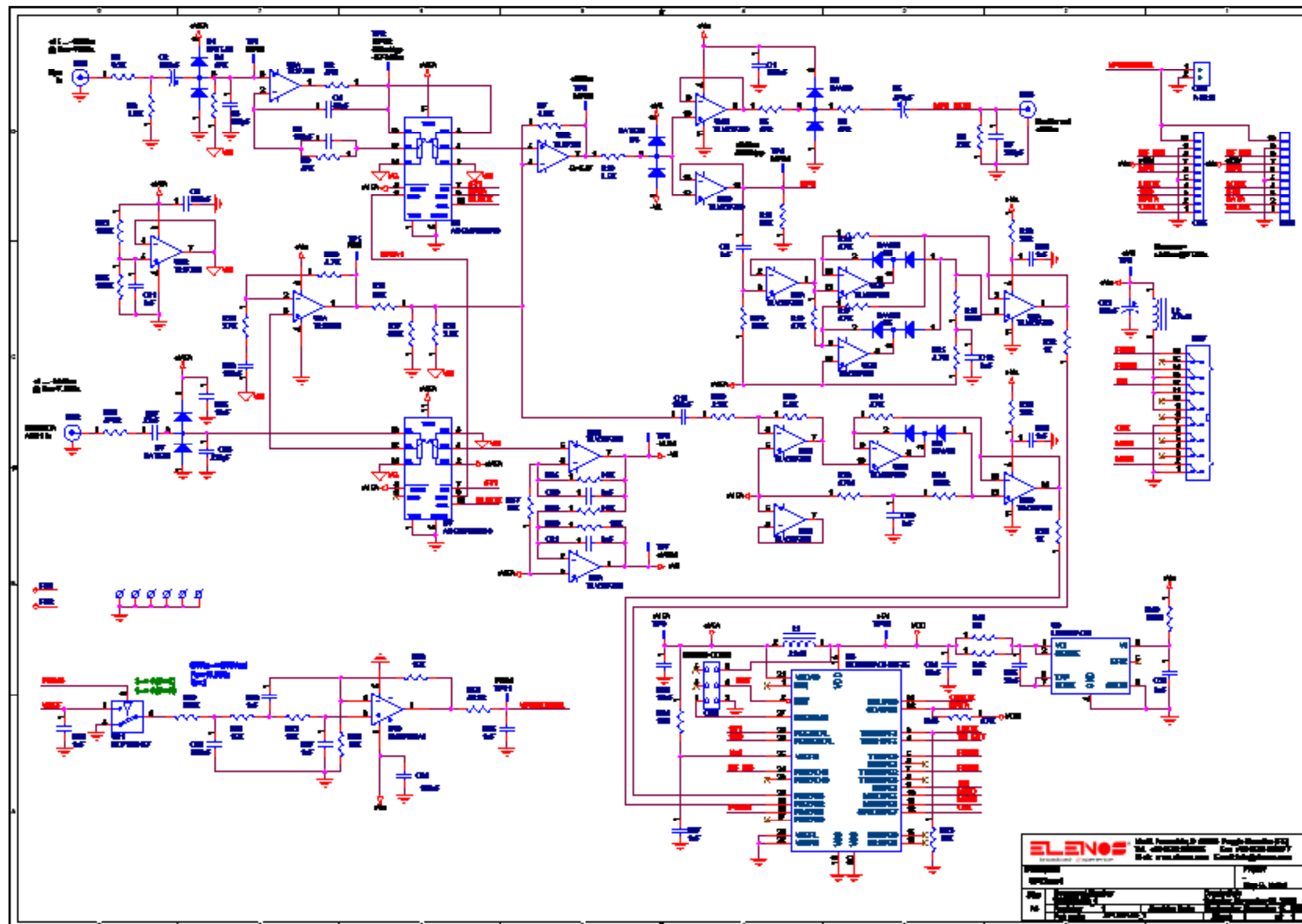


Schematics

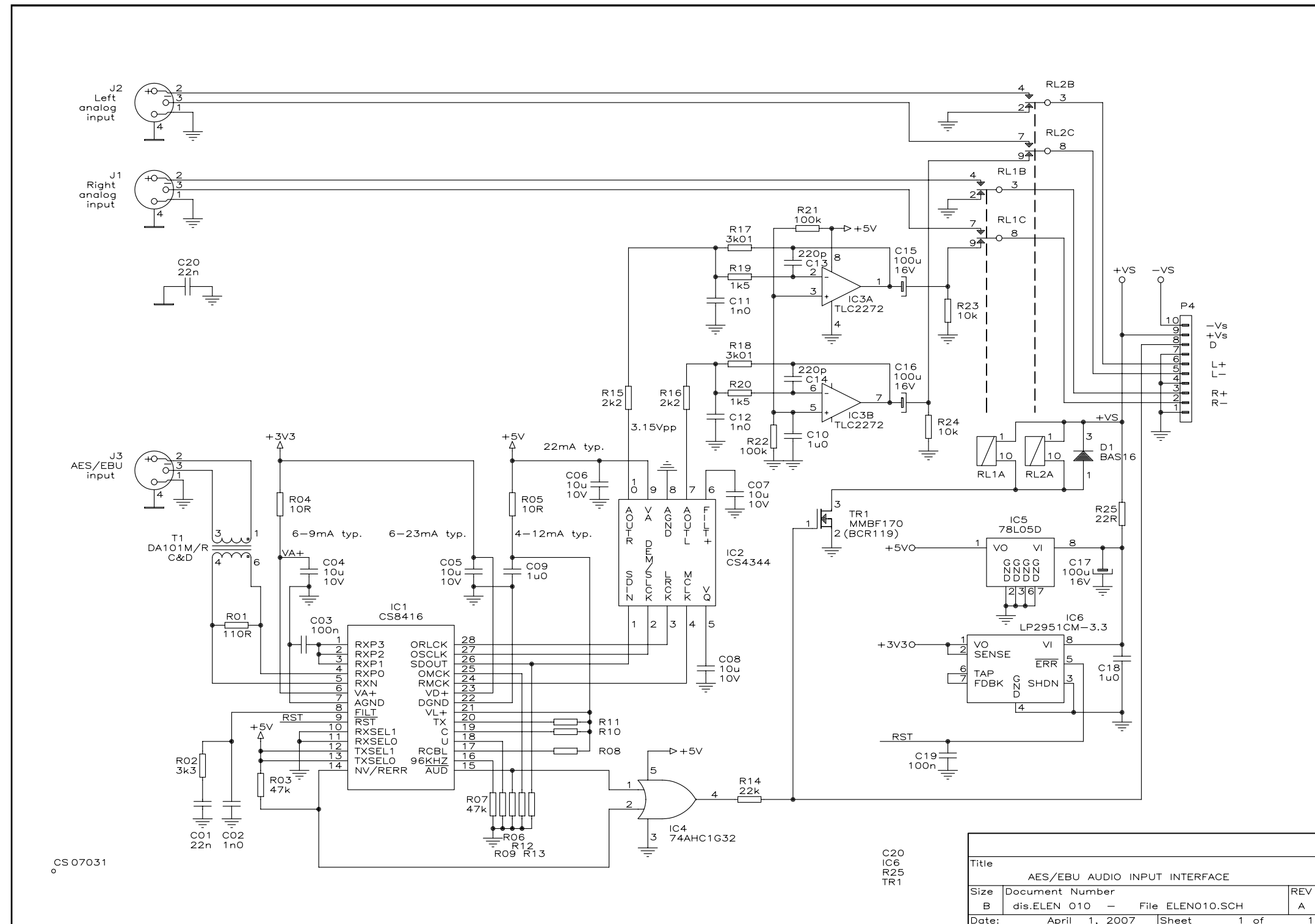
STEREO CODER with MPX and AUX inputs (TG3K0B066-1)



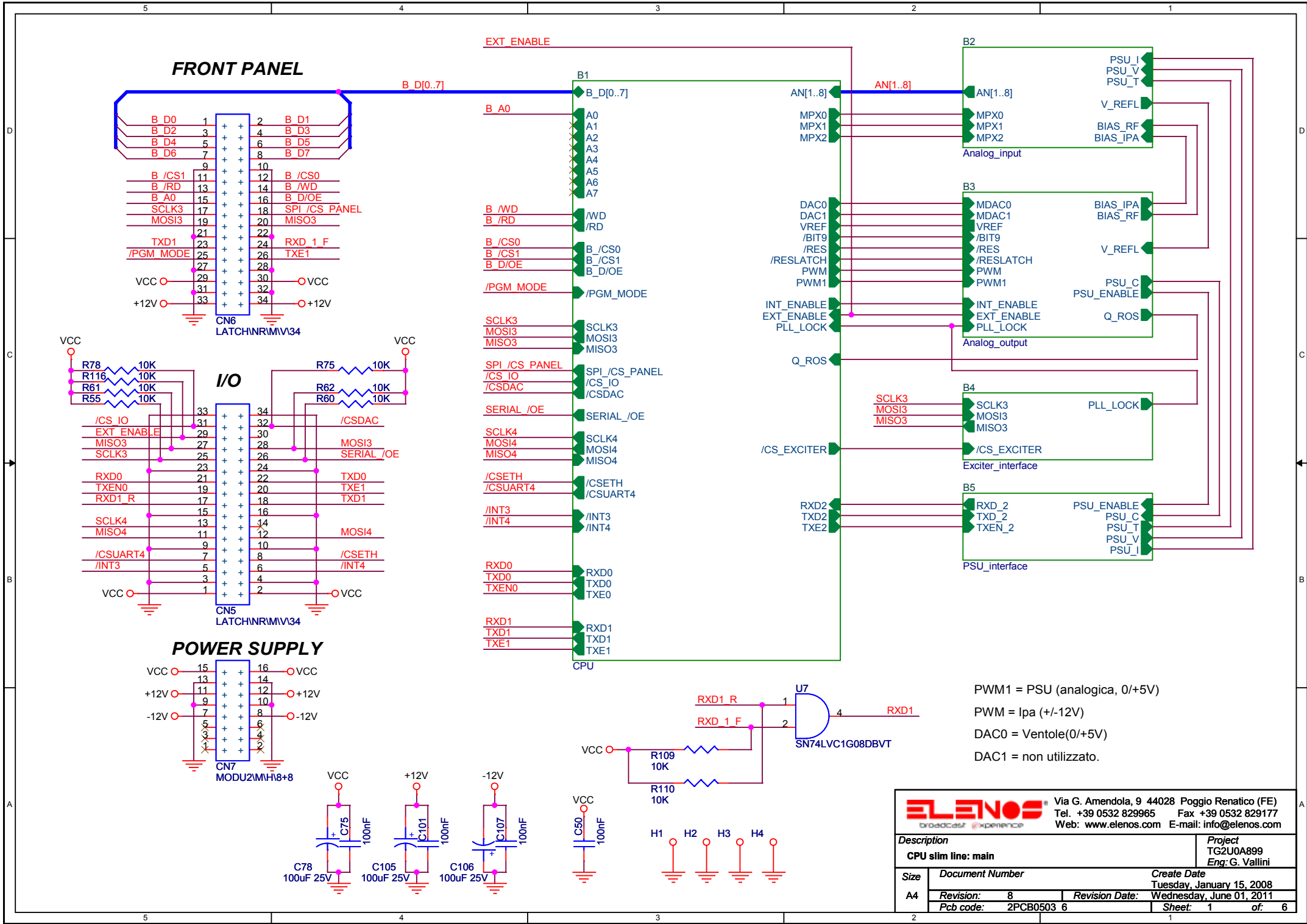
MPX CODER with AUX inputs (TG2UGA890)

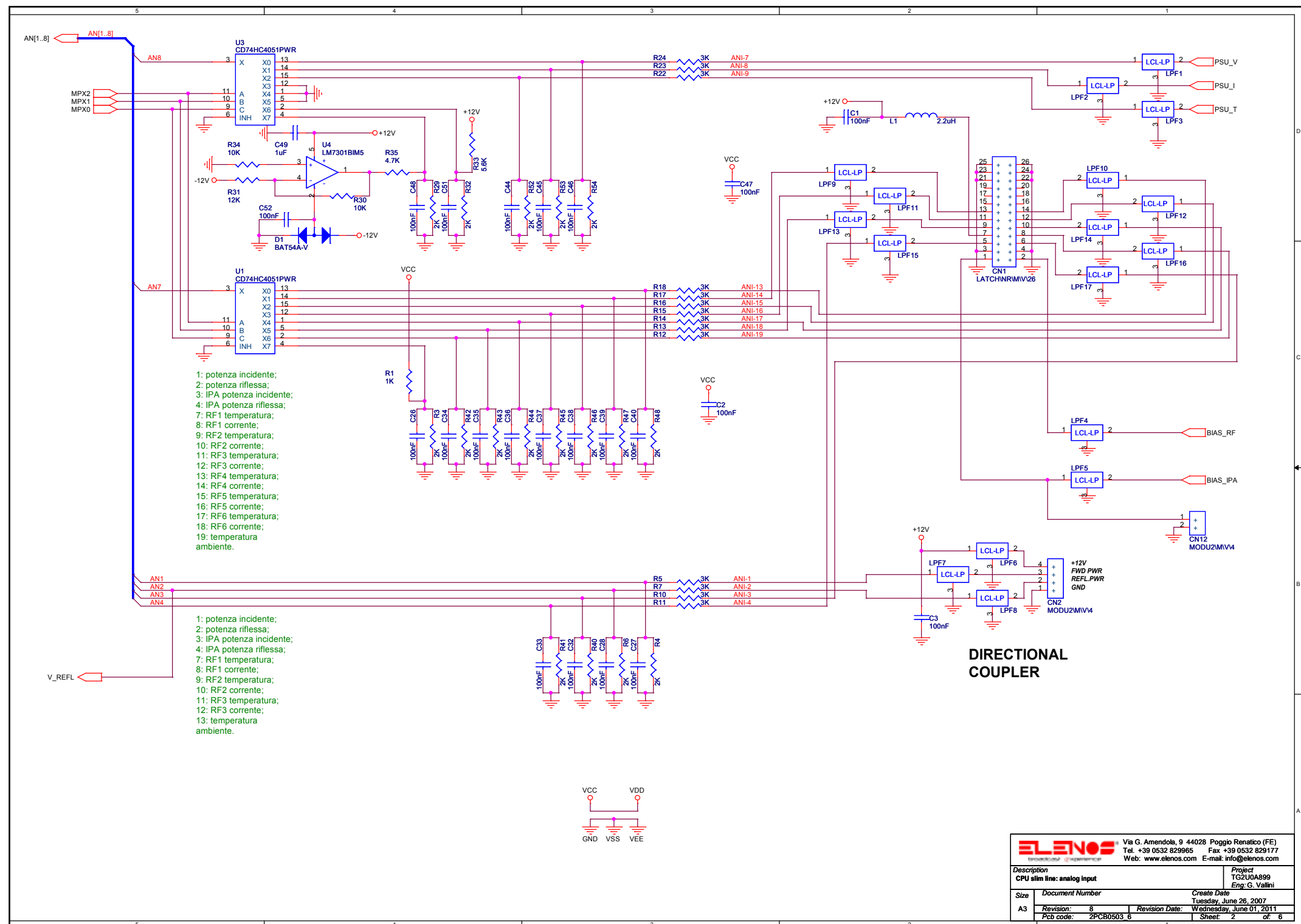


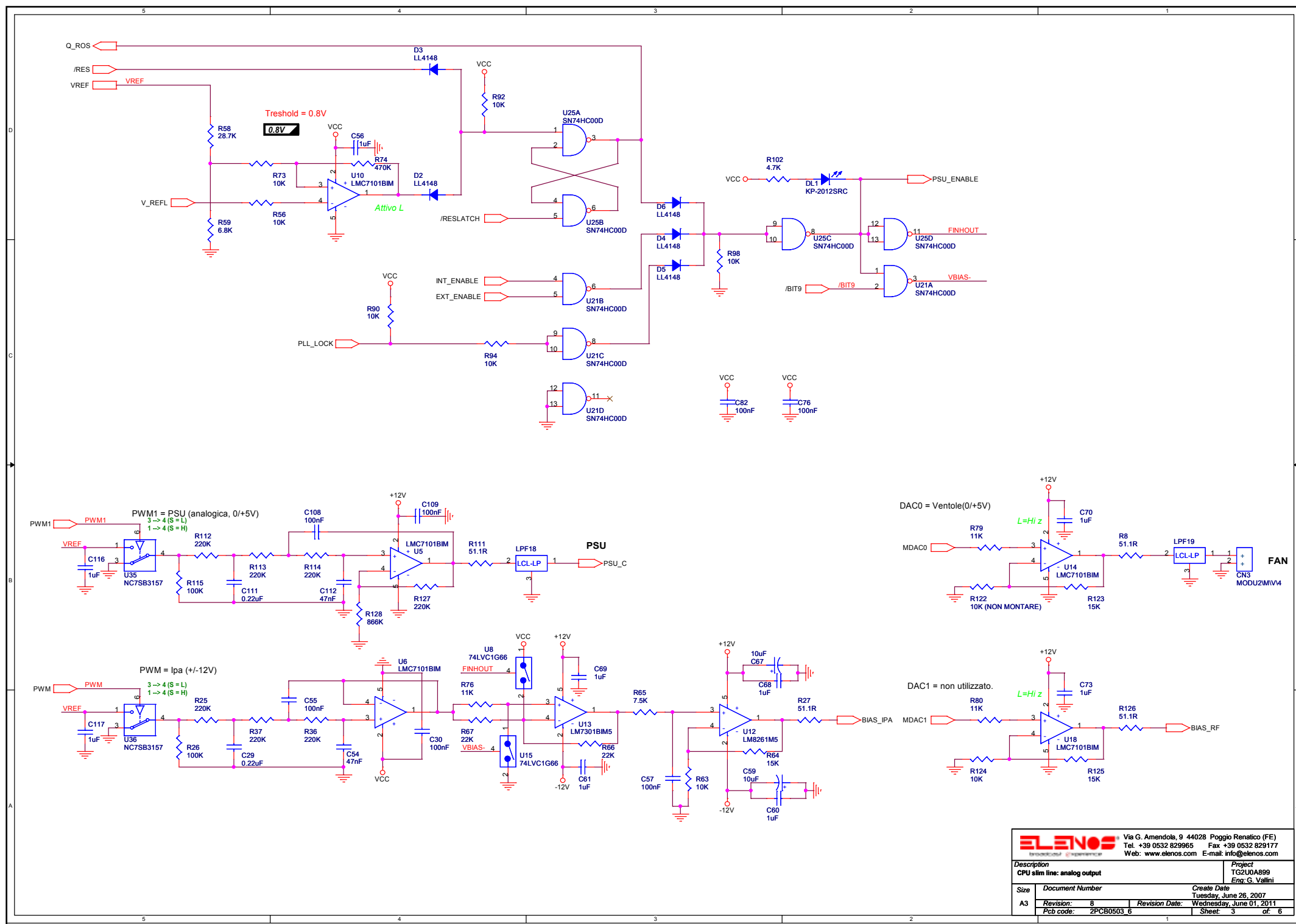
STEREO + AES/EBU INPUTS (TG3K2A867-1)



CPU (TG2U0A890-8)

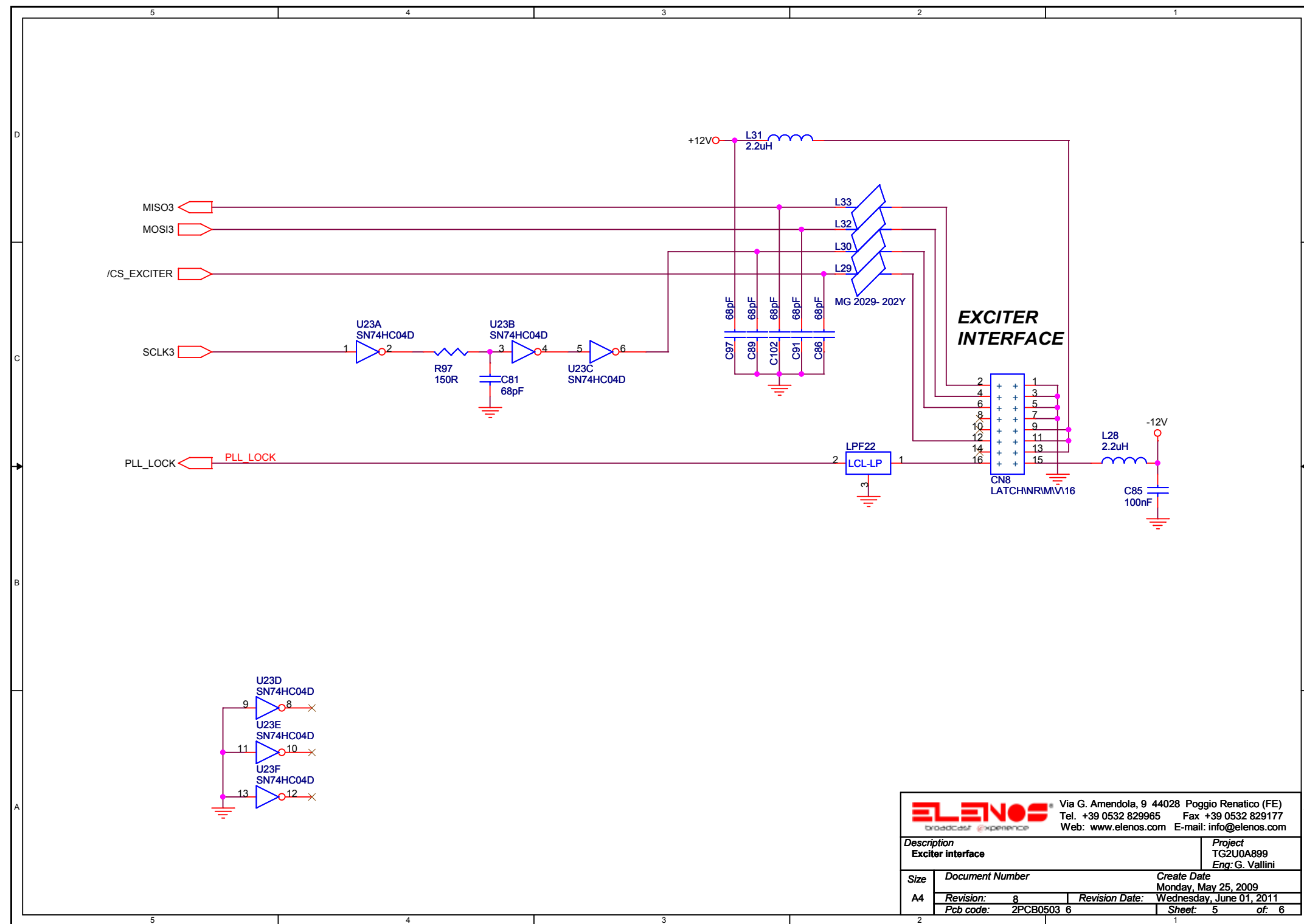




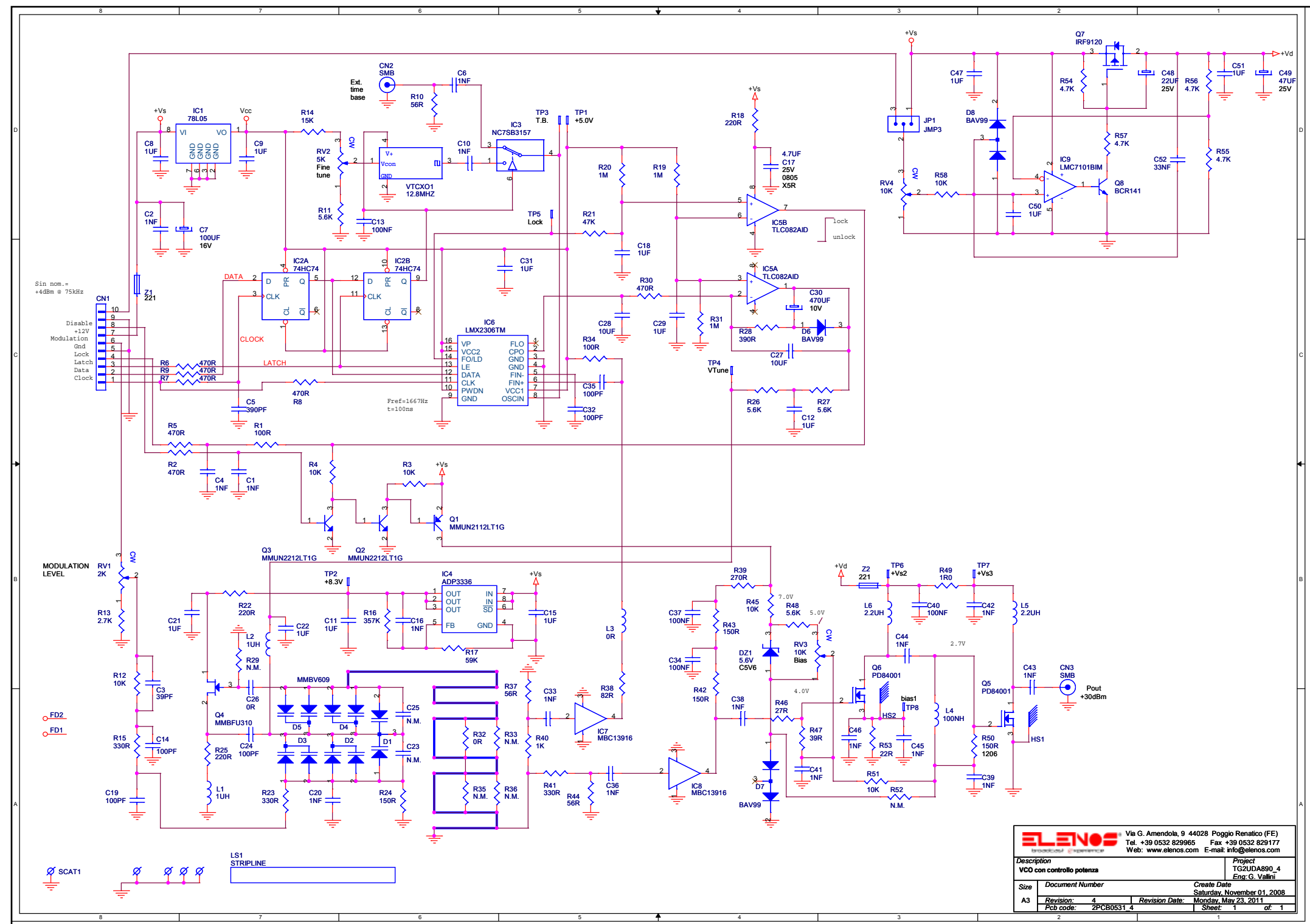


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Web: www.eleos.com		E-mail: info@eleos.com	
Description		Project	
CPU slim line: analog output		TG2U0A899	
Size		Create Date	
A3		Tuesday, June 26, 2007	
Revision: 8		Revision Date: Wednesday, June 01, 2011	
Pcb code: 2PCB0503_6		Sheet 3 of 6	

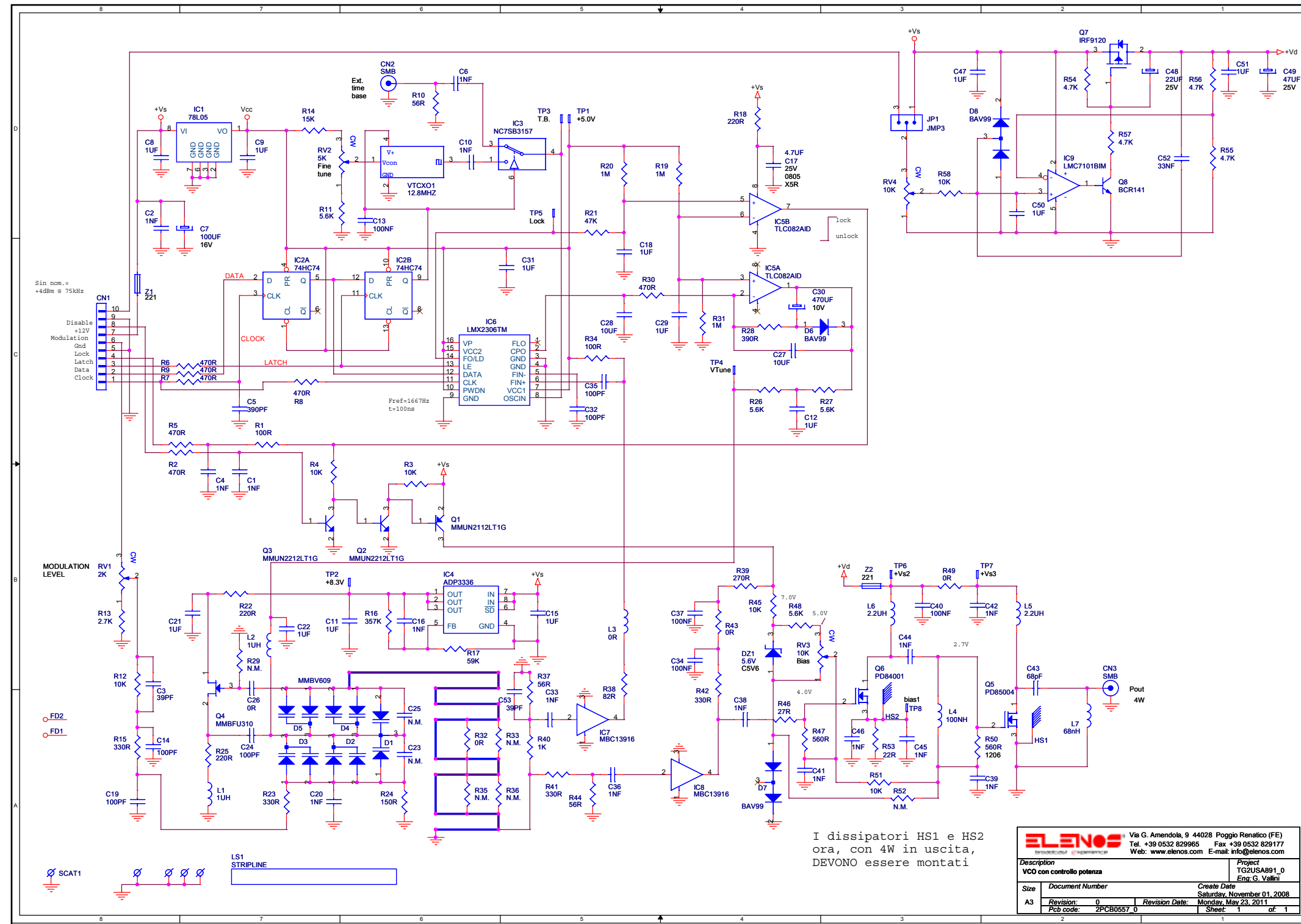




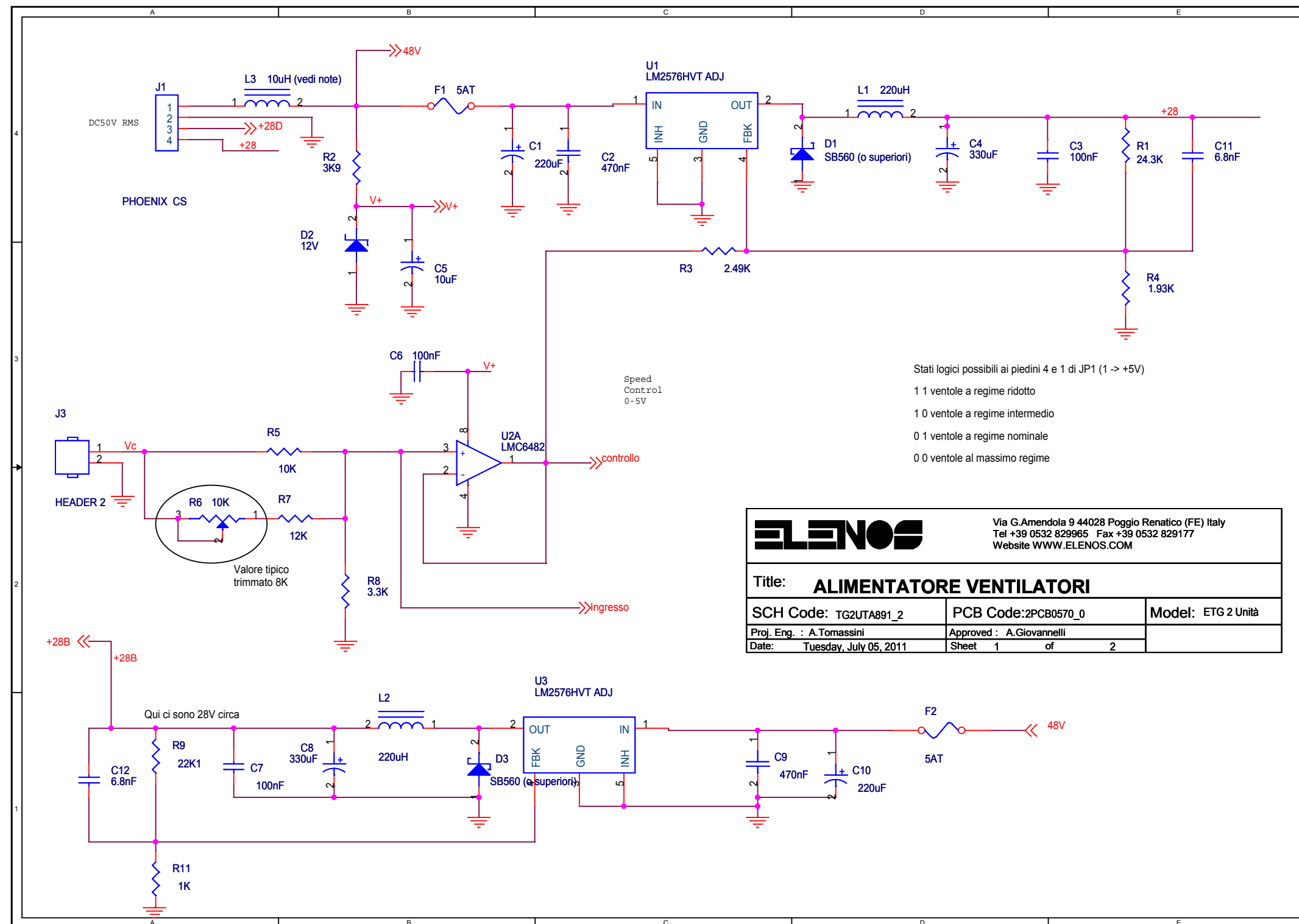
VCO (TG2UDA890-4)

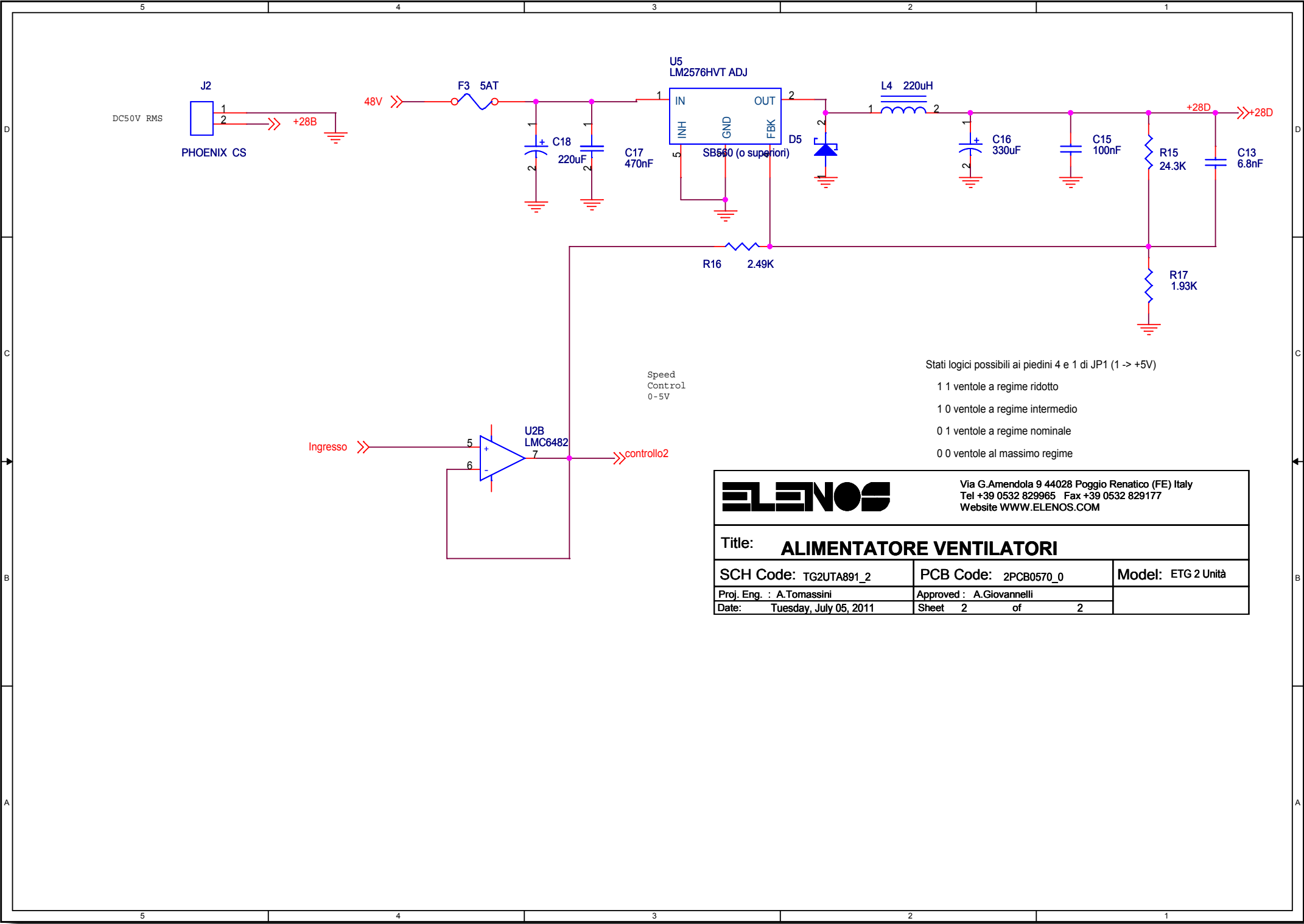


ENHANCED VCO (TG2USA891-0)

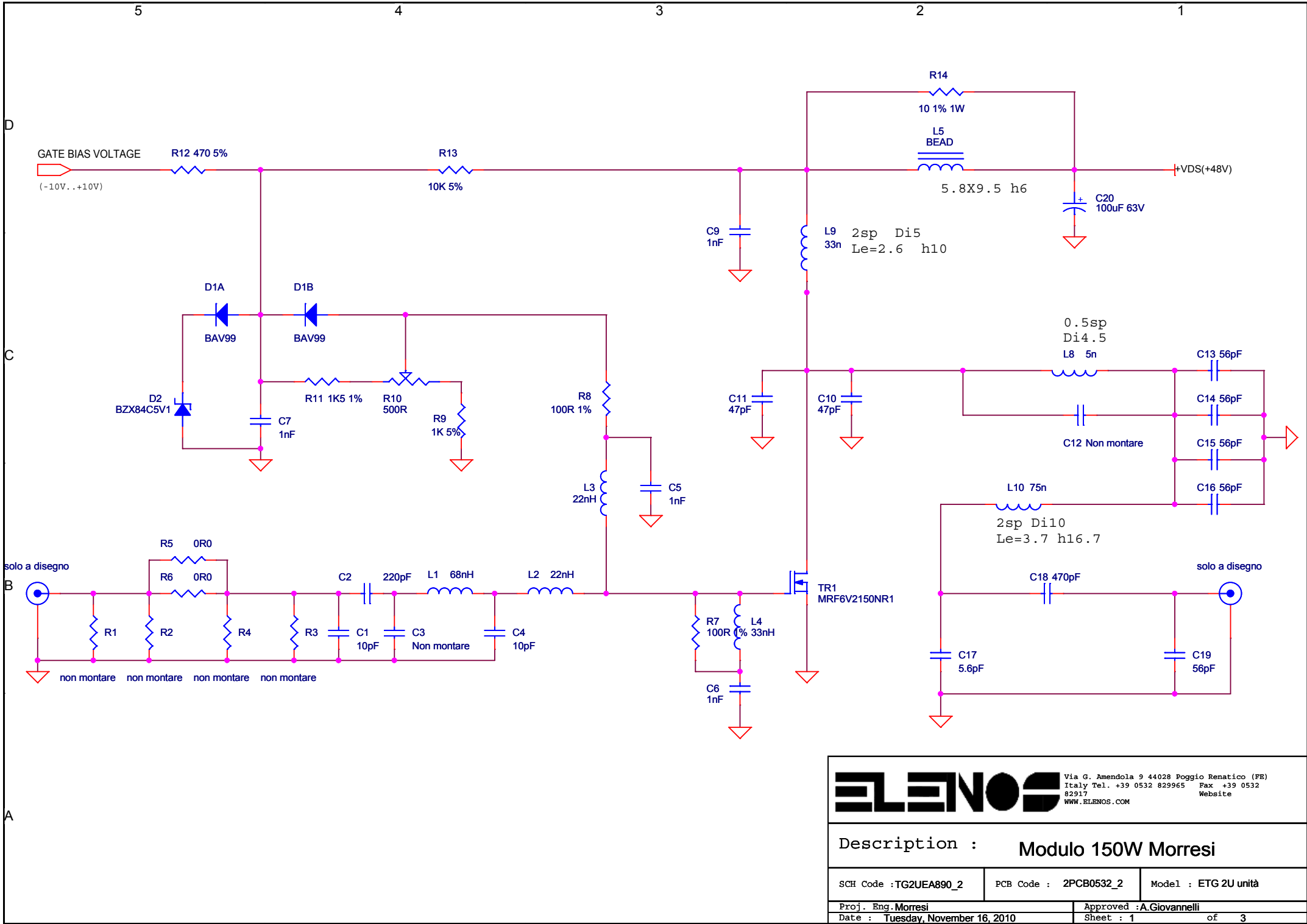


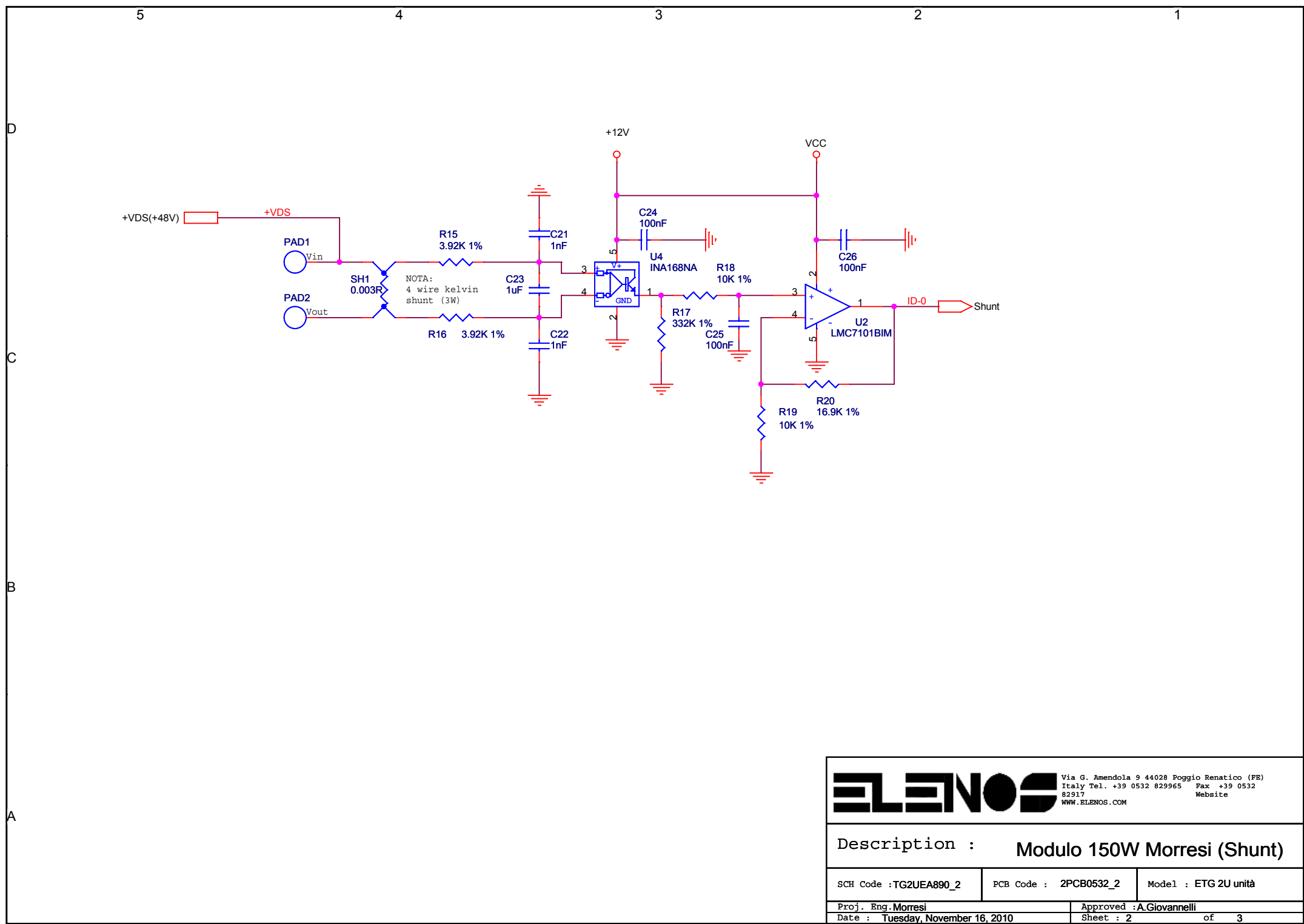
FAN & DRIVER POWER SUPPLY (TG2UTA891-2)





150W RF AMPLIFIER MODULE (TG2UEA890-2)

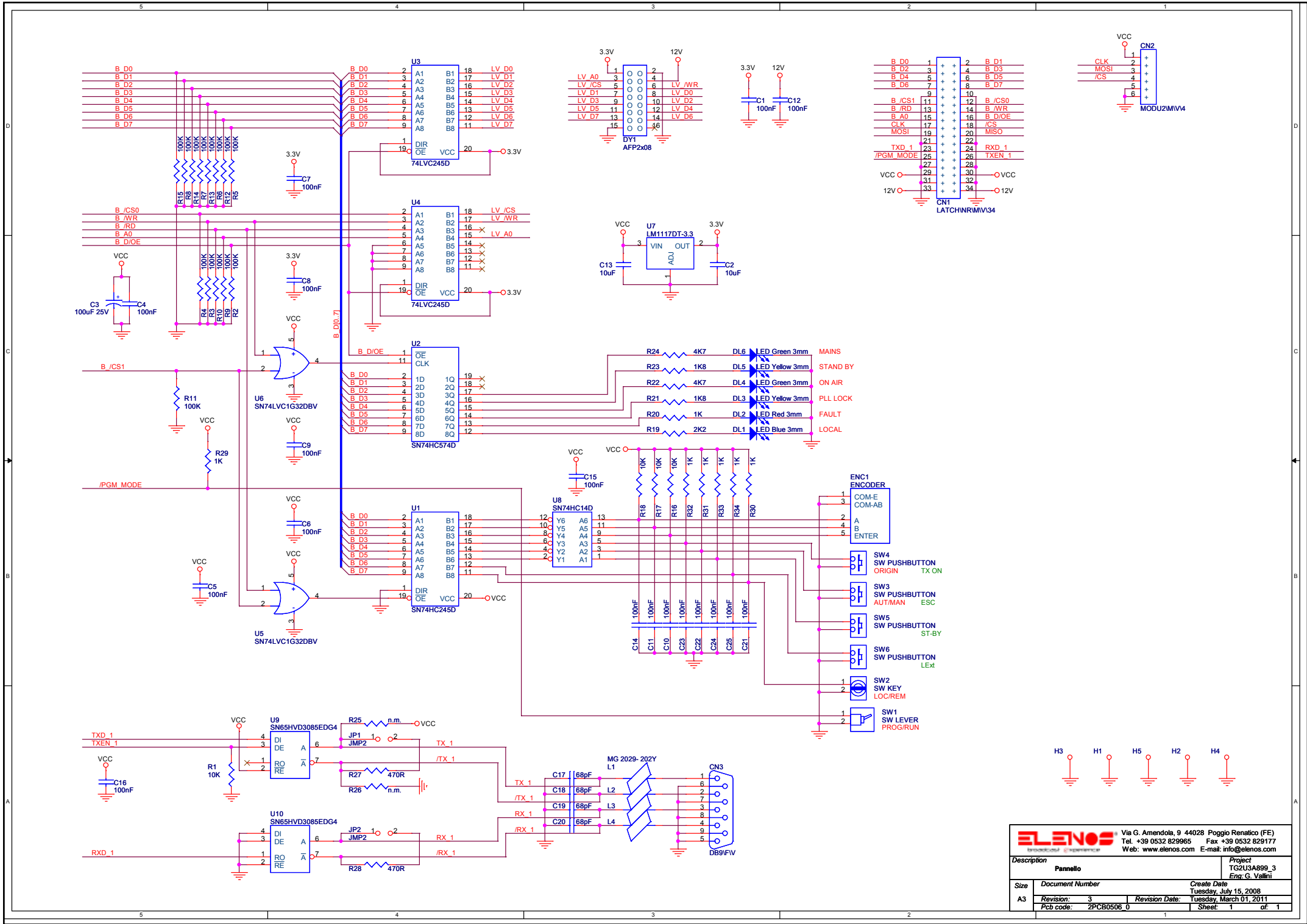






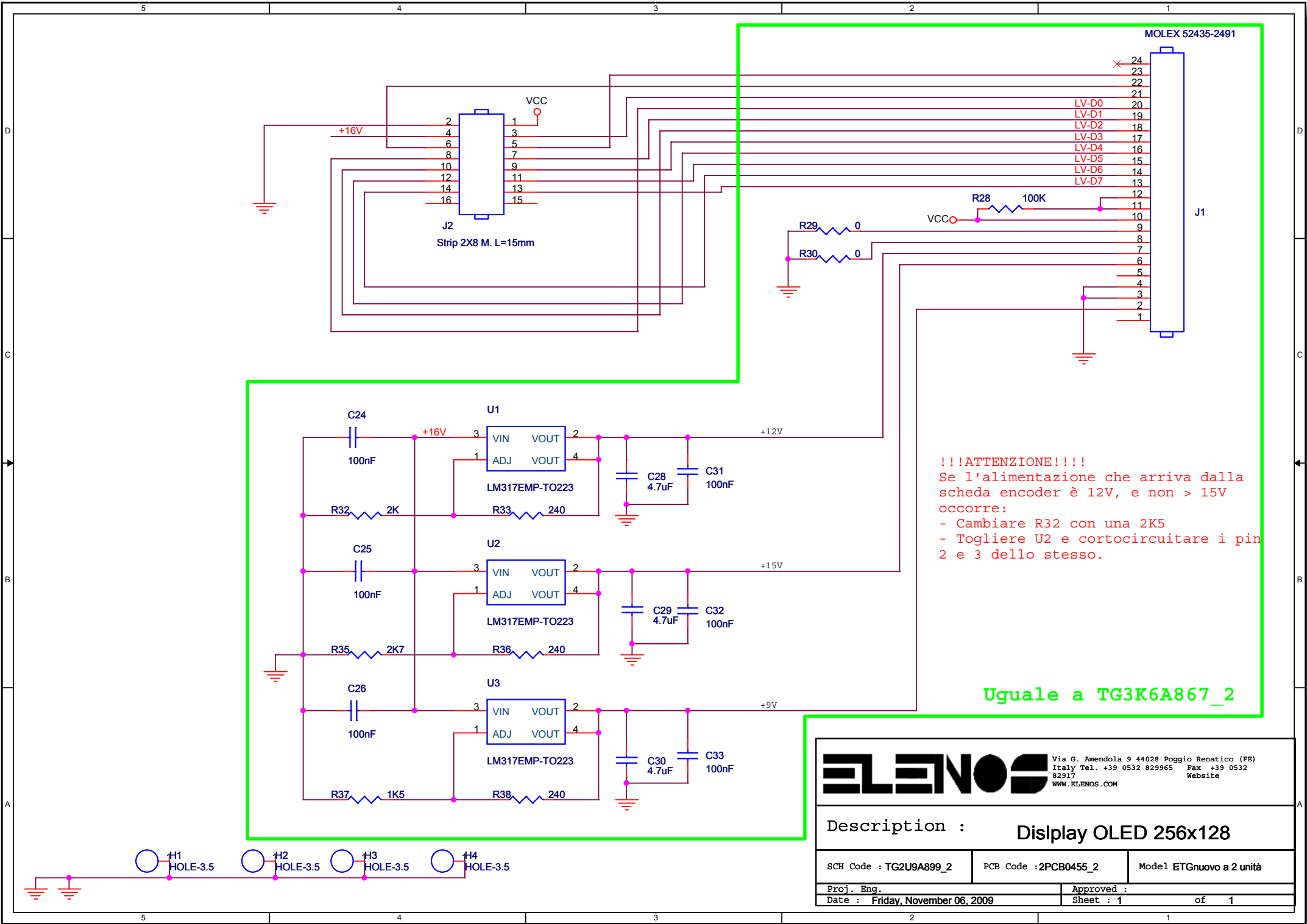
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CONTROL PANEL (TG2U3A899)

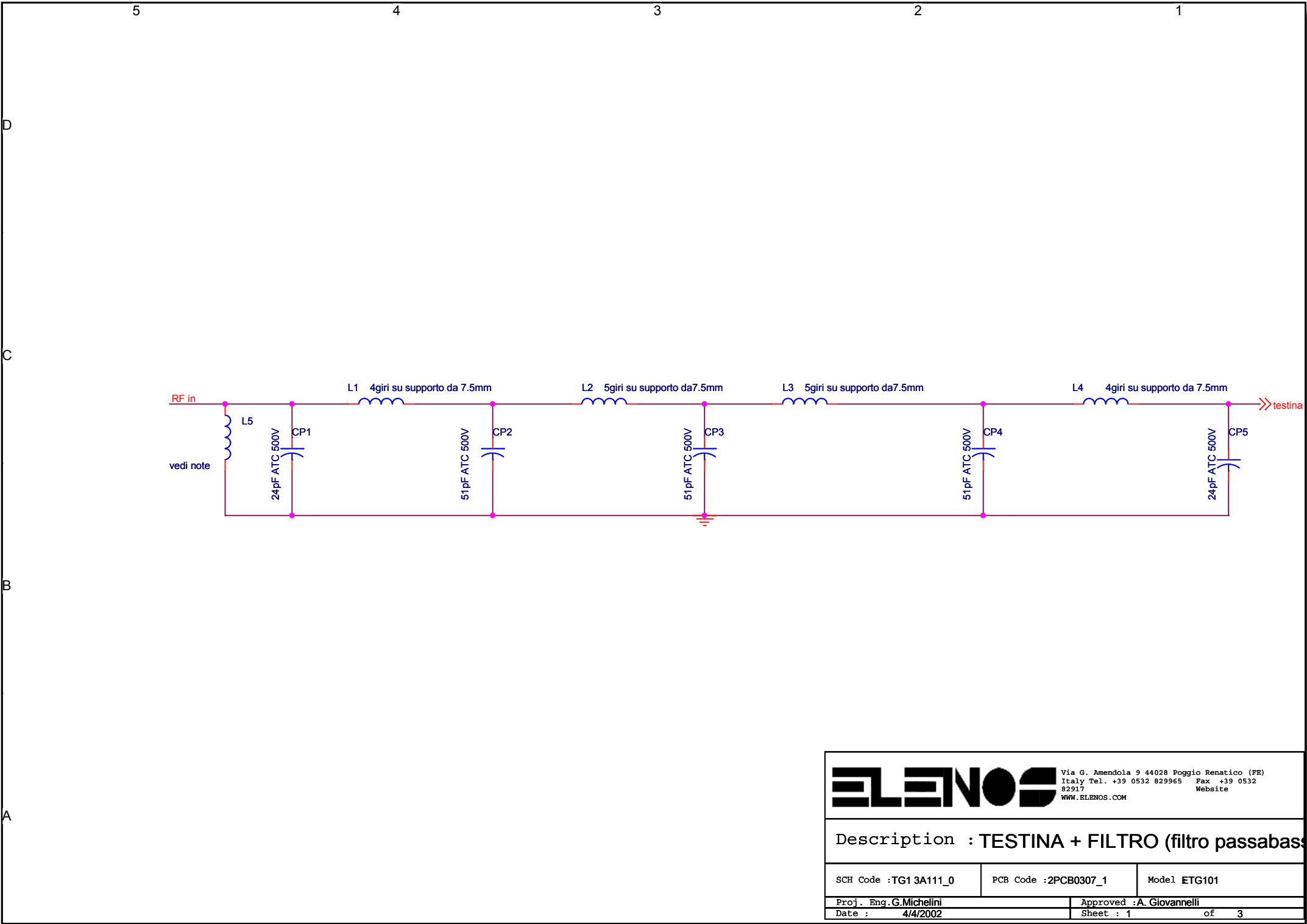


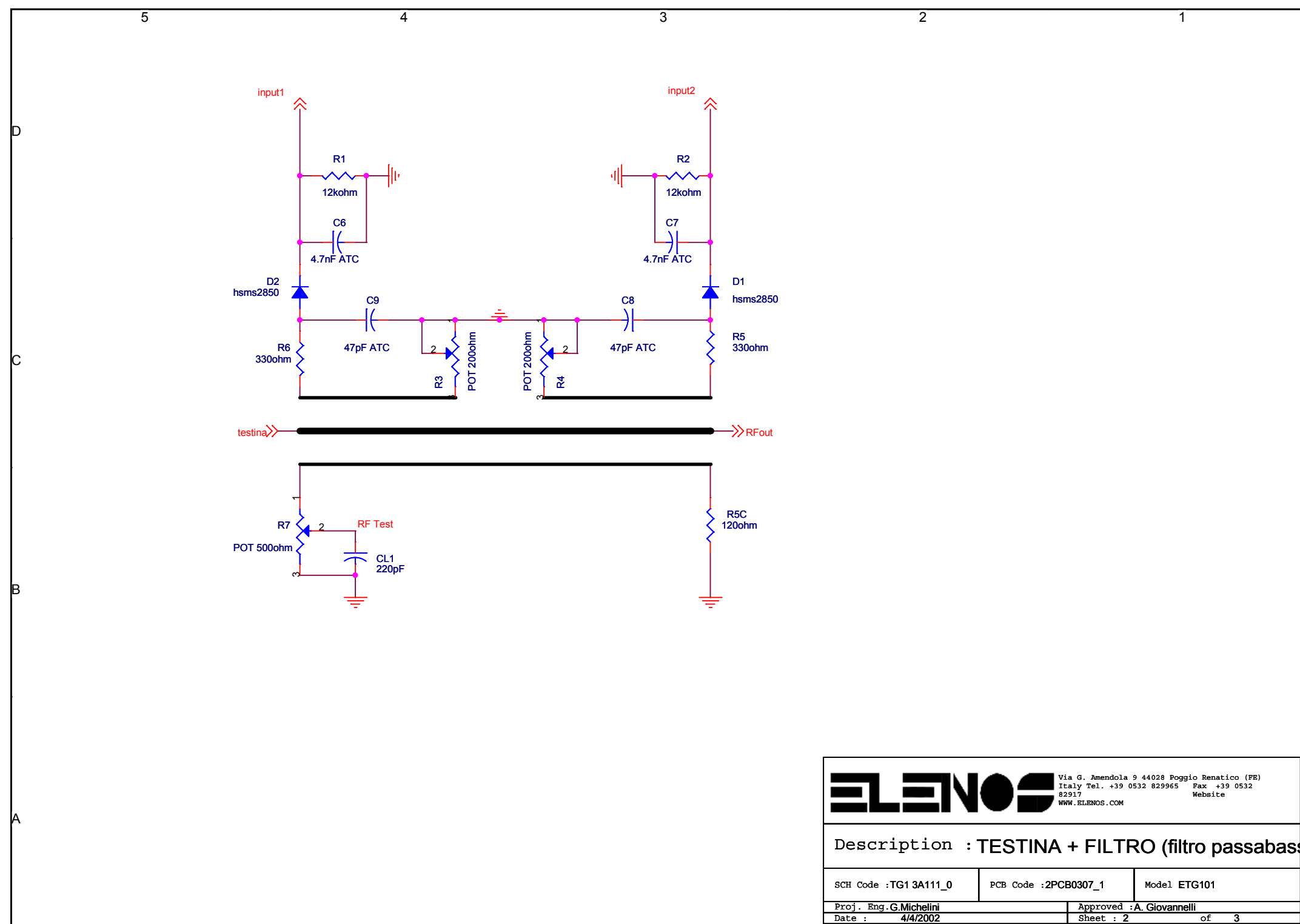
ELENOS		Via G. Amendola, 9 44028 Poggio Renatico (FE)	
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		Web: www.eleunos.com E-mail: info@eleunos.com	
Description		Pannello	
Size		Document Number	
A3		Revision: 3	
		Revision Date: Tuesday, March 01, 2011	
		Pcb code: 2PCB0506 0	
		Sheet: 1 of 1	

OLED DISPLAY ADAPTER (TG2U9A899)



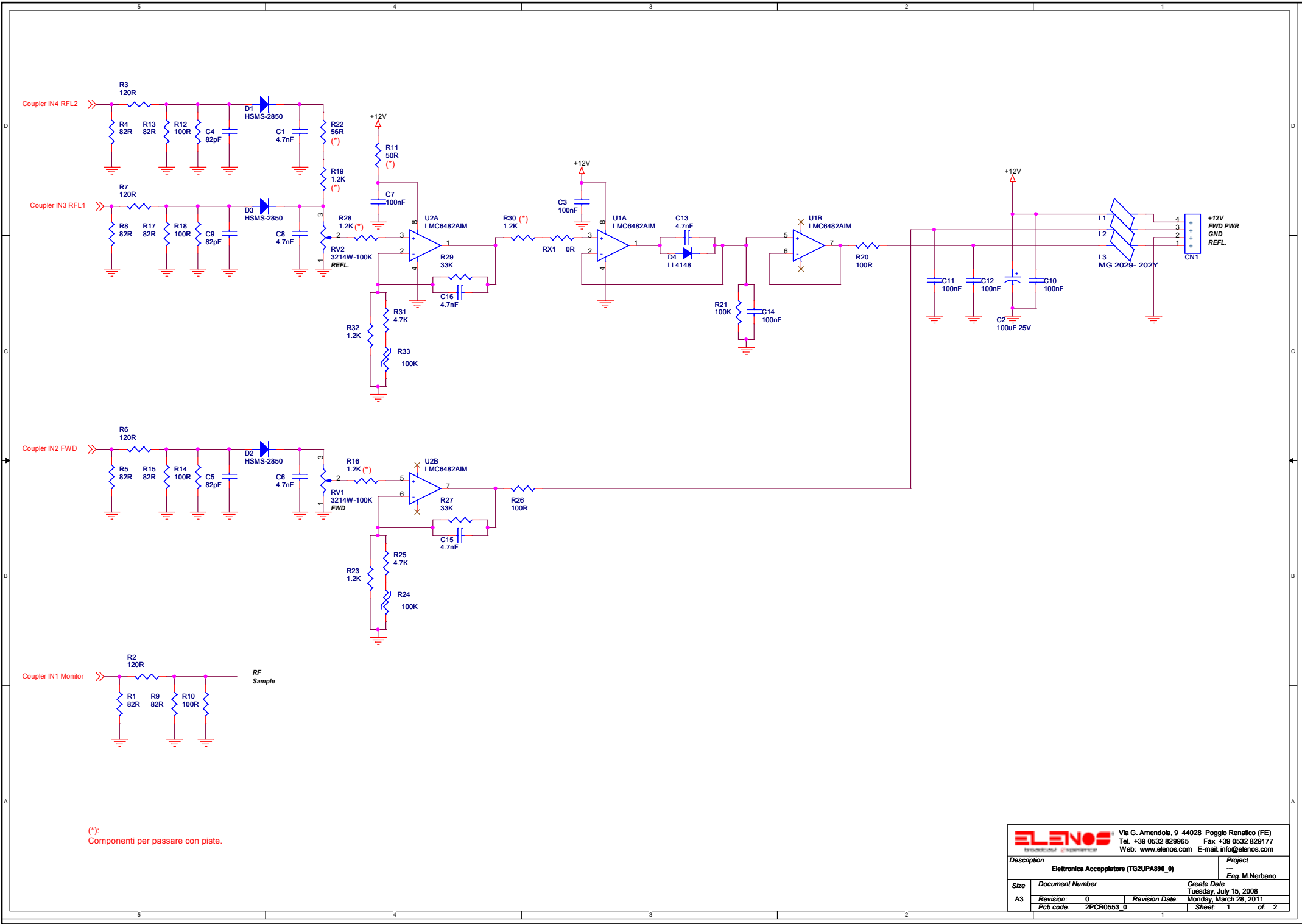
LOW PASS FILTER WITH DIRECTIONAL COUPLER (TG1 3A111-0)

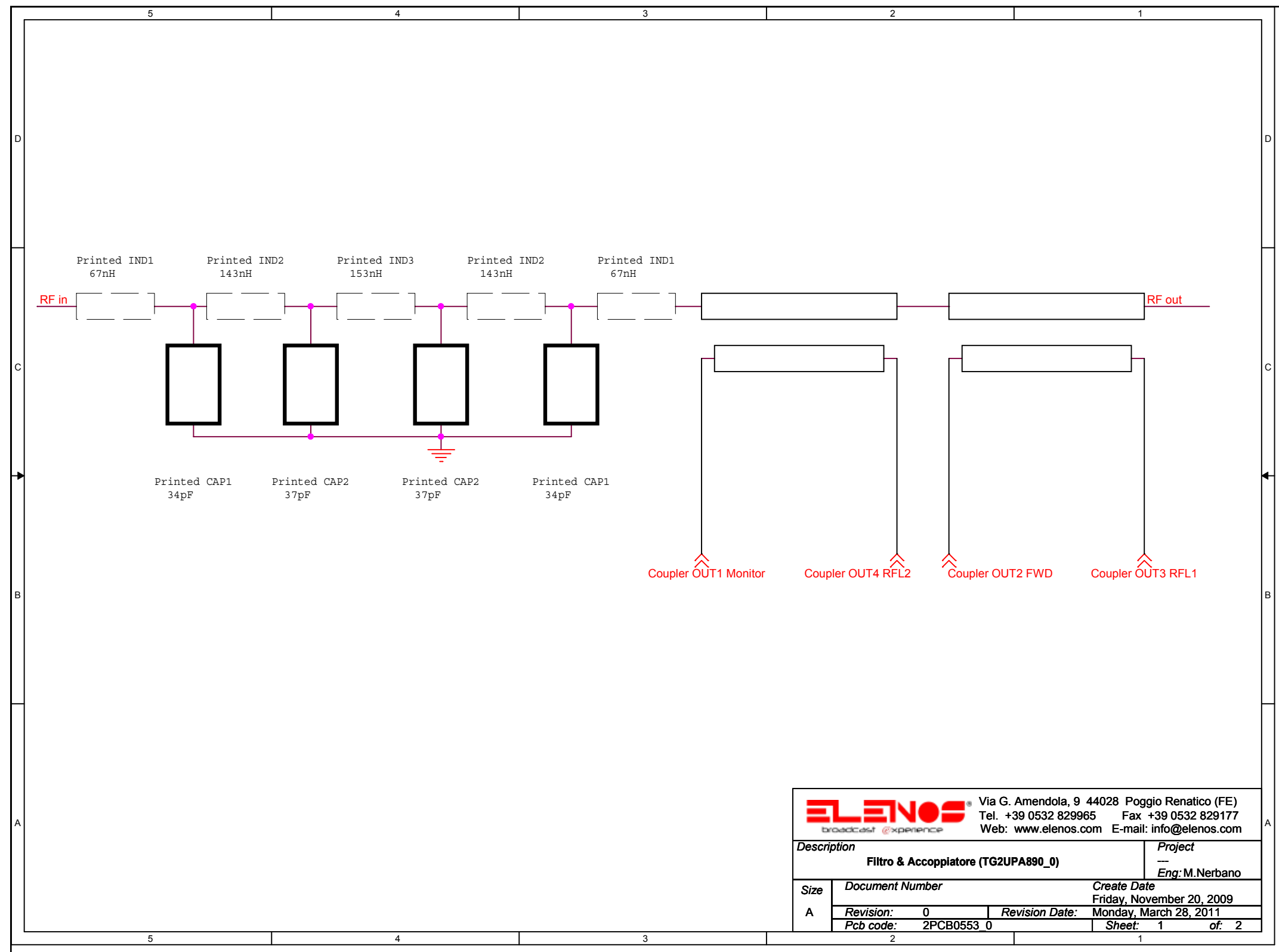




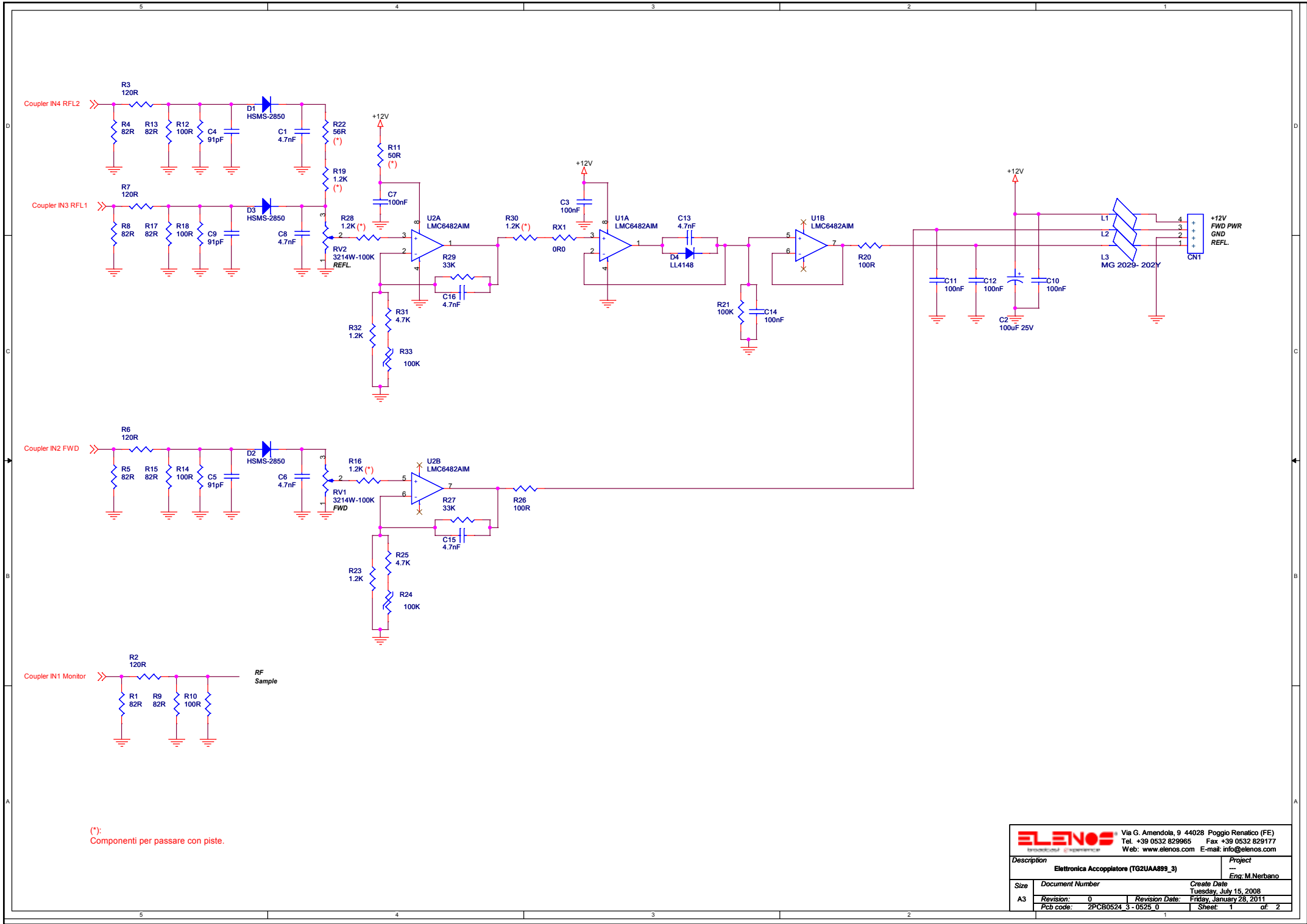


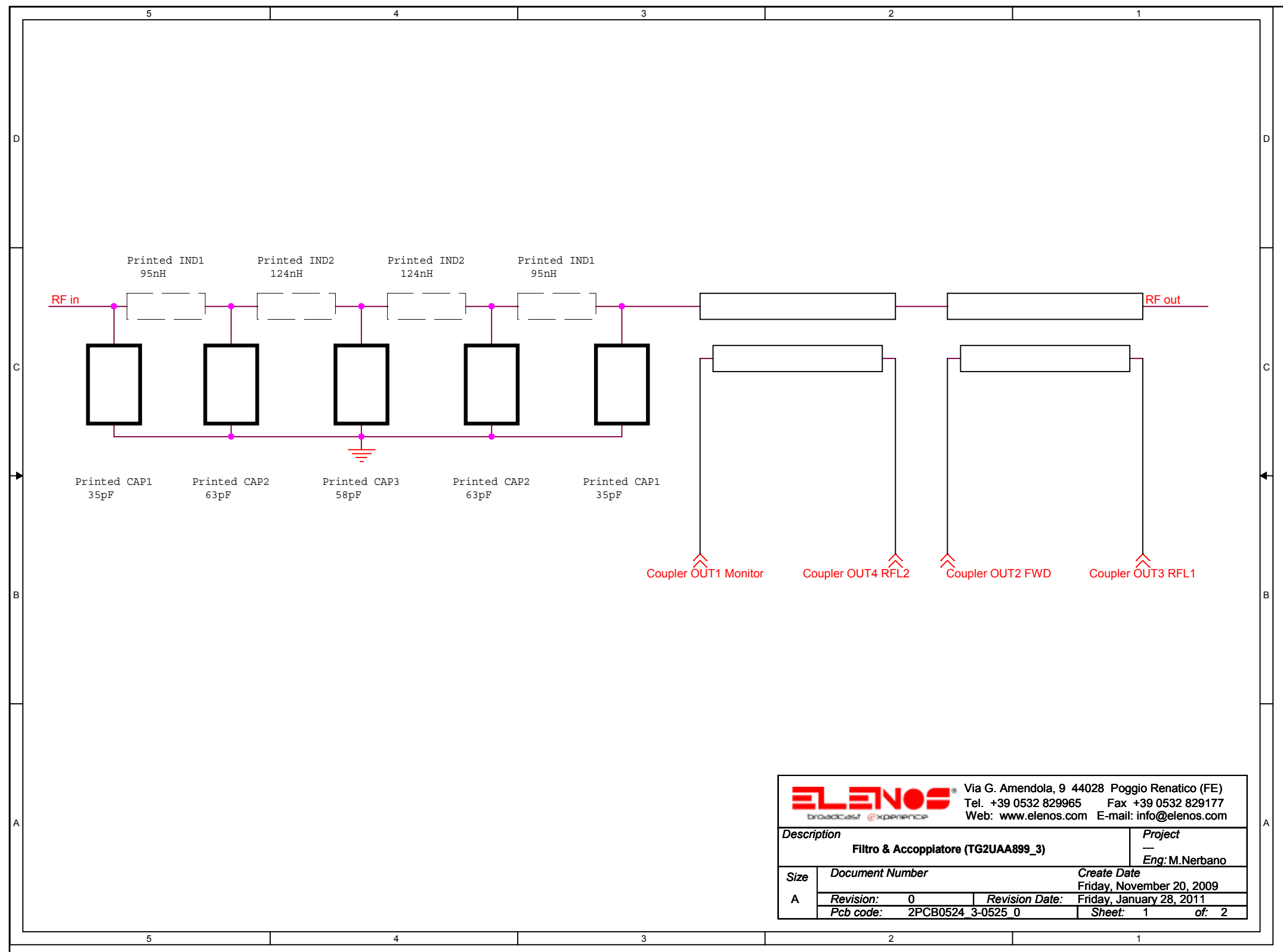
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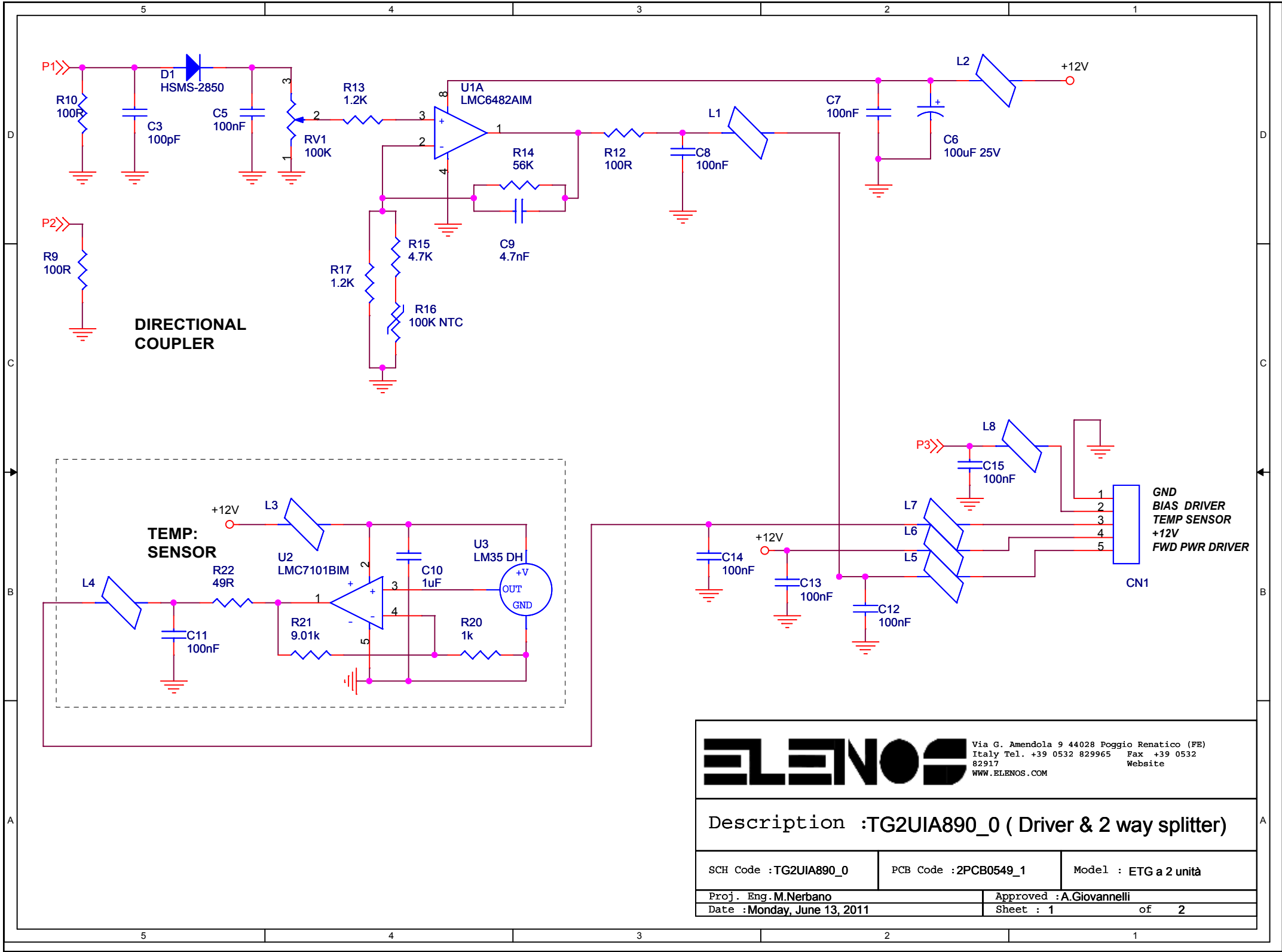


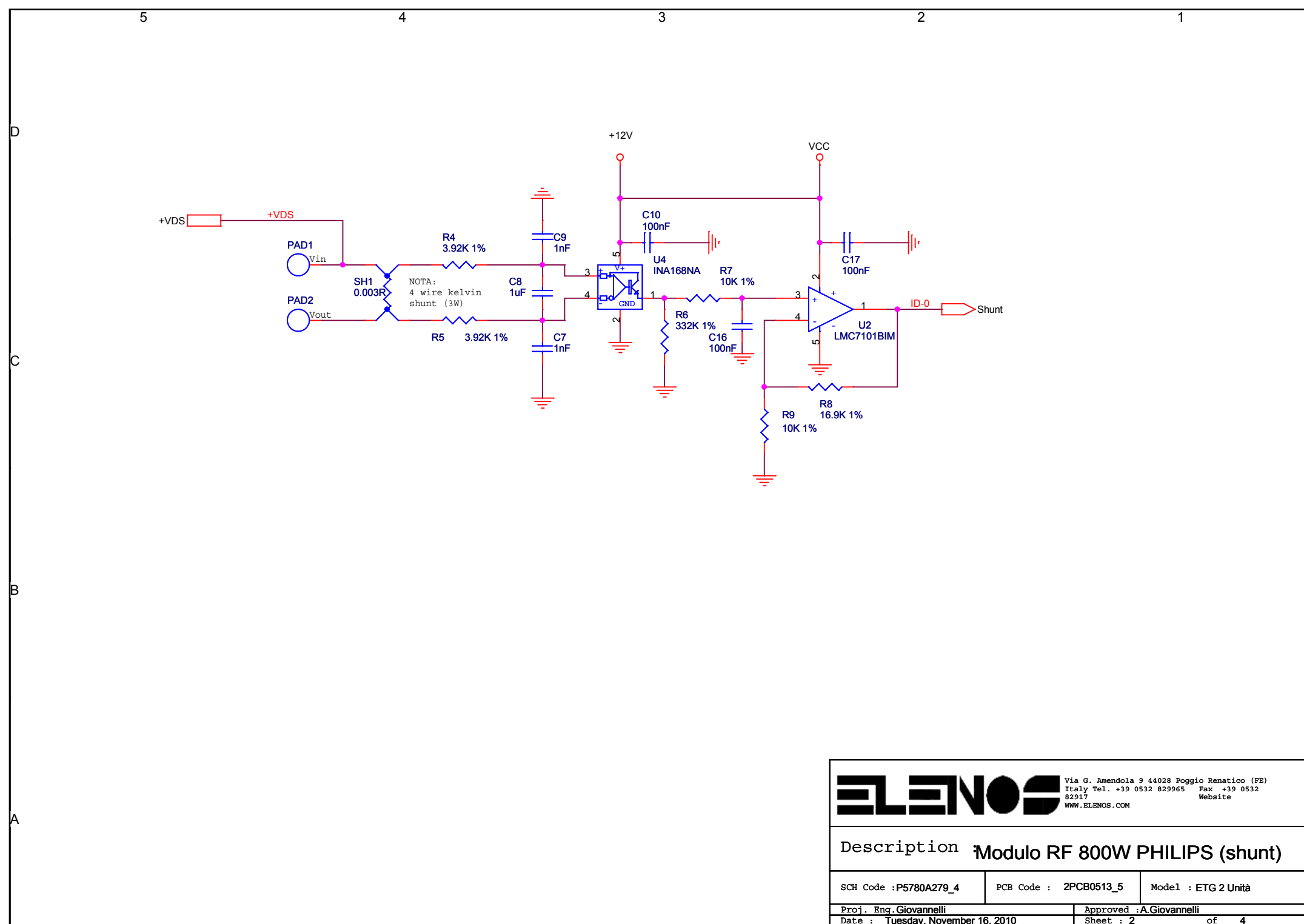
LOW PASS FILTER WITH DIRECTIONAL COUPLER (TG2UAA890-0)

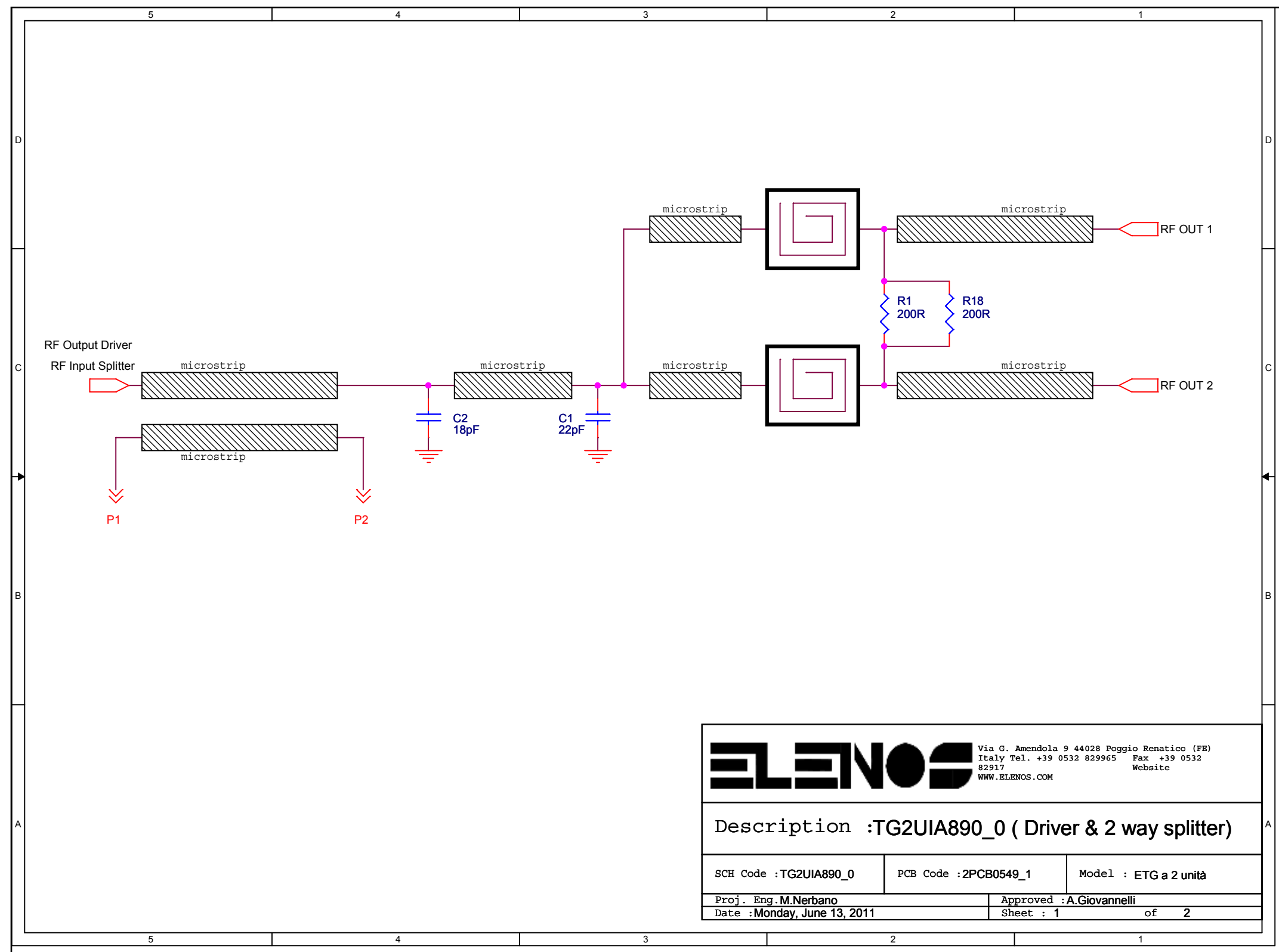




2-WAY DIVIDER WITH DRIVER (TG2UIA890-0)

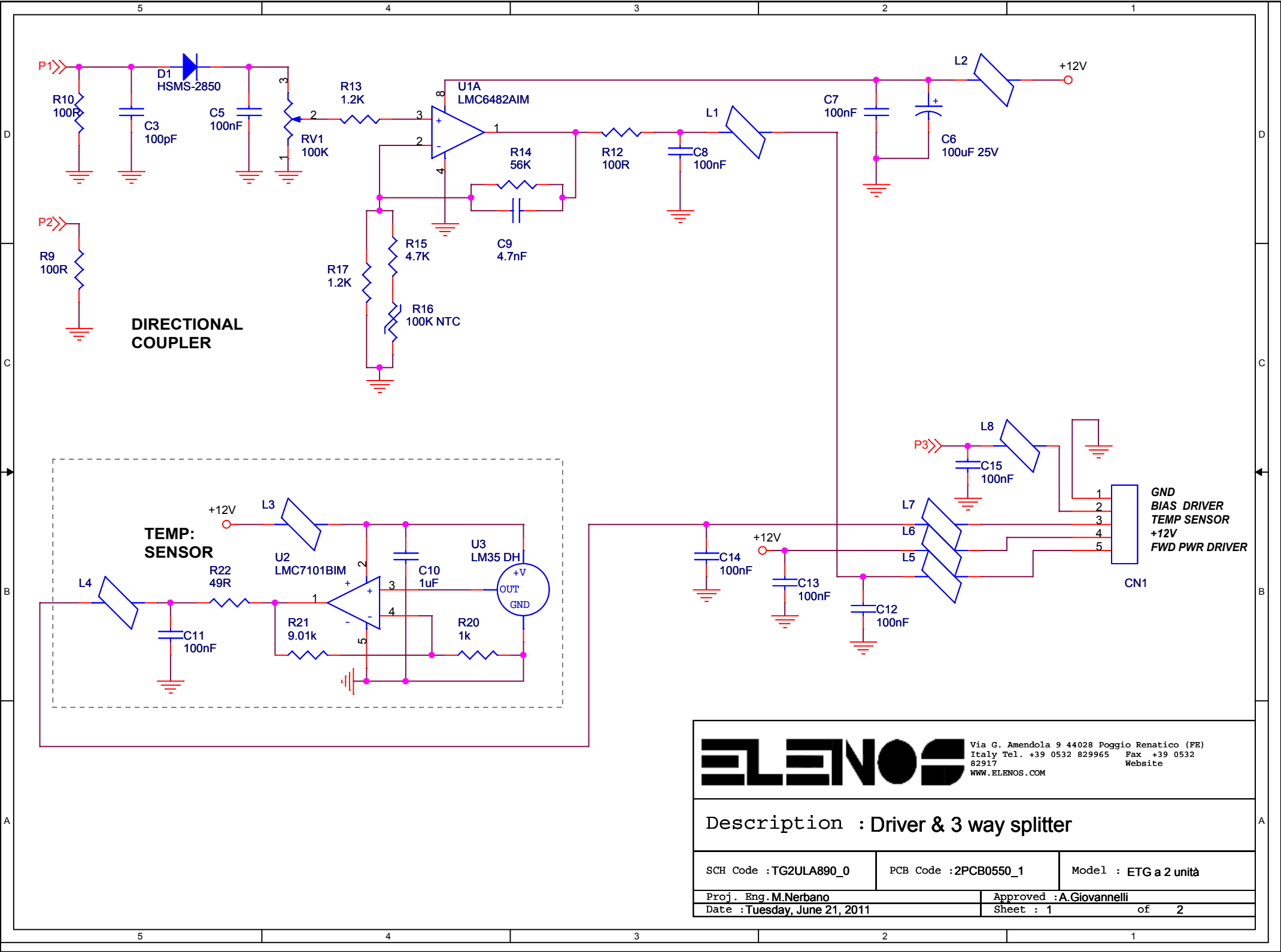


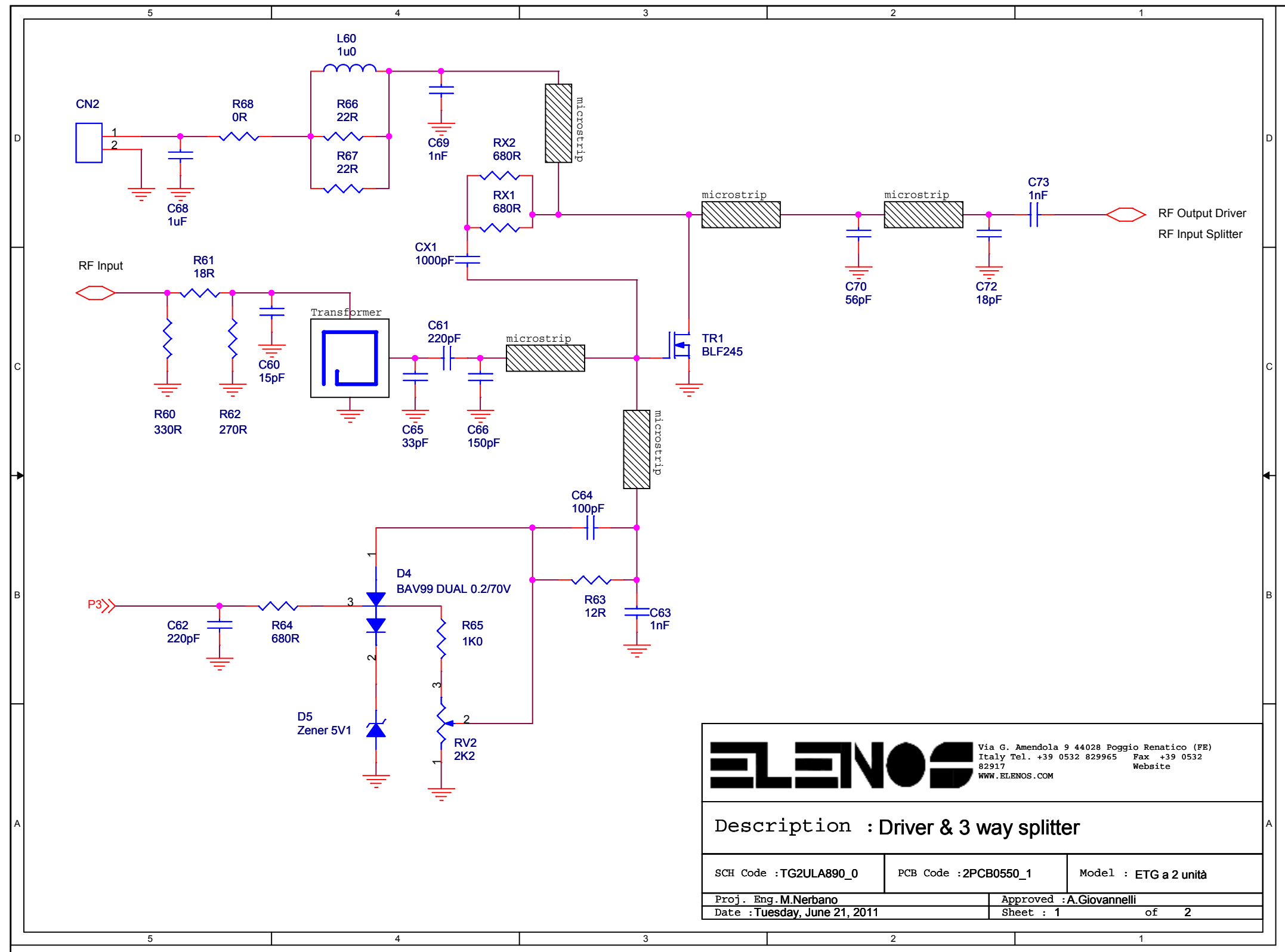




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Description :TG2UIA890_0 (Driver & 2 way splitter)		
SCH Code :TG2UIA890_0	PCB Code :2PCB0549_1	Model : ETG a 2 unità
Proj. Eng. M.Nerbano	Approved :A.Giovannelli	
Date :Monday, June 13, 2011	Sheet : 1	of 2

3-WAY DIVIDER WITH DRIVER (TG2ULA890-0)



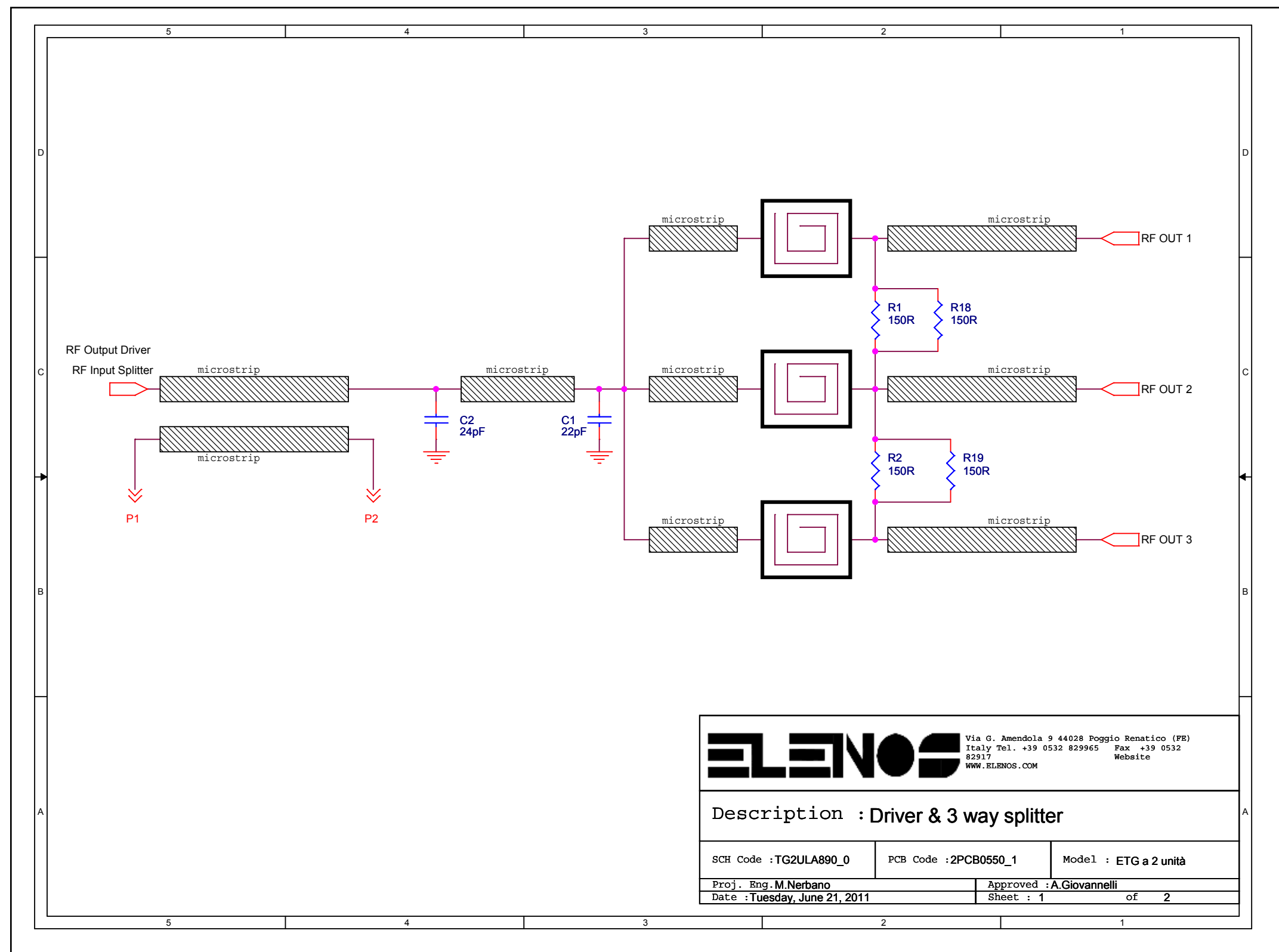




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WWW.ELENNOS.COM

Description : Driver & 3 way splitter

SCH Code :TG2ULA890_0	PCB Code :2PCB0550_1	Model : ETG a 2 unità
Proj. Eng. M.Nerbano		Approved :A.Giovannelli
Date :Tuesday, June 21, 2011		Sheet : 1 of 2



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WWW.ELENNOS.COM Website

Description : Driver & 3 way splitter

SCH Code :TG2ULA890_0

PCB Code :2PCB0550_1

Model : ETG a 2 unità

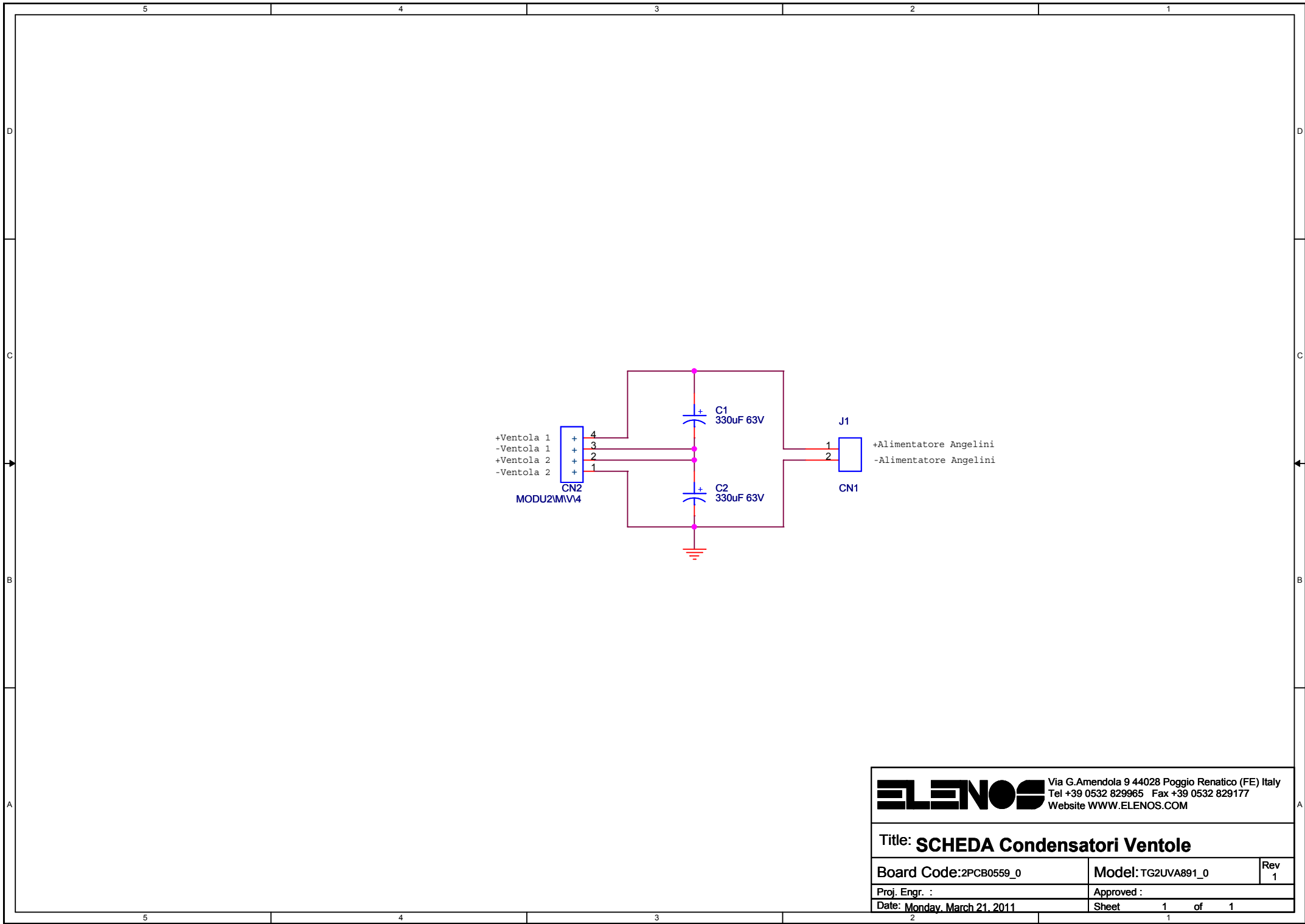
Proj. Eng. M.Nerbano

Approved : A.Giovannelli

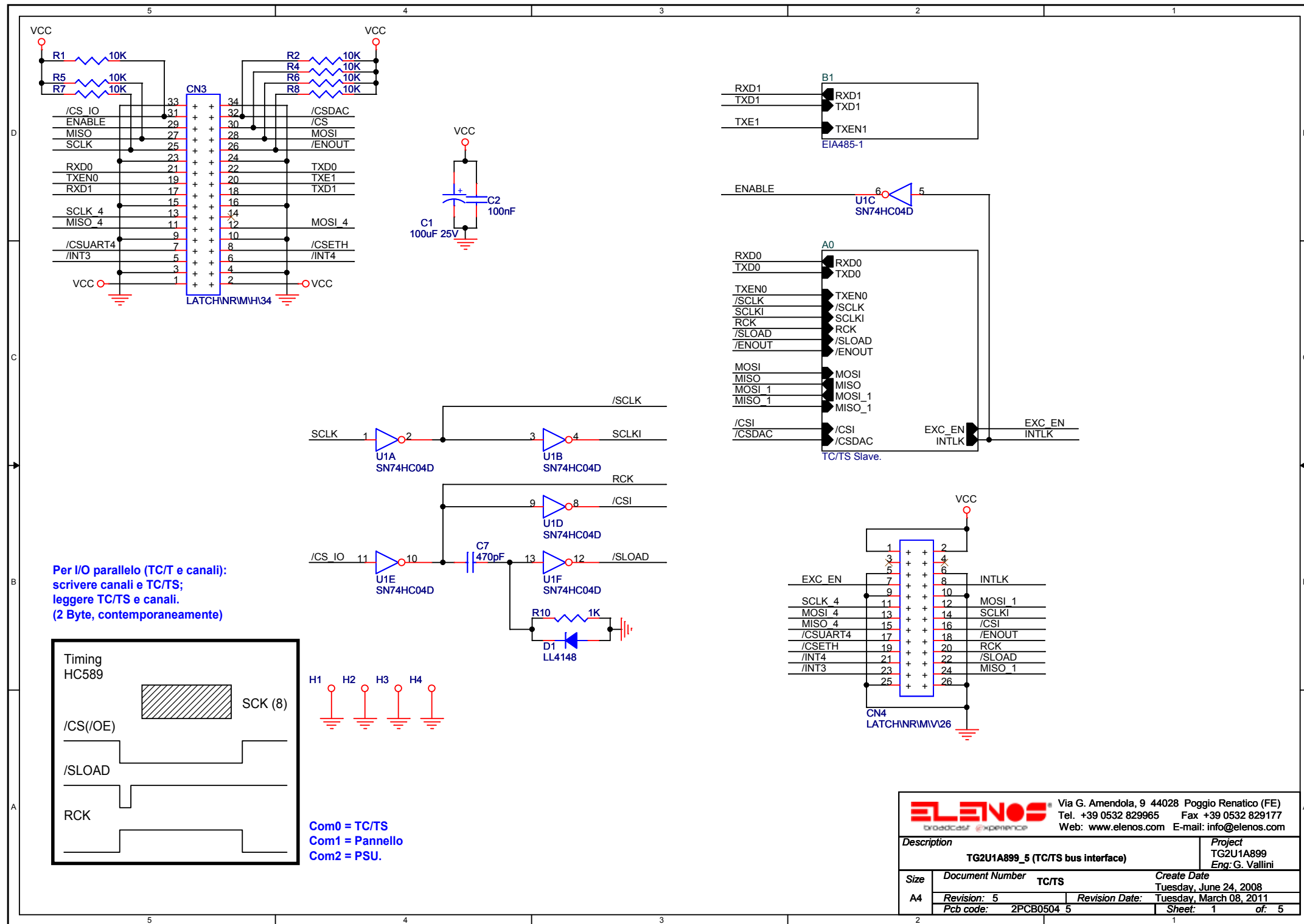
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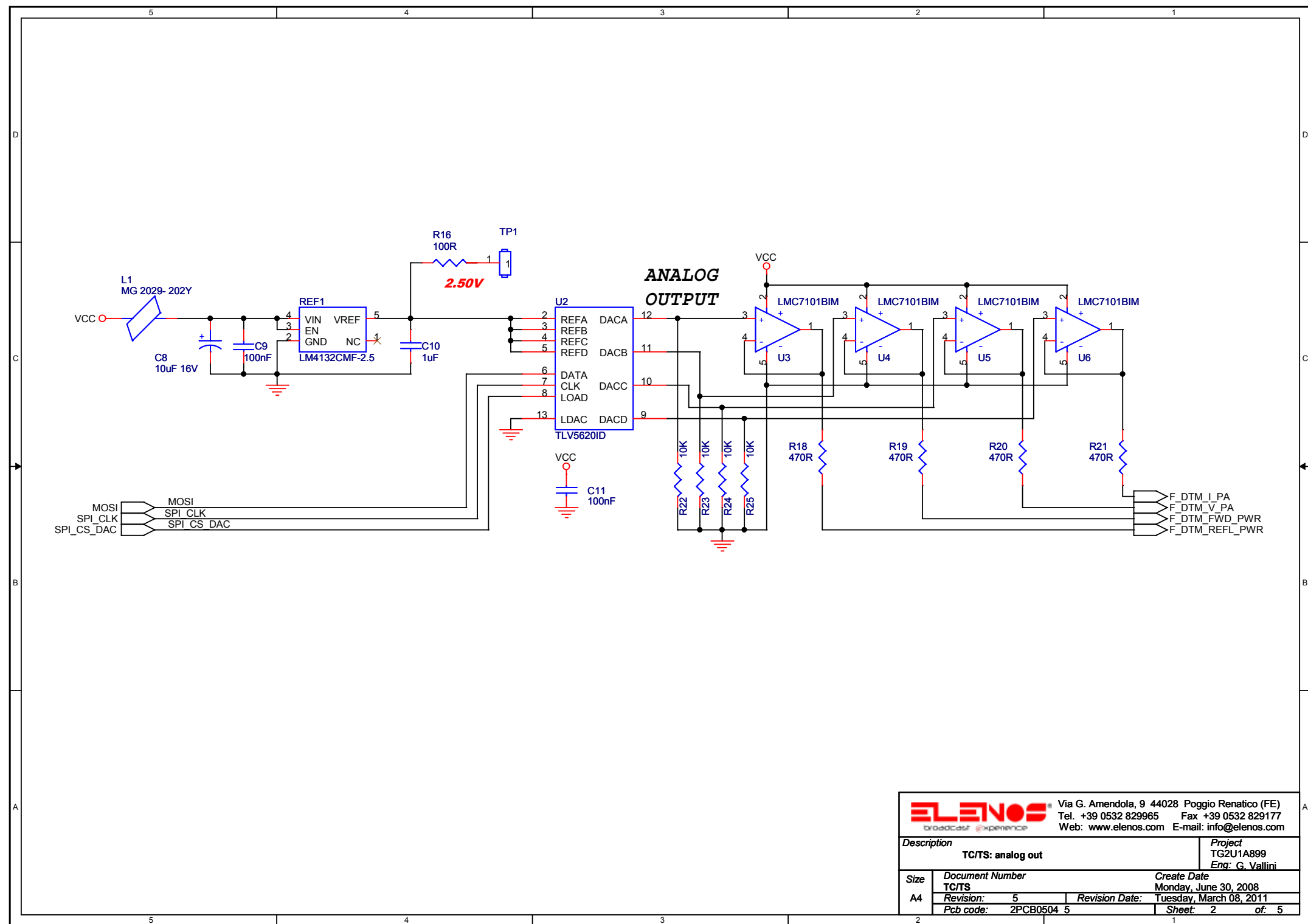
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FAN STARTER (TG2UVA891-0)

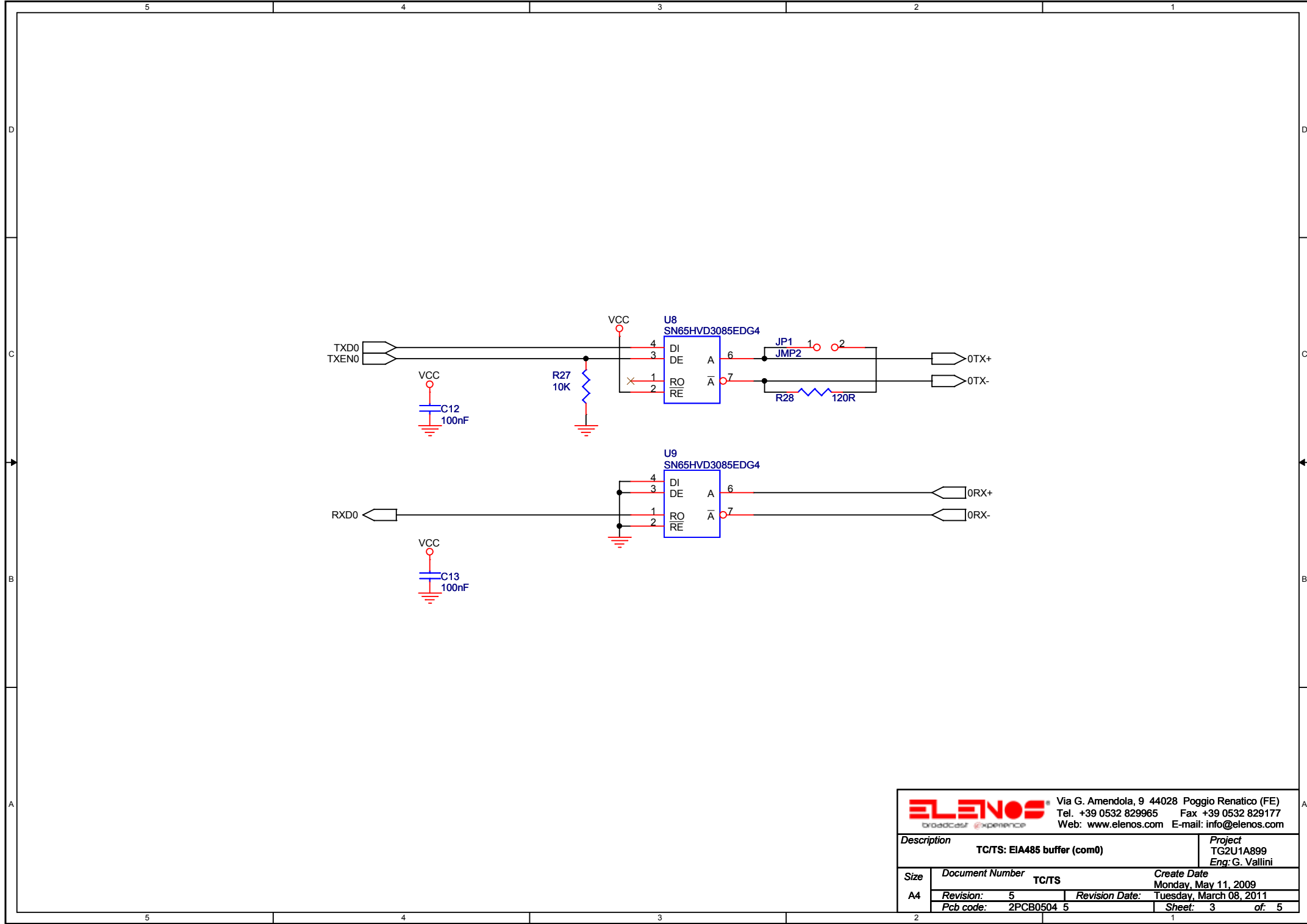


TC/TS BOARD (TG2U1A899_5)



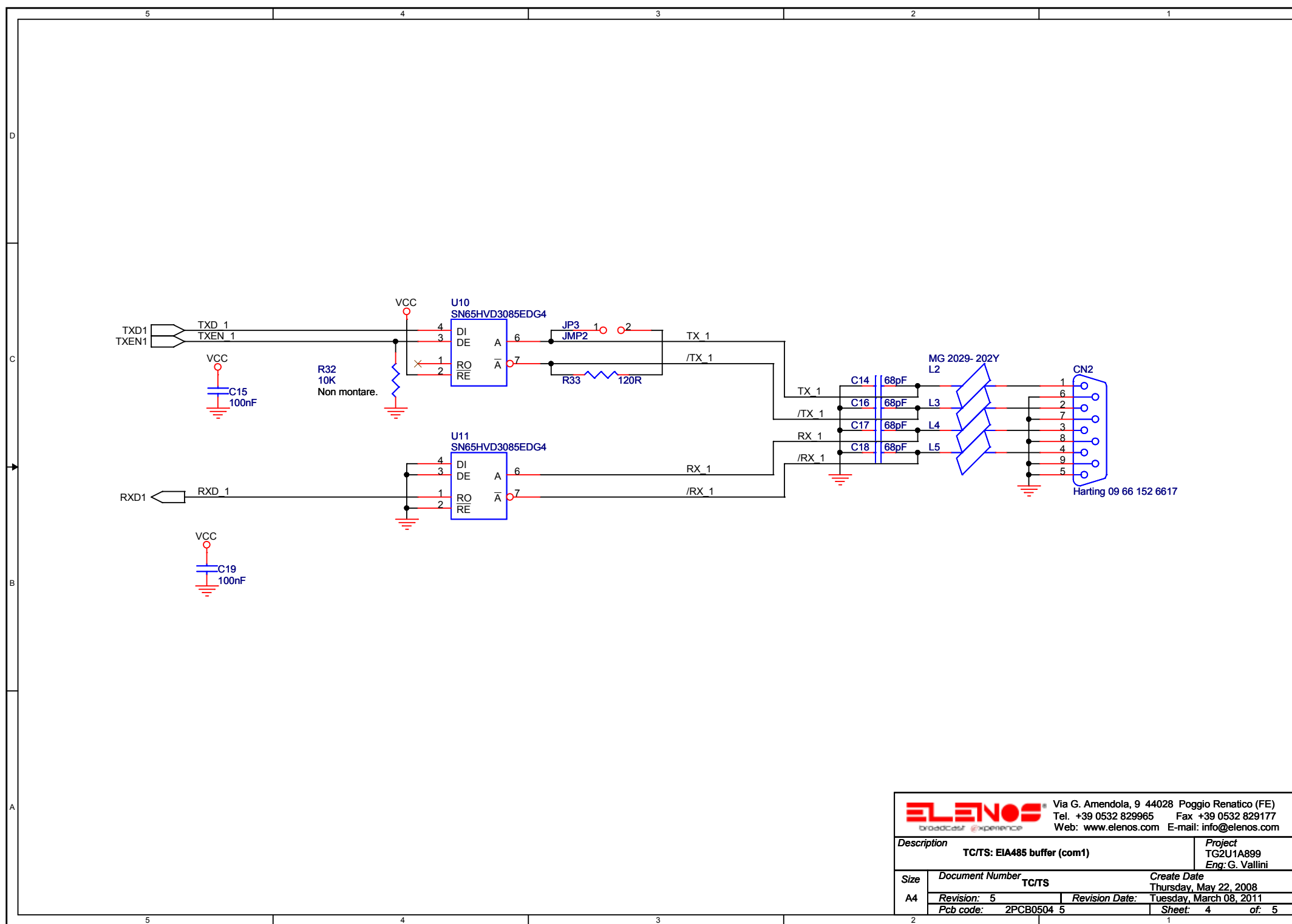


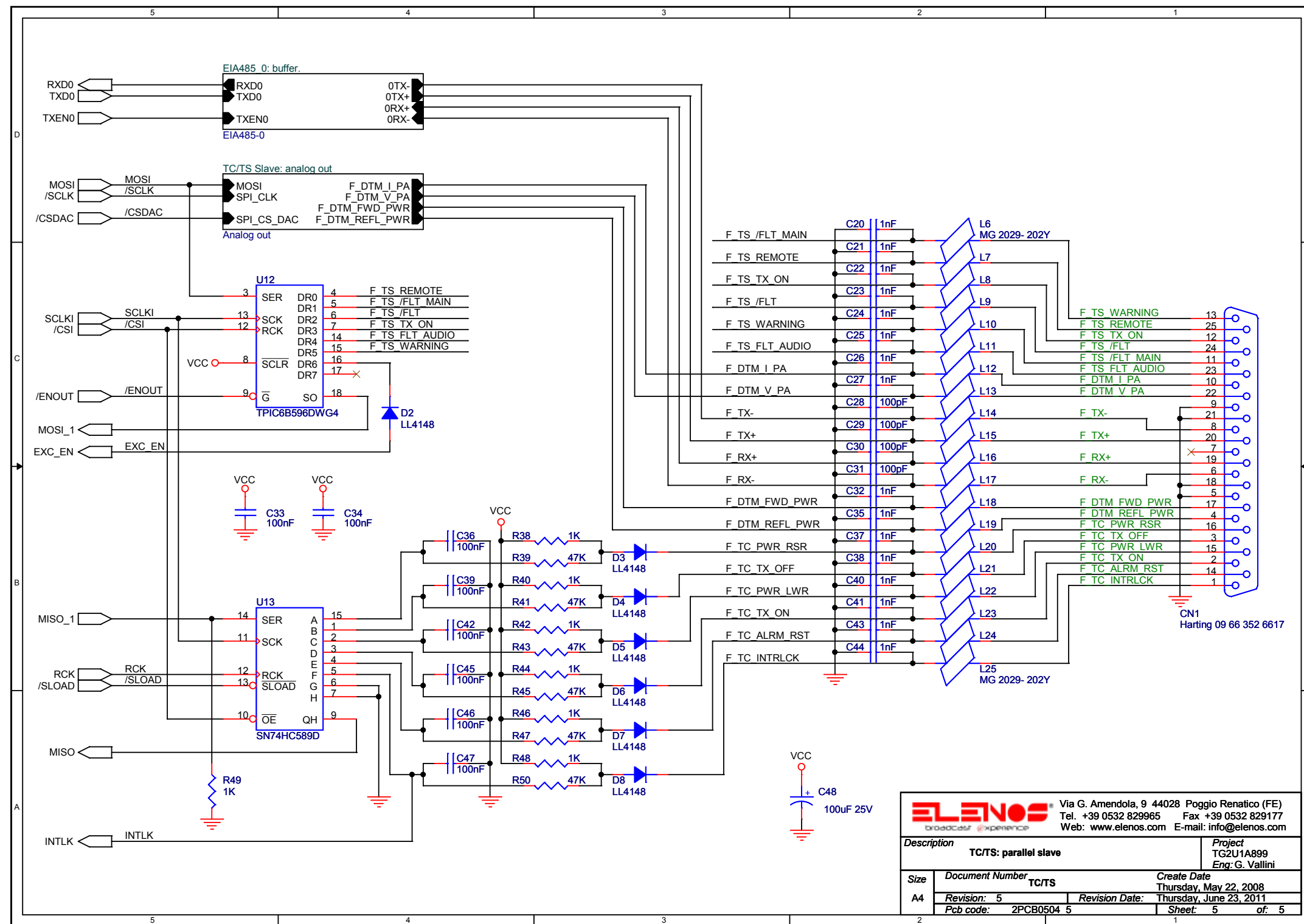
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Description		Project	
TC/TS: analog out		TG2U1A899	
Size		Create Date	
A4		Monday, June 30, 2008	
Revision: 5		Revision Date: Tuesday, March 08, 2011	
Pcb code: 2PCB0504 5		Sheet: 2 of: 5	



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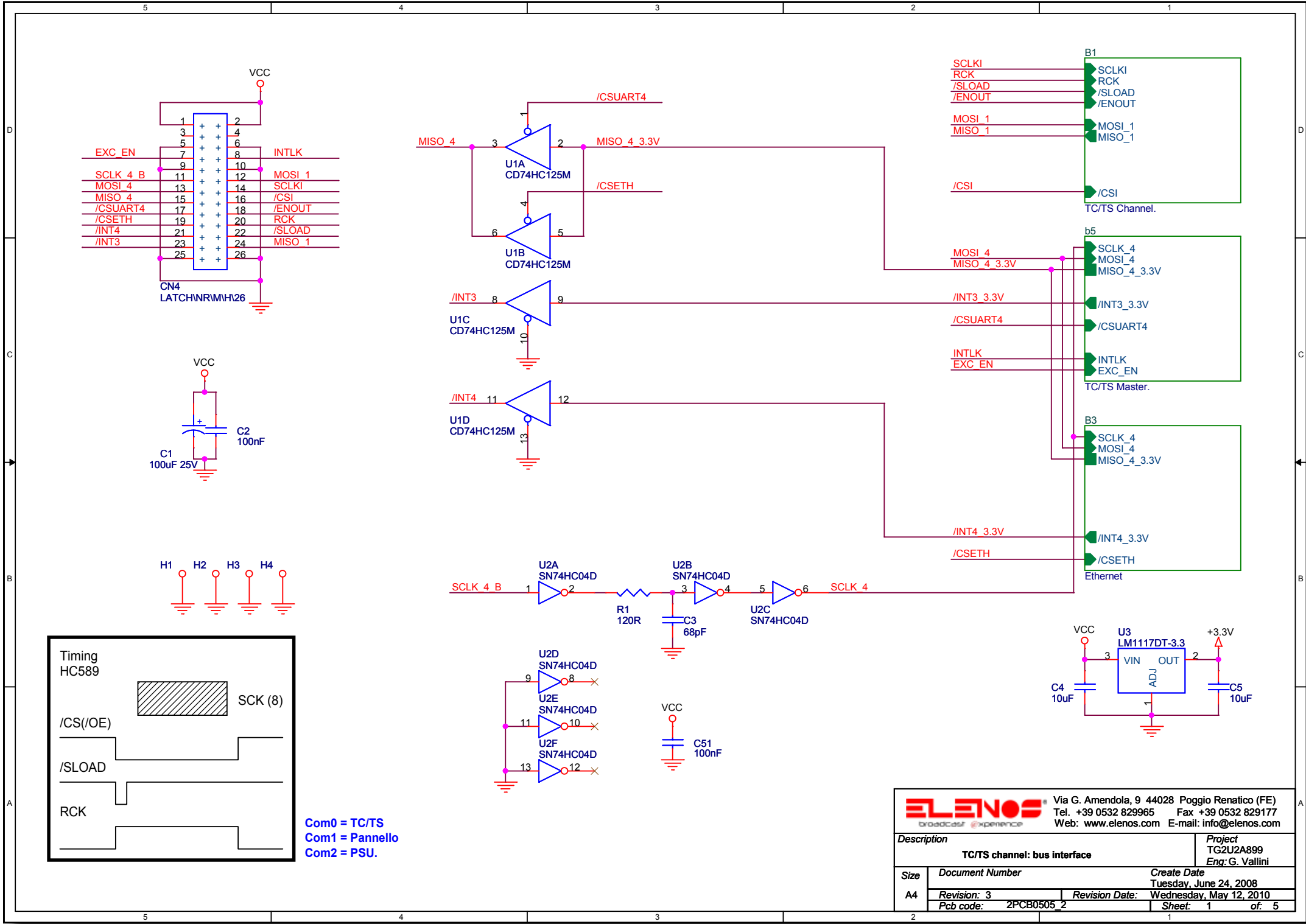
Description		TC/TS: EIA485 buffer (com0)		Project TG2U1A899 Eng: G. Vallini	
Size A4	Document Number		TC/TS		Create Date
	Revision: 5		Revision Date:		Monday, May 11, 2009
	Pcb code: 2PCB0504 5		Sheet: 3		of: 5

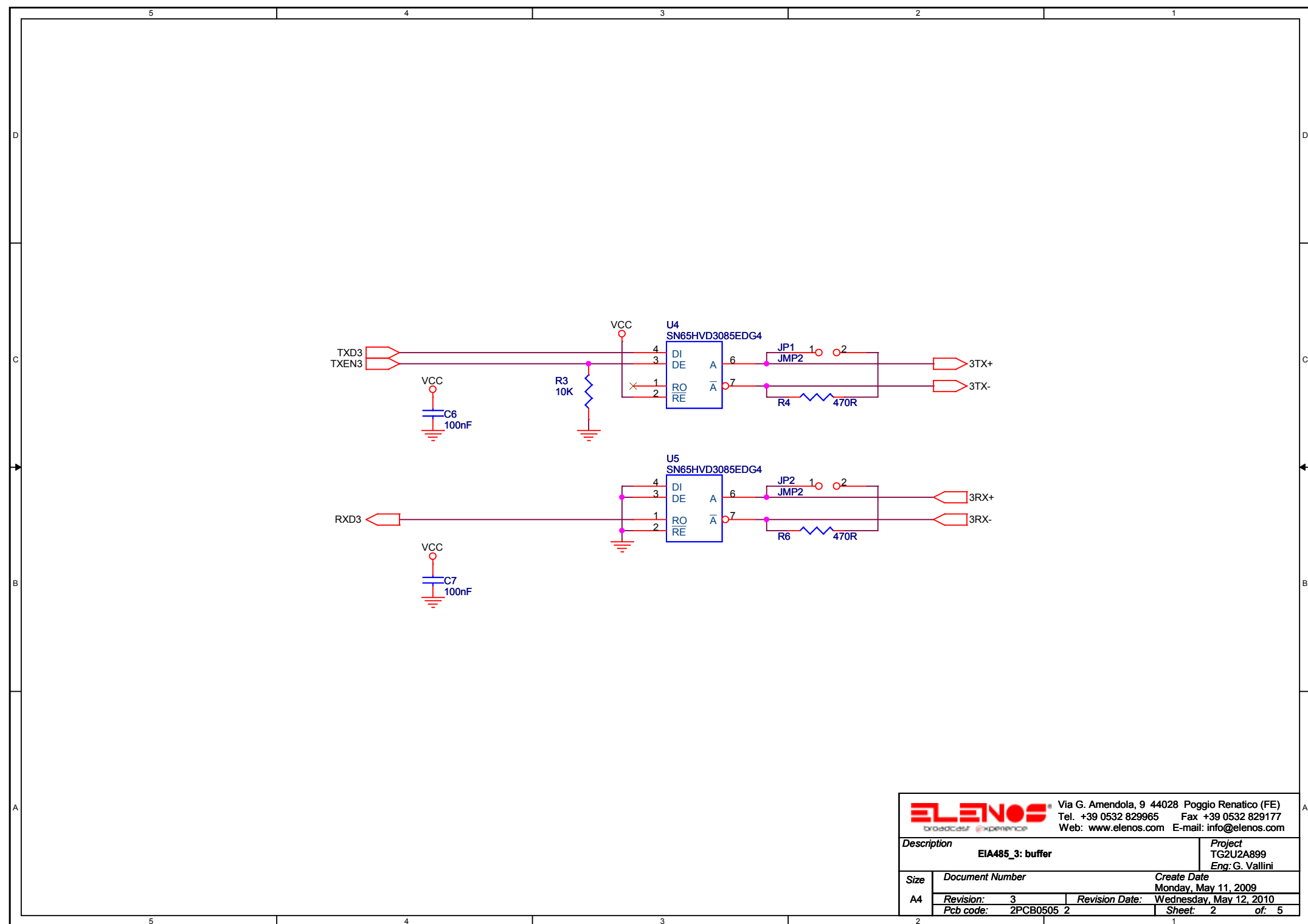




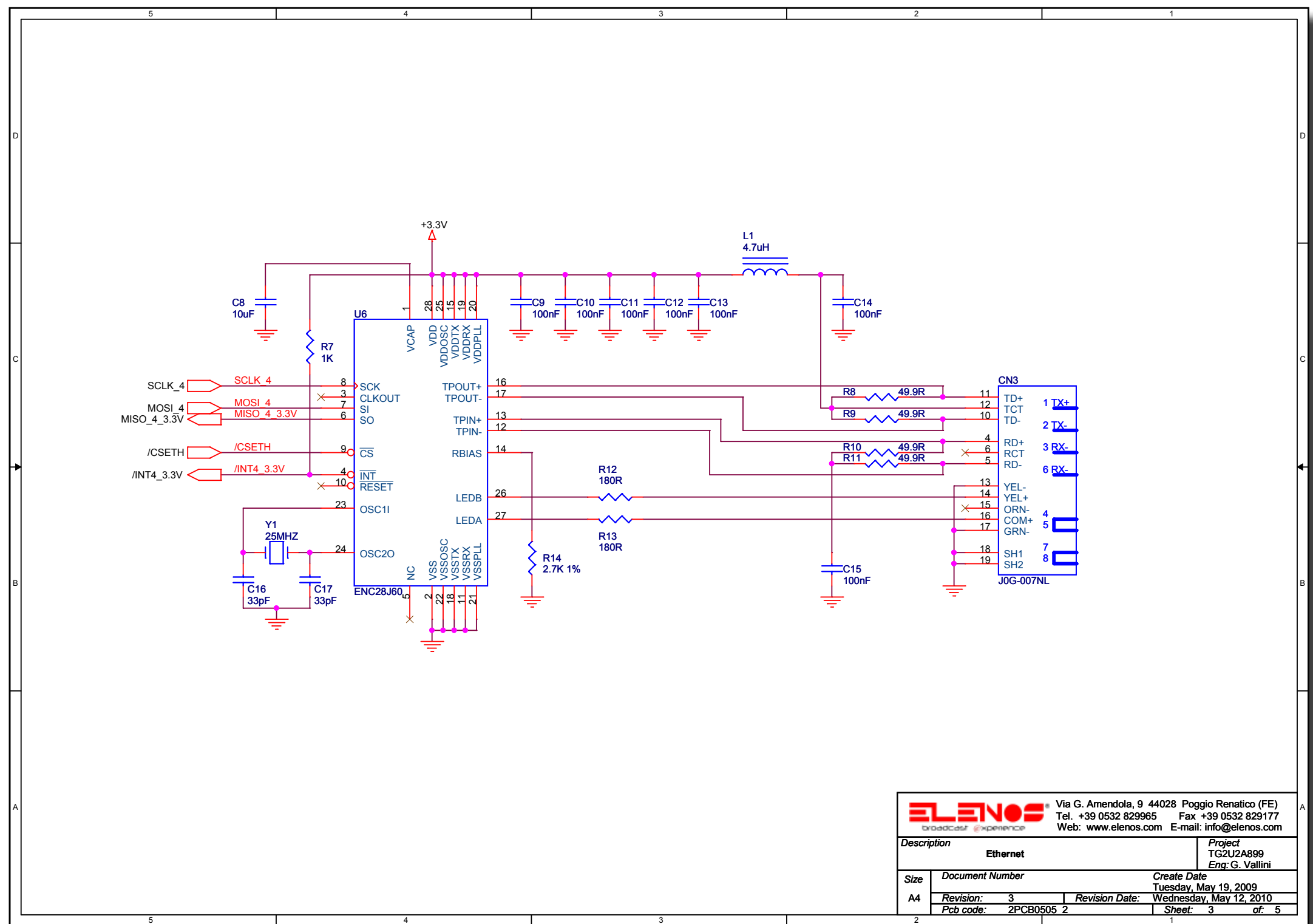
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Description		TC/TS: parallel slave	
Size		Document Number	
A4		Revision: 5	
		Pcb code: 2PCB0504 5	
		Create Date	
		Thursday, May 22, 2008	
		Revision Date:	
		Thursday, June 23, 2011	
		Sheet: 5 of: 5	

CHANNEL BOARD (TG2U2A899_3)





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Description EIA485_3: buffer		Project TG2U2A899 Eng. G. Vallini	
Size A4	Document Number		Create Date Monday, May 11, 2009
	Revision: 3	Revision Date:	Wednesday, May 12, 2010
	Pcb code: 2PCB0505 2	Sheet: 2	of: 5



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Description		Ethernet		Project TG2U2A899 Eng: G. Vallini	
Size	Document Number	Create Date		Tuesday, May 19, 2009	
A4	Revision: 3	Revision Date:		Wednesday, May 12, 2010	
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