

RF DATA CORP.
FCC ID: OFMRFD-8-ISM
EXHIBIT #: 10

RF DATA CORP.
'Sensor Mate' RADIO MODEM BLOCK DIAGRAM

The modem consists of several separate blocks. The controller is responsible for all interfacing and receives and responds to all incoming events.

The controller executes a multitasking kernel which resides in internal FLASH memory and which can be upgraded in-circuit using the auxiliary SPI bus interface connection. The controller powers up and down the receiver sections, transmitter sections, the timebase and power management.

The RF modem block consists of combination digital and analog circuitry which performs the data formatting and data retrieval functions. This circuitry also controls forward error correction, which corrects all 1 bit per byte errors and detects 99.999% of all other errors. This section also performs the GMSK filtering and data rate generation for the RF section.

