

EXHIBIT 5
BLOCK DIAGRAM & CIRCUIT DESCRIPTION

ONE (1) BLOCK DIAGRAM FOLLOWS THIS SHEET

BLOCK DIAGRAM
FCC ID: OHRTX323

EXHIBIT 5

TX323 BLOCK DIAGRAM

FCC ID: OHRTX323

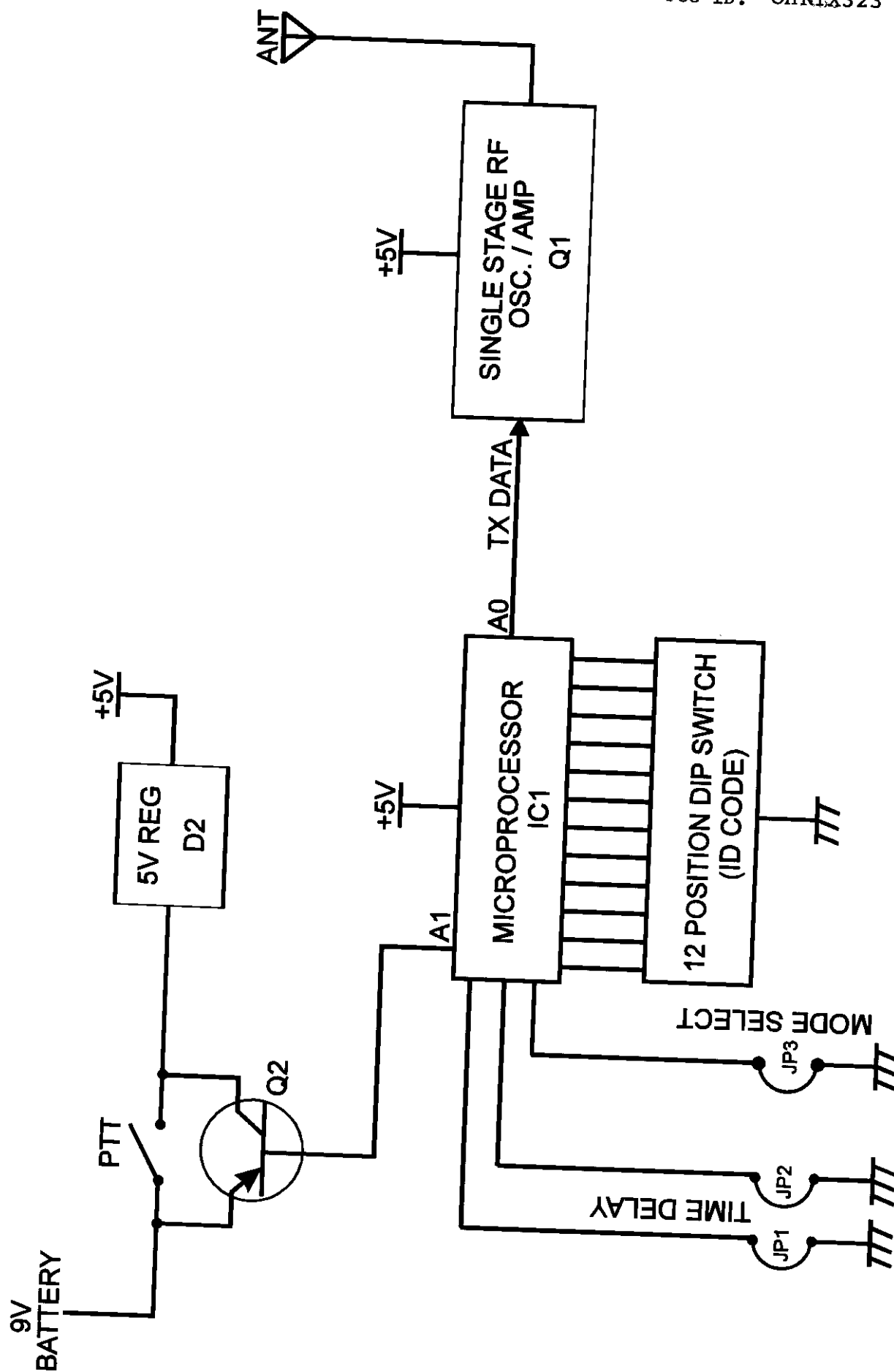


EXHIBIT 6

CIRCUIT DESCRIPTION

The TX323 remote control transmitter basically consists of an RF oscillator section that is driven and controlled by a single chip microprocessor (see block diagram).

The RF oscillator/amp section is a simple one stage element composed of transistor Q1 and associated "L" and "C" components (see schematic). Its operating frequency is 389 MHz.

Microprocessor IC1, upon power up, reads a number of jumpers and switches to determine its proper function and what ID code data is to be sent. Once this has been determined, the single stage RF section is then AM modulated with data. The data is formatted such that a logical "0" is 20 KHz and a logical "1" is 10 KHz.

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EXHIBIT 6