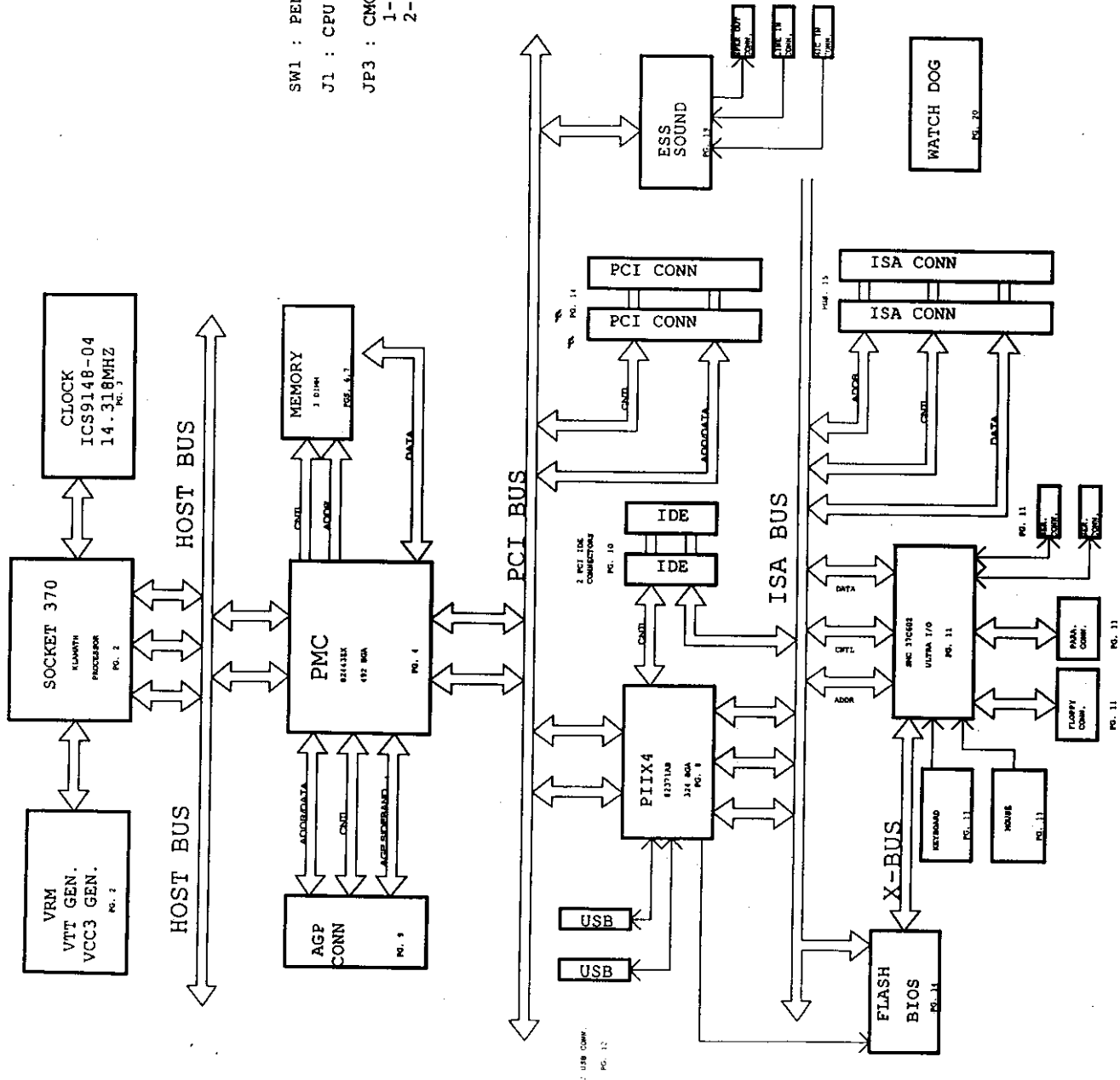




SW1 : PENTIUM-II CPU SPEED SETTING
 J1 : CPU FAN POWER CON.
 JP3 : CMOS DATA CLEAR
 1-2 : DEFAULT CLOSE
 2-3 : CLEAR CMOS DATA

MV67S/MV67S

Ver.0.9

ATX

3DIMM

SUPPORTS 1 AGP SLOT

SUPPORTS 4 PCI SLOTS

SUPPORTS 2 ISA SLOTS

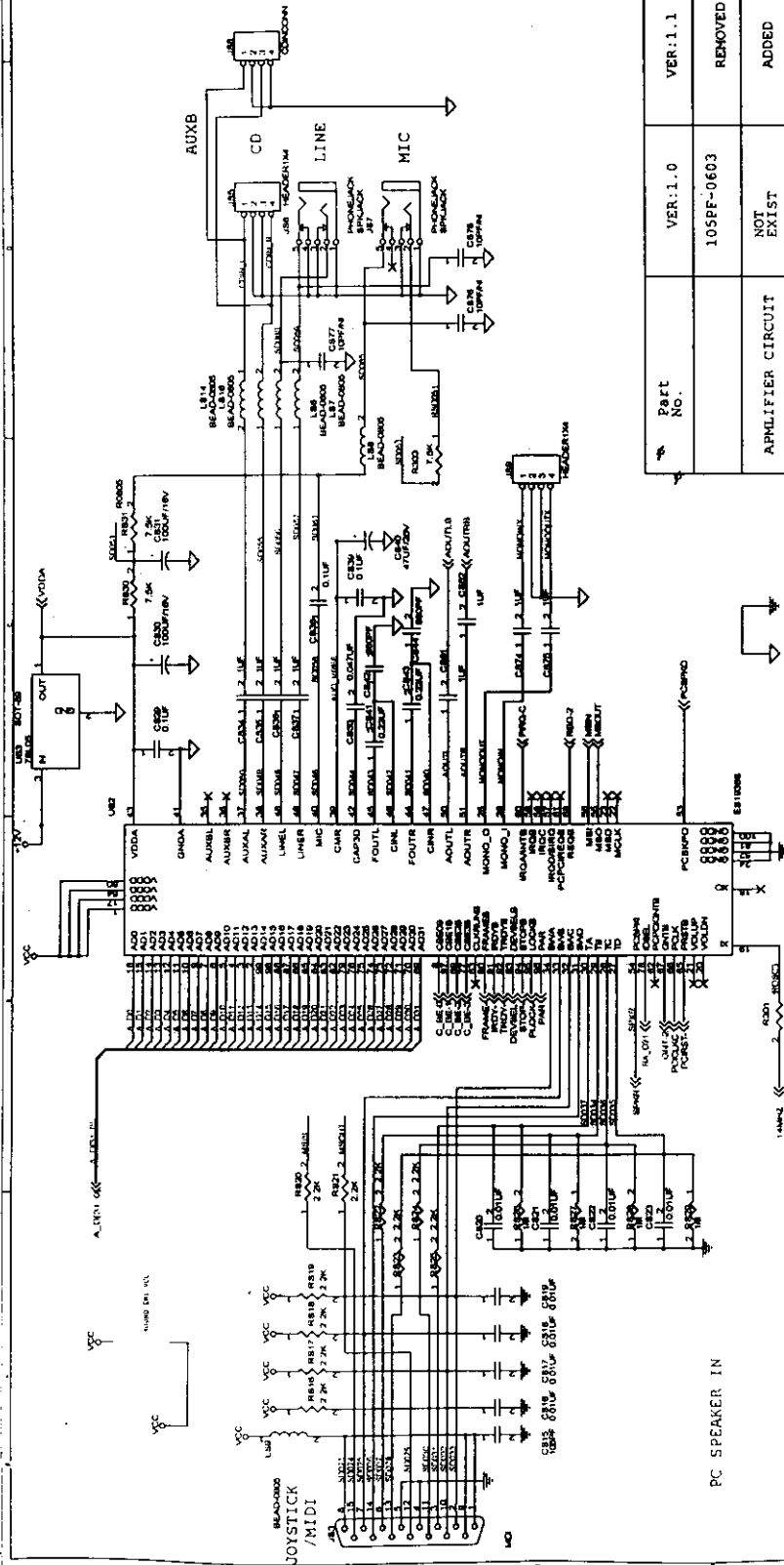
MEC

ZVNA-ESS BASED M8

Document Number MEC M80

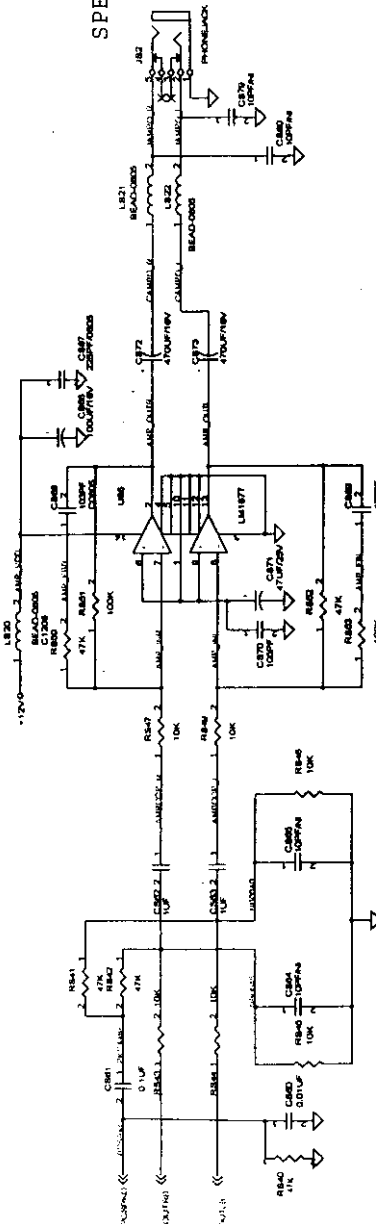
Rev. 0.9

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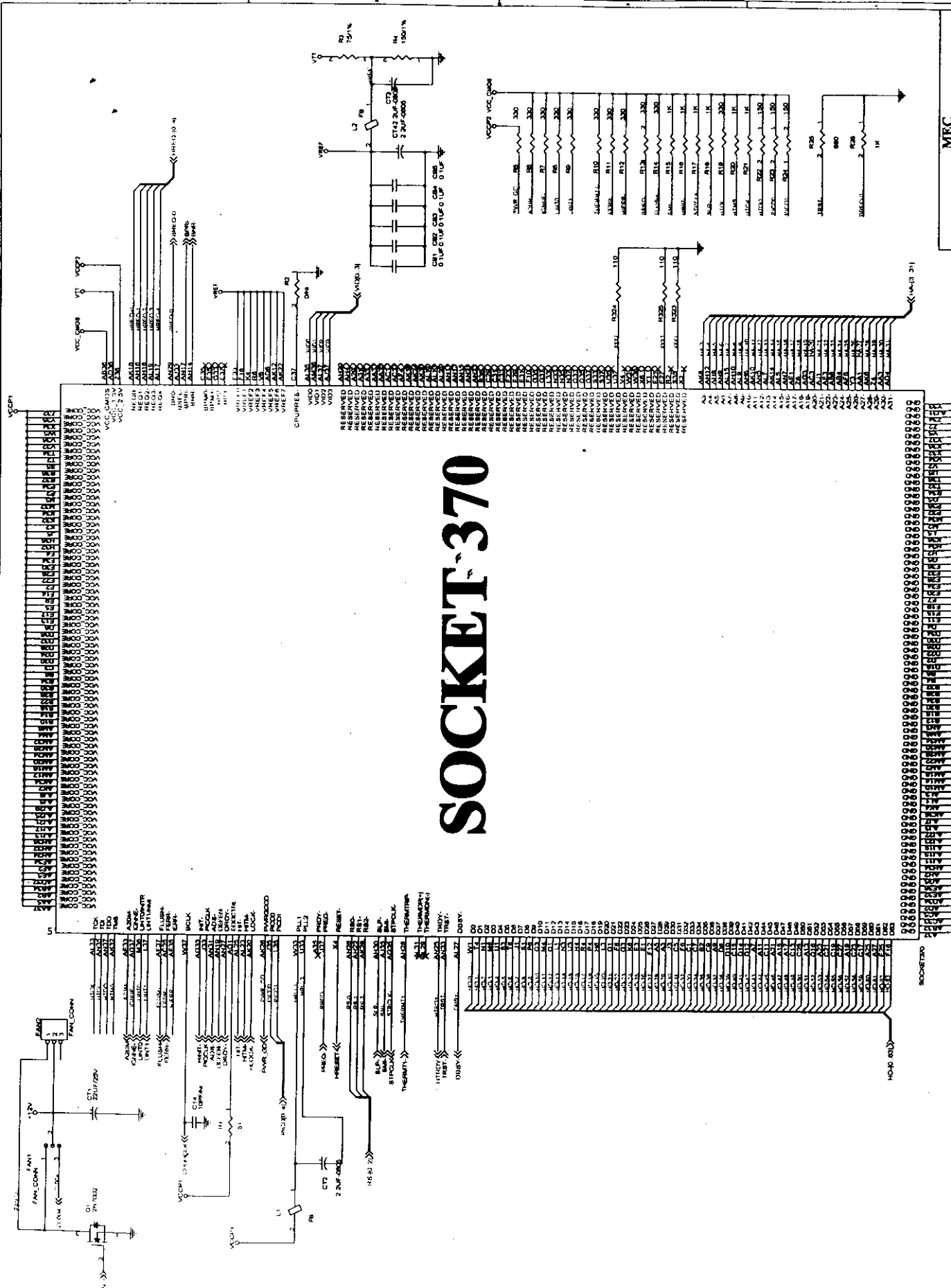


Part No.	VER:1.0	VER:1.1
	105PF-0603	REMOVED
	AMPLIFIER CIRCUIT	NOT EXIST
		ADDED

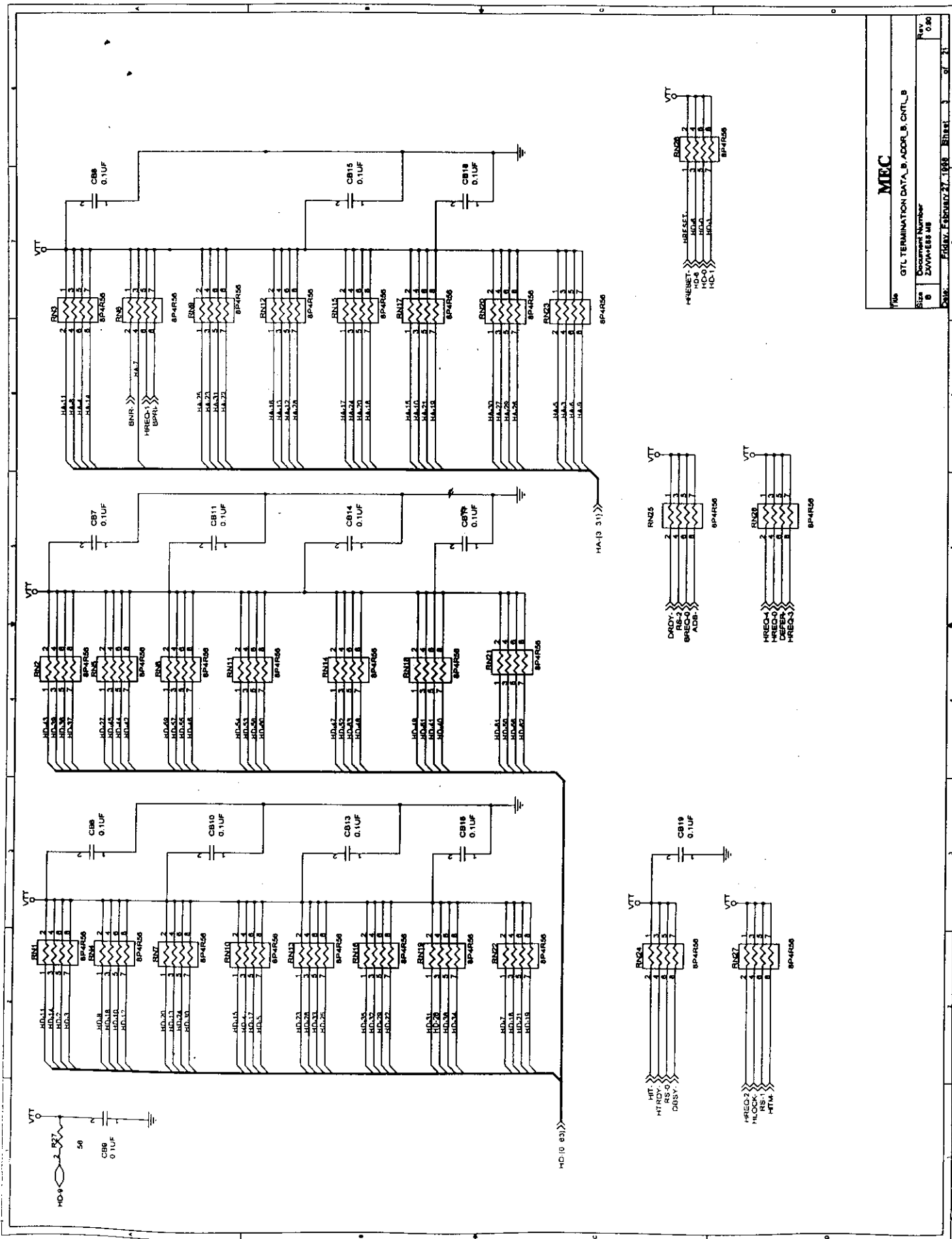
SPEAKERS

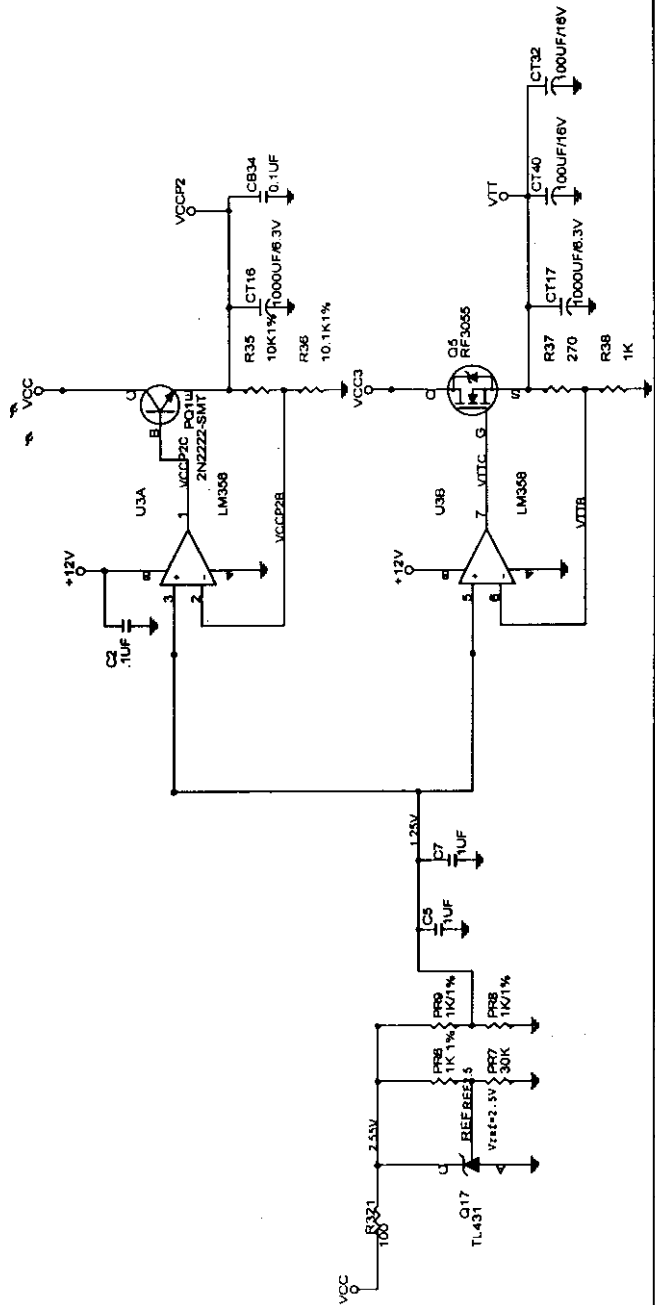
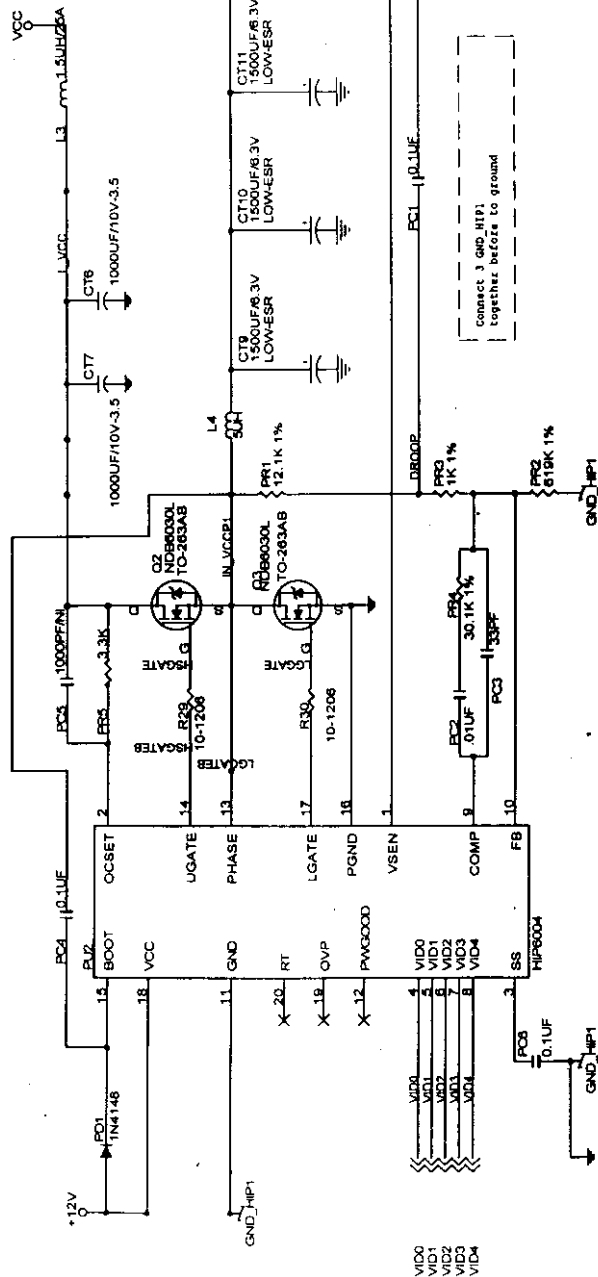


SOCKET-370



SOCKET 370
MEC





MEC

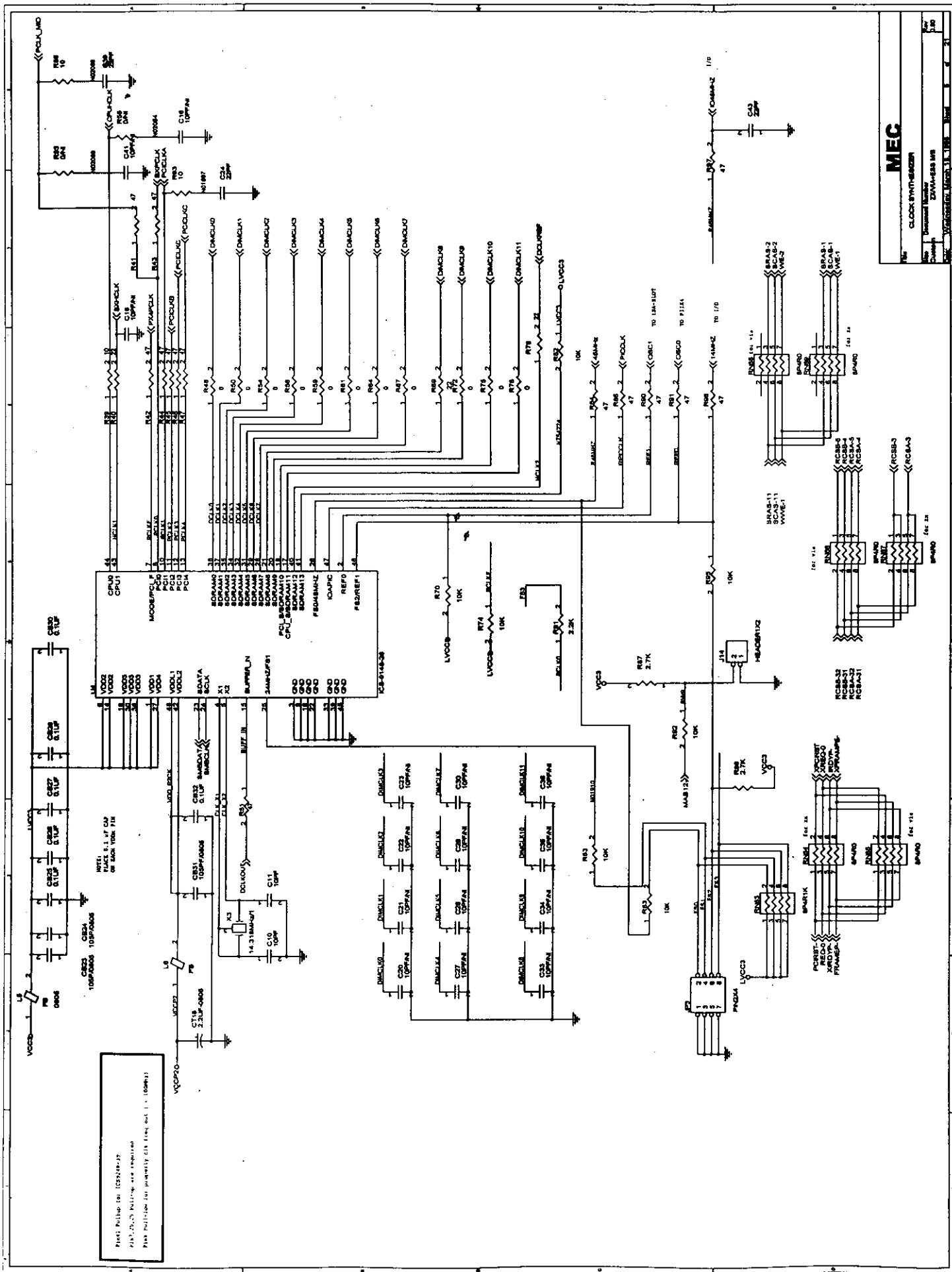
DC to DC CONVERT

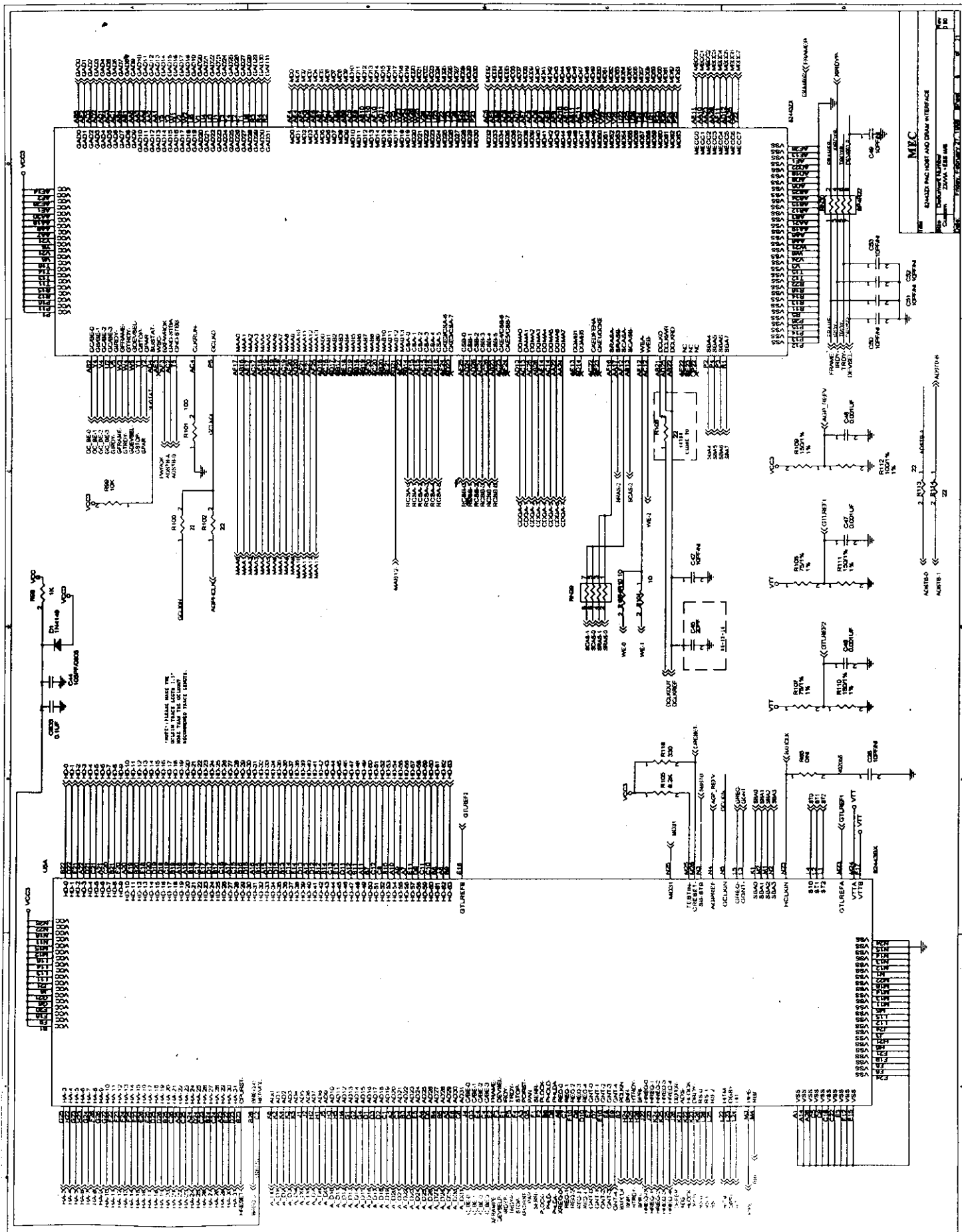
Document Number
Z000A-ESS M6

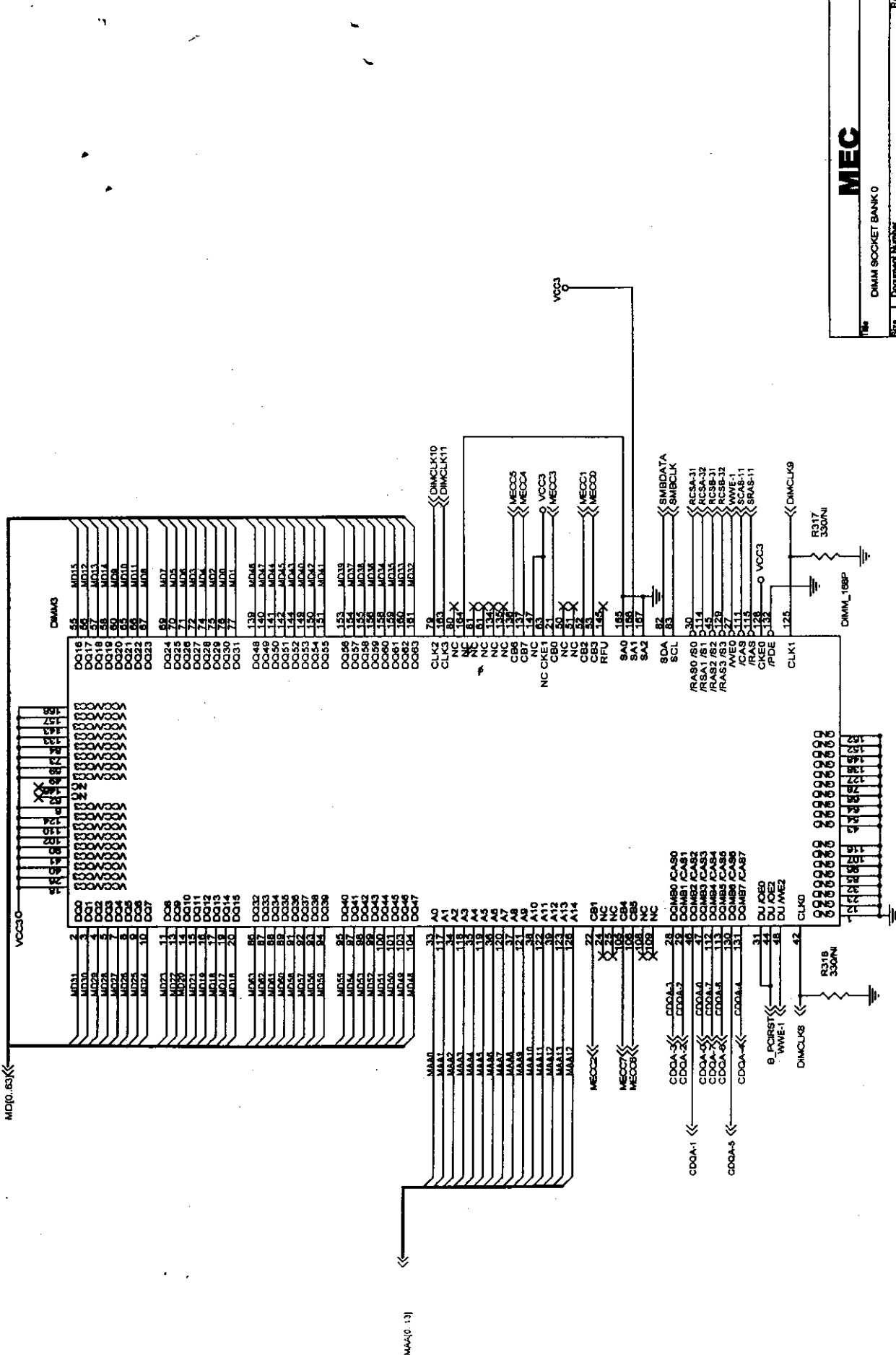
Size
B

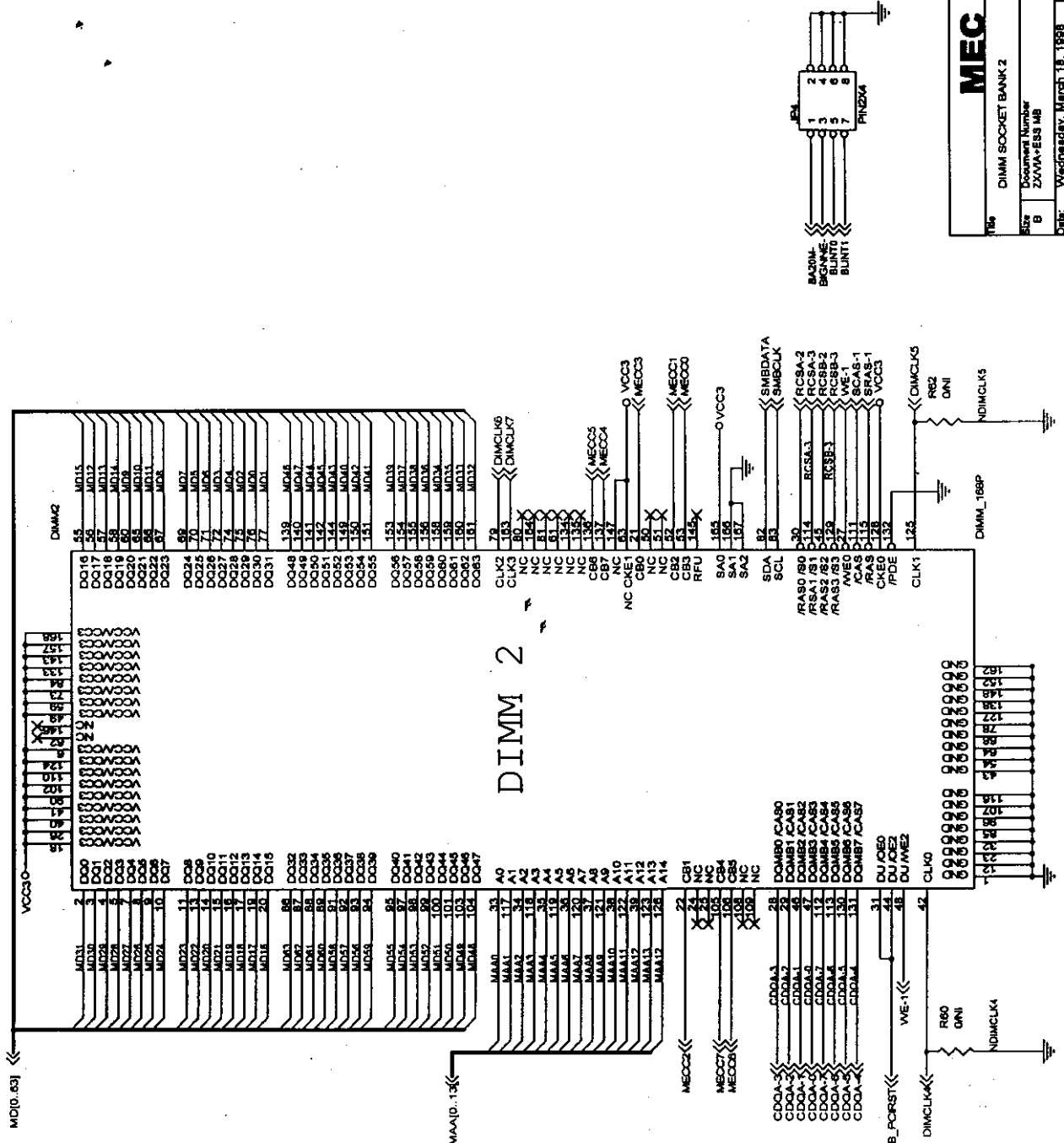
Date
Wednesday, March 18, 1998

Page
4 of 21







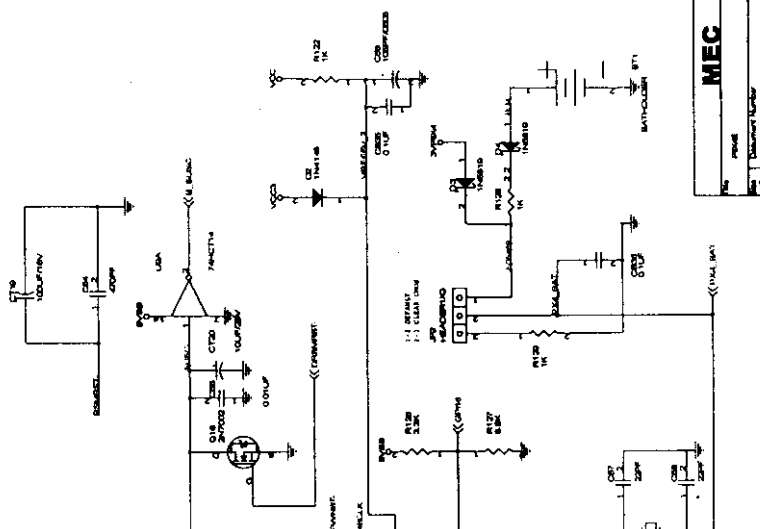
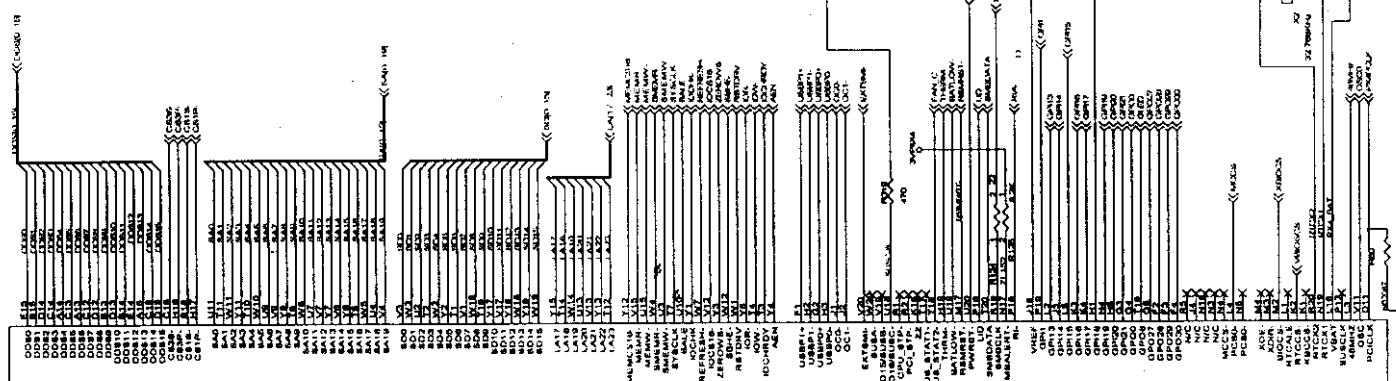


MEC

DIMM SOCKET BANK 2

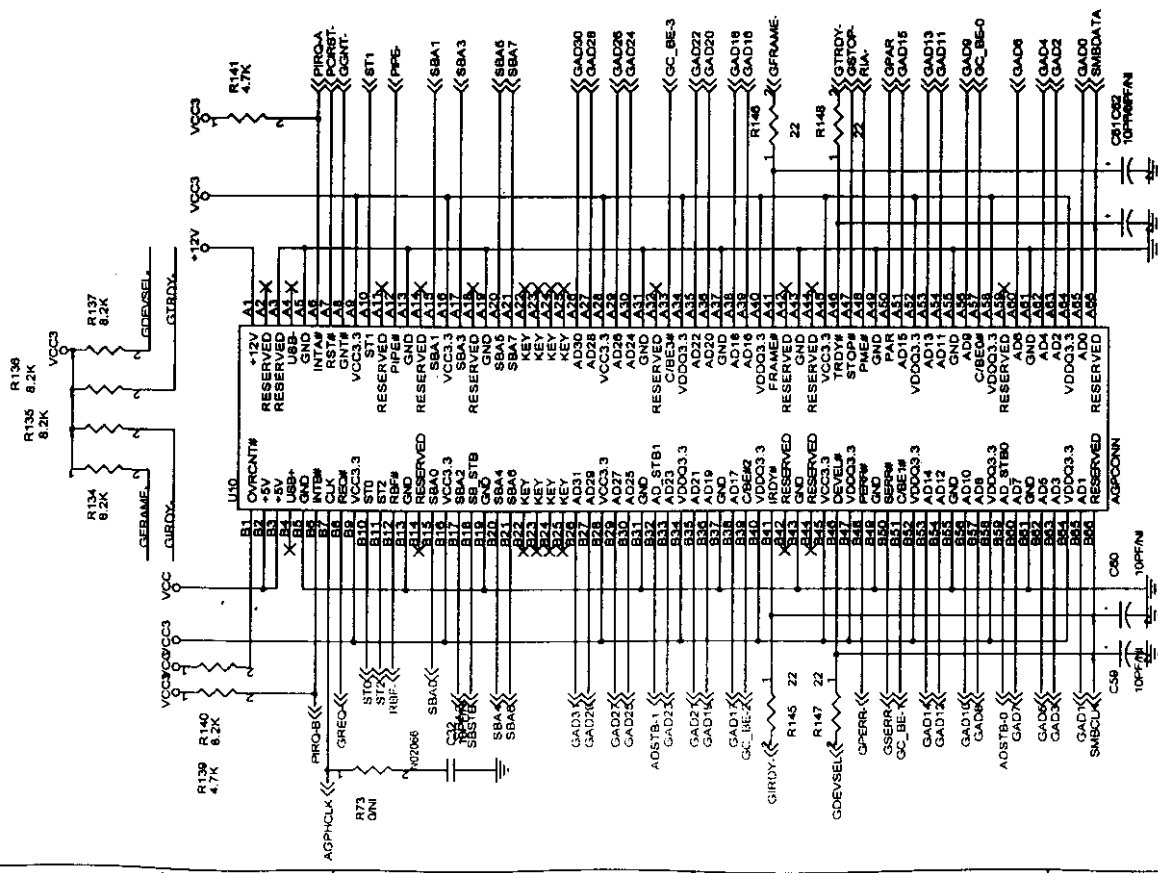
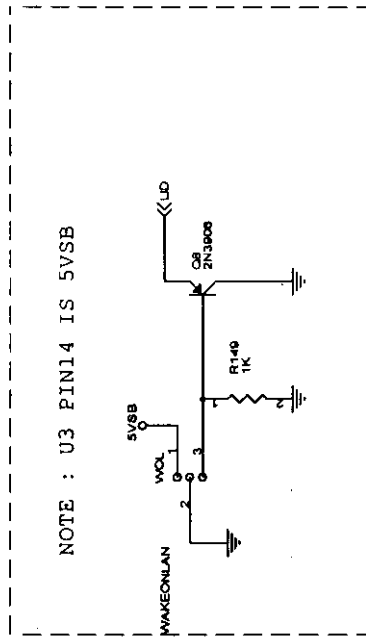
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2200A-ESS MB

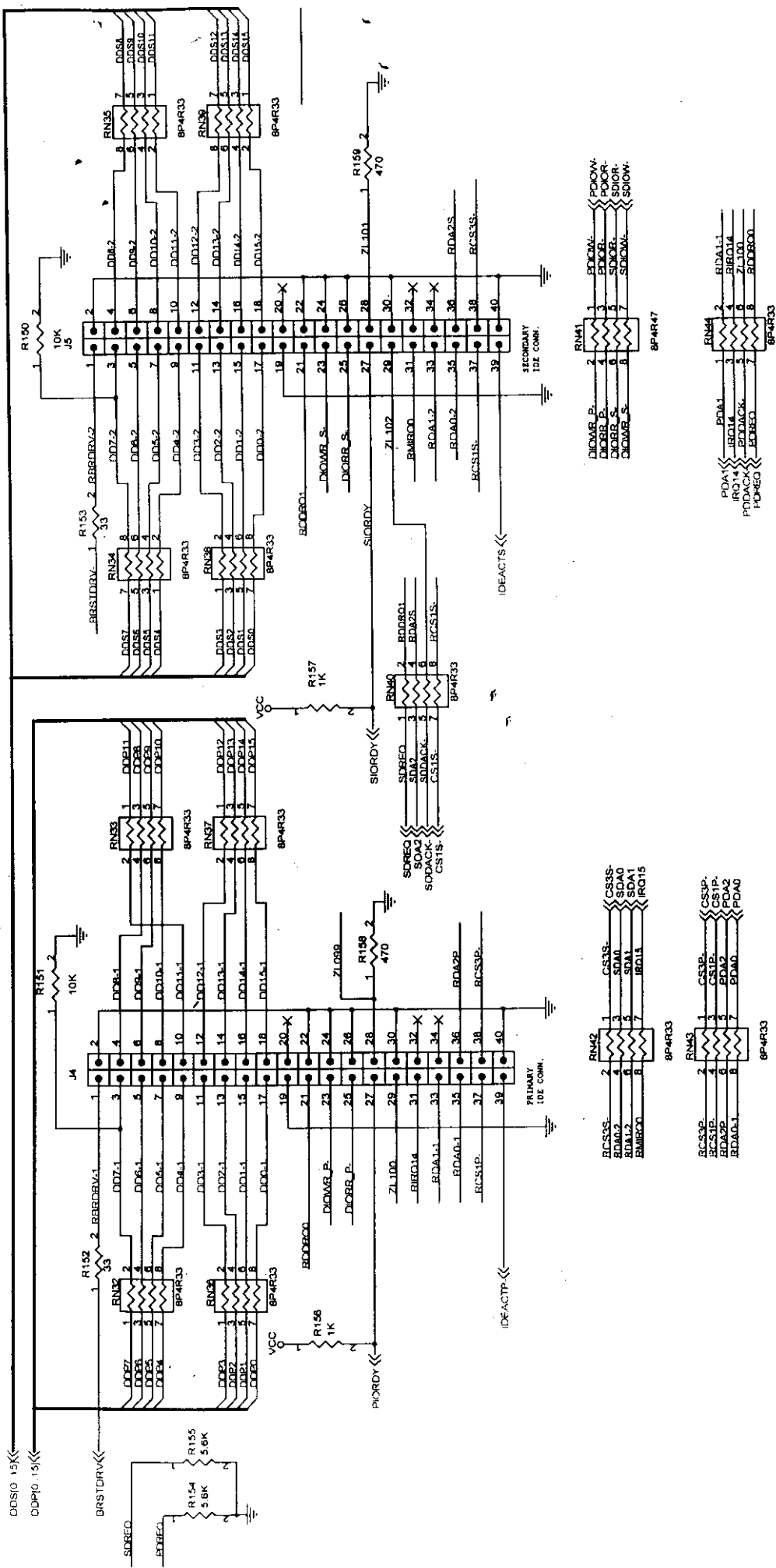
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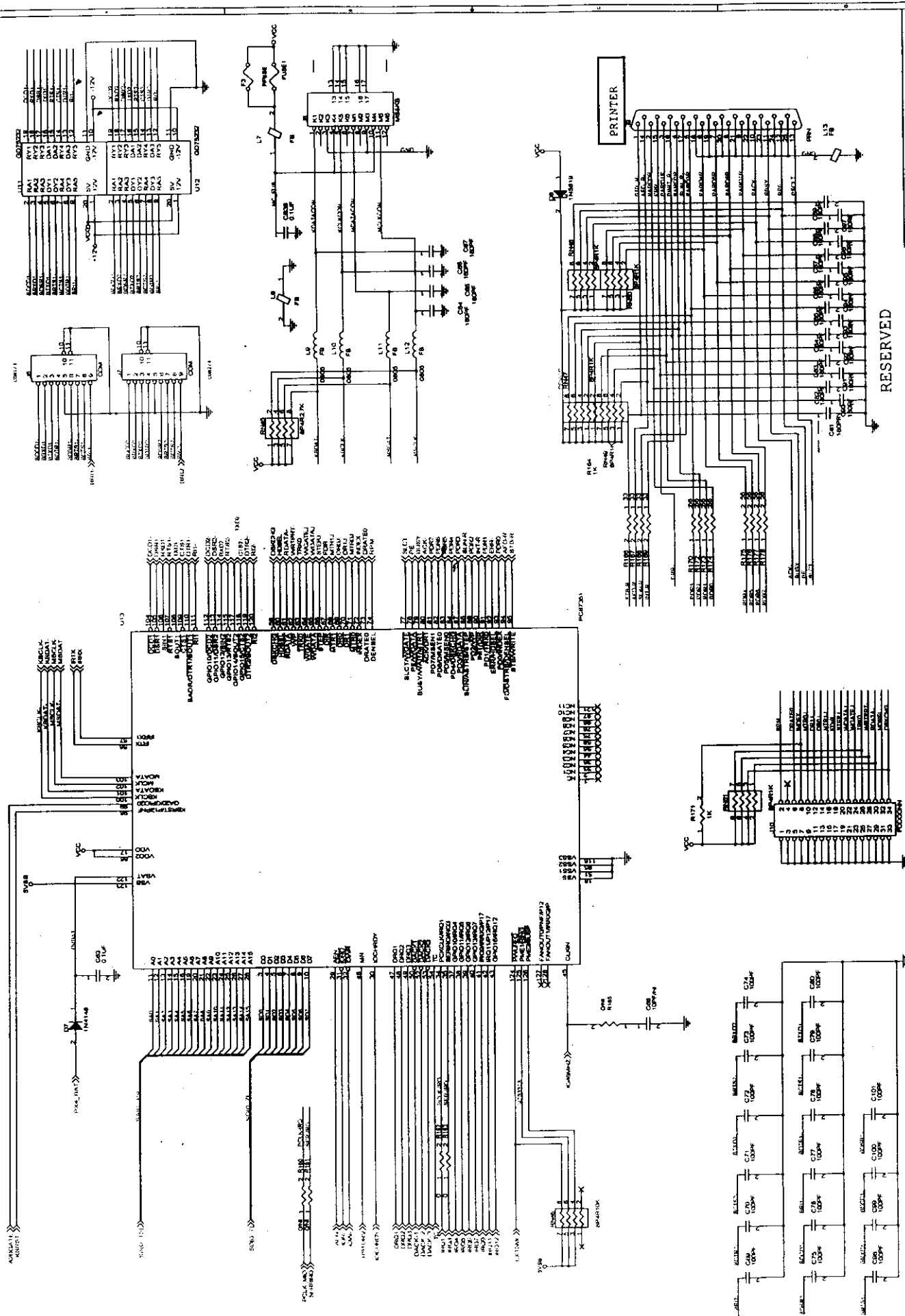
Date:	Wednesday, March 15, 1995	Sheet	10	of	21
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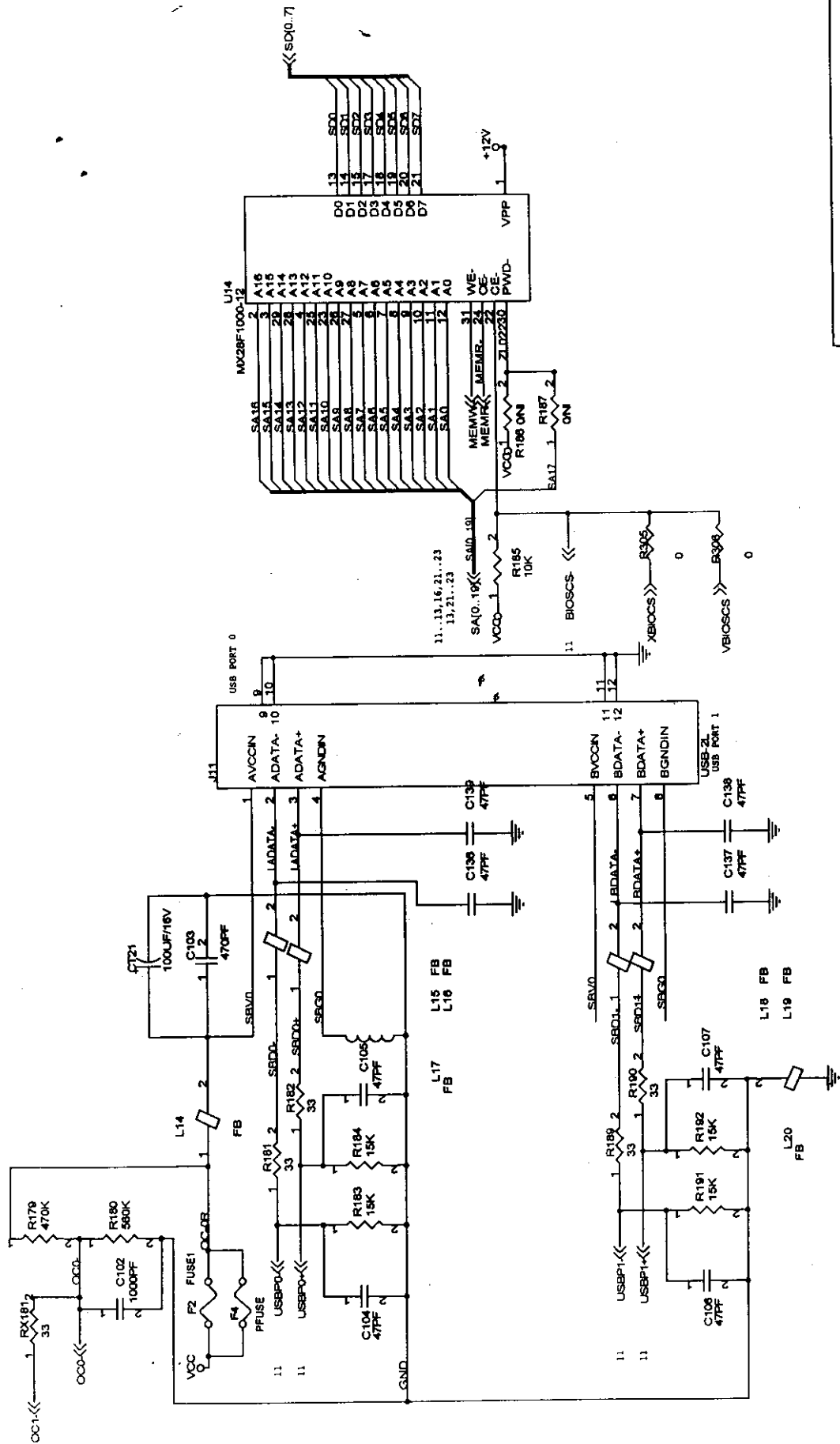




RESERVED

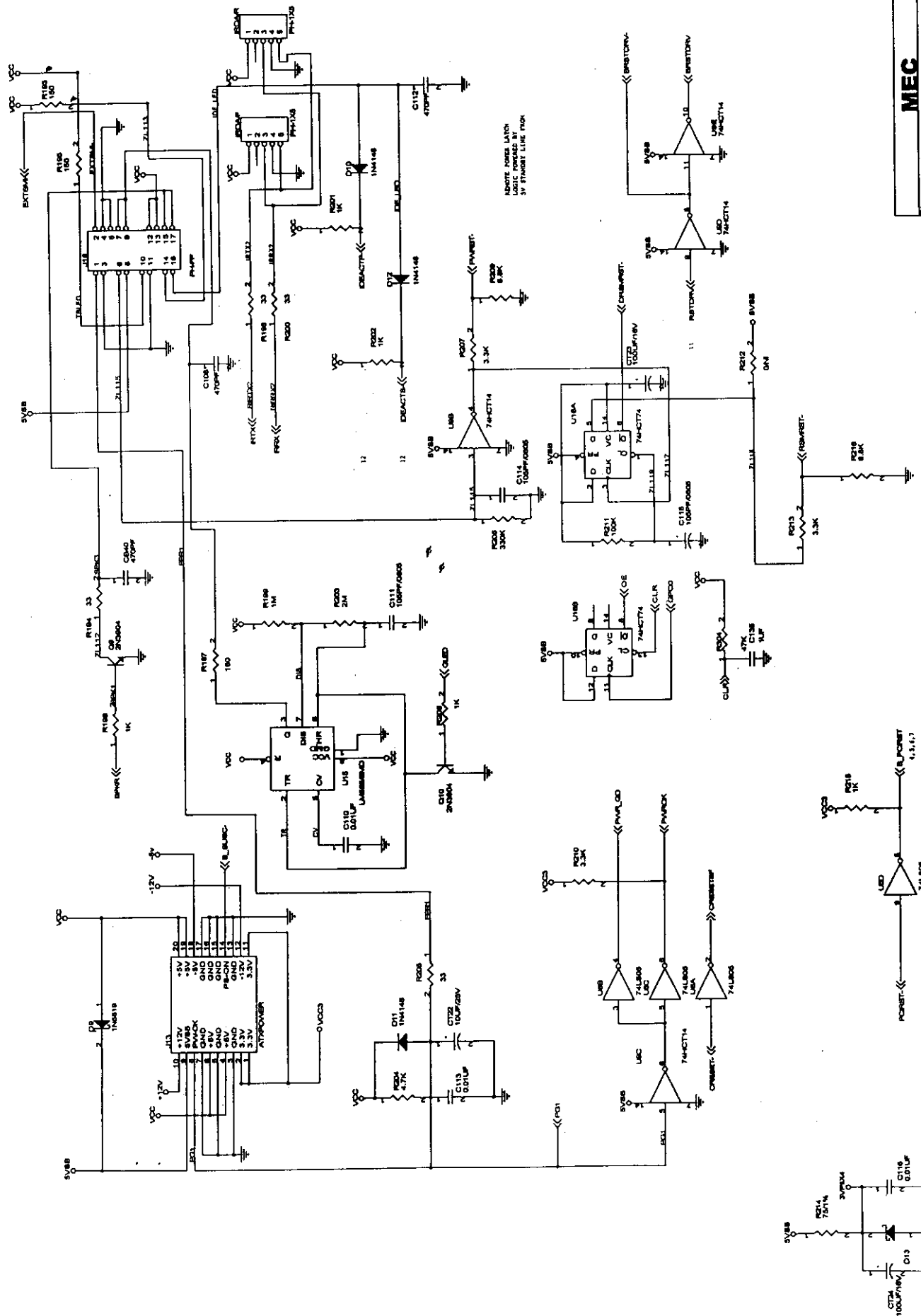
PRINTER





MEC

USB PORT INTERFACES & BIOS
 Document Number
 ZVVA-ESS M8
 Custom
 Date: Wednesday, March 18, 1998
 Sheet 14 of 21

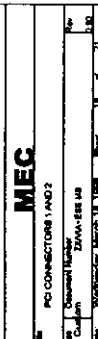


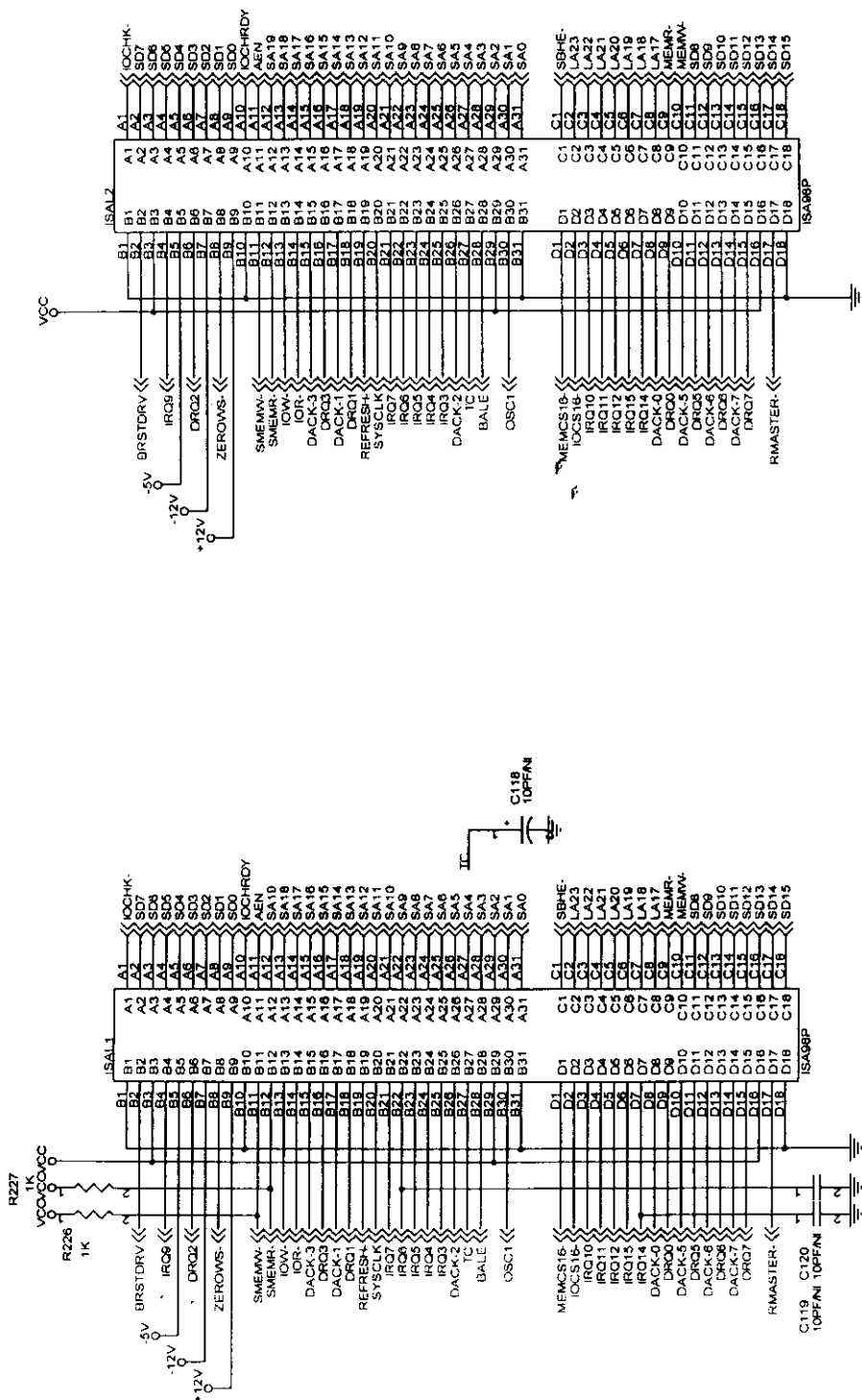
REMOTE NOISE LATCH
LOGIC FORCED BY
3V STANLEY LINE PWR

MEC

POWER POINT CONNECTIONS

Document Number
2000-100-100
Rev. 1.0
Date: 10/10/00





MEC

ISA BUS SLOTS 1.2

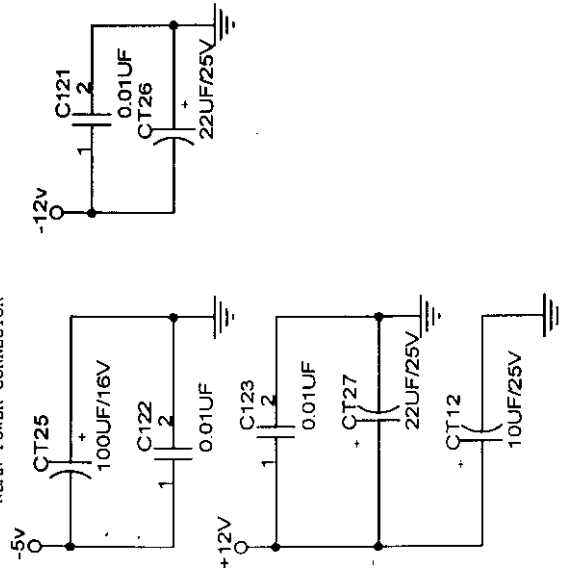
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ZXMA-ESS MB

Date: Wednesday, March 18, 1998

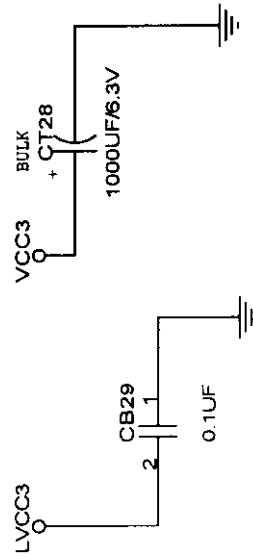
Sheet 17 of 21

BULK DECOUPLING

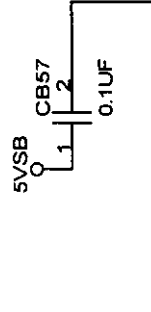
NEAR POWER CONNECTOR



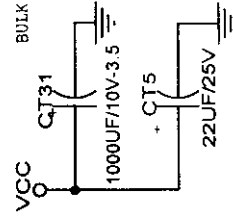
3 VOLT DECOUPLING



STANDBY 5 VOLT DECOUPLING



5 VOLT DECOUPLING



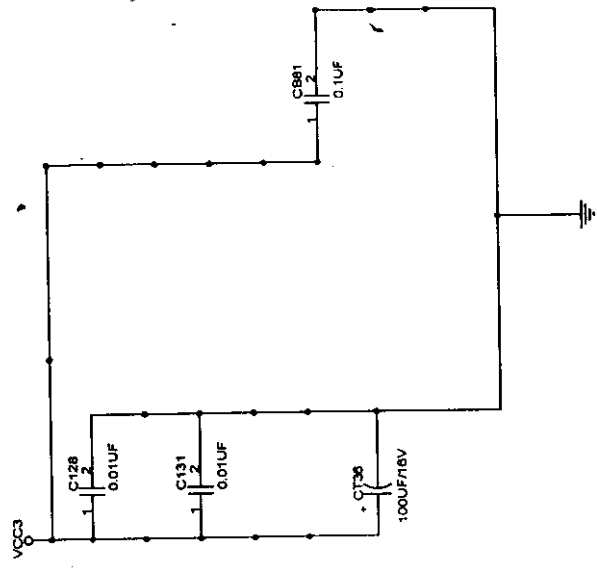
MEC

3.3 VOLT AND BULK POWER DECOUPLING

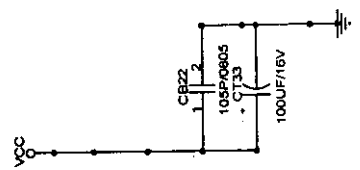
Size Custom
Document Number ZWVIA+ESS MB

Date: Wednesday, March 18, 1998 Sheet 19 of 21

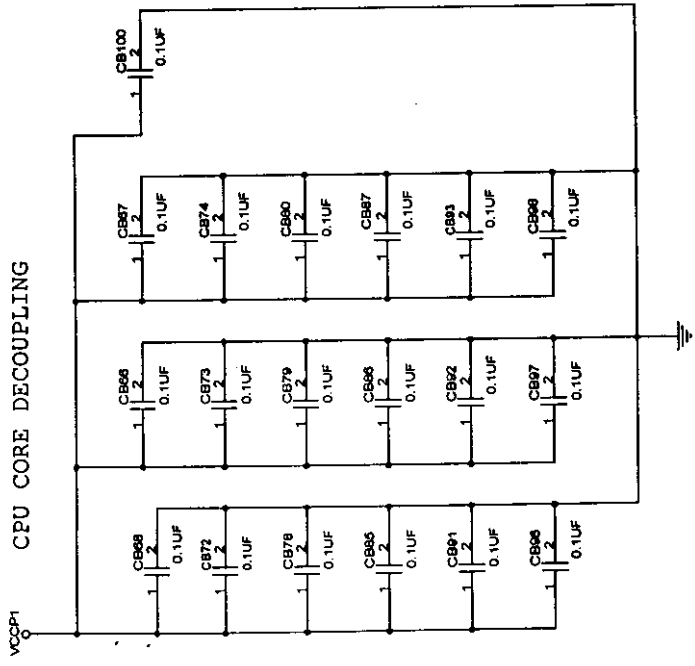
CPU L2 (3.3 V) DECOUPLING



TERMINATION VOLTAGE DECOUPLING



CPU CORE DECOUPLING



MEC

CPU DECOUPLING CAPACITORS

Docu# 1000

ZVMA+ESS MB

Wednesday, March 18, 1998 Sheet 20 of 21



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WACHSBERG, M.