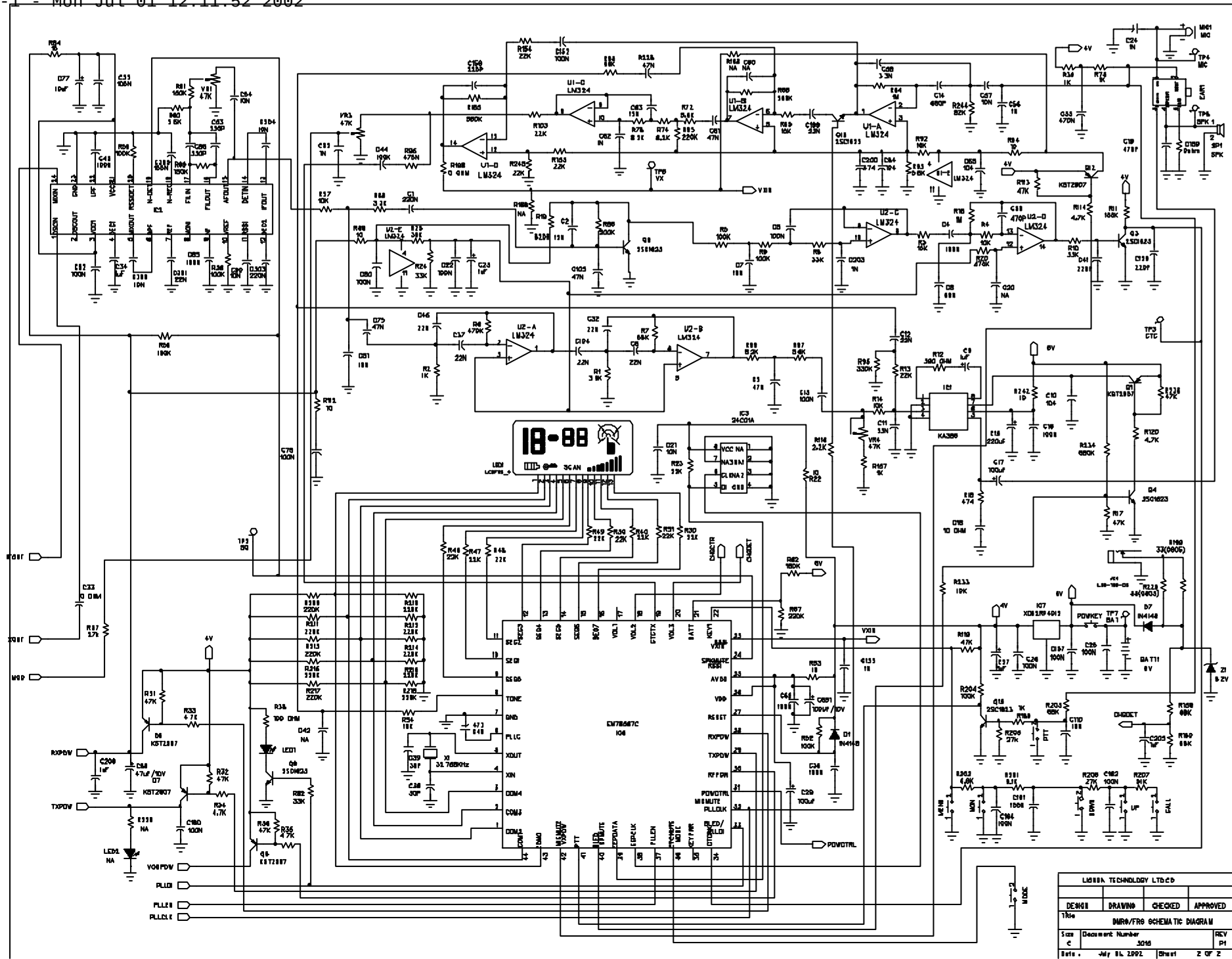




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DMM0/FRS SCHEMATIC DIAGRAM			
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