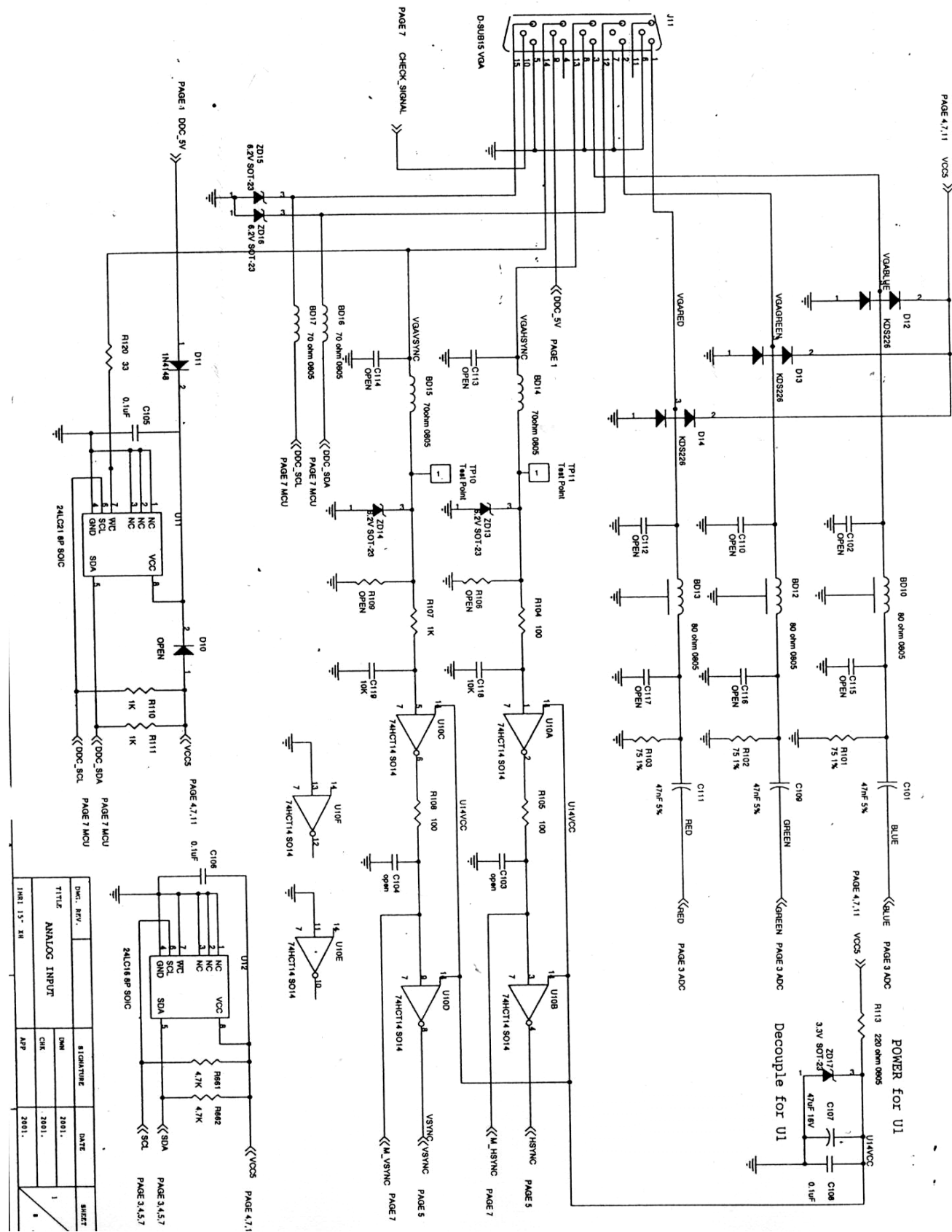
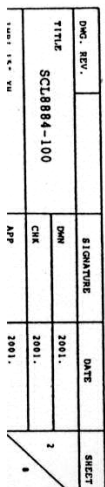
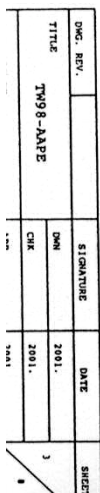
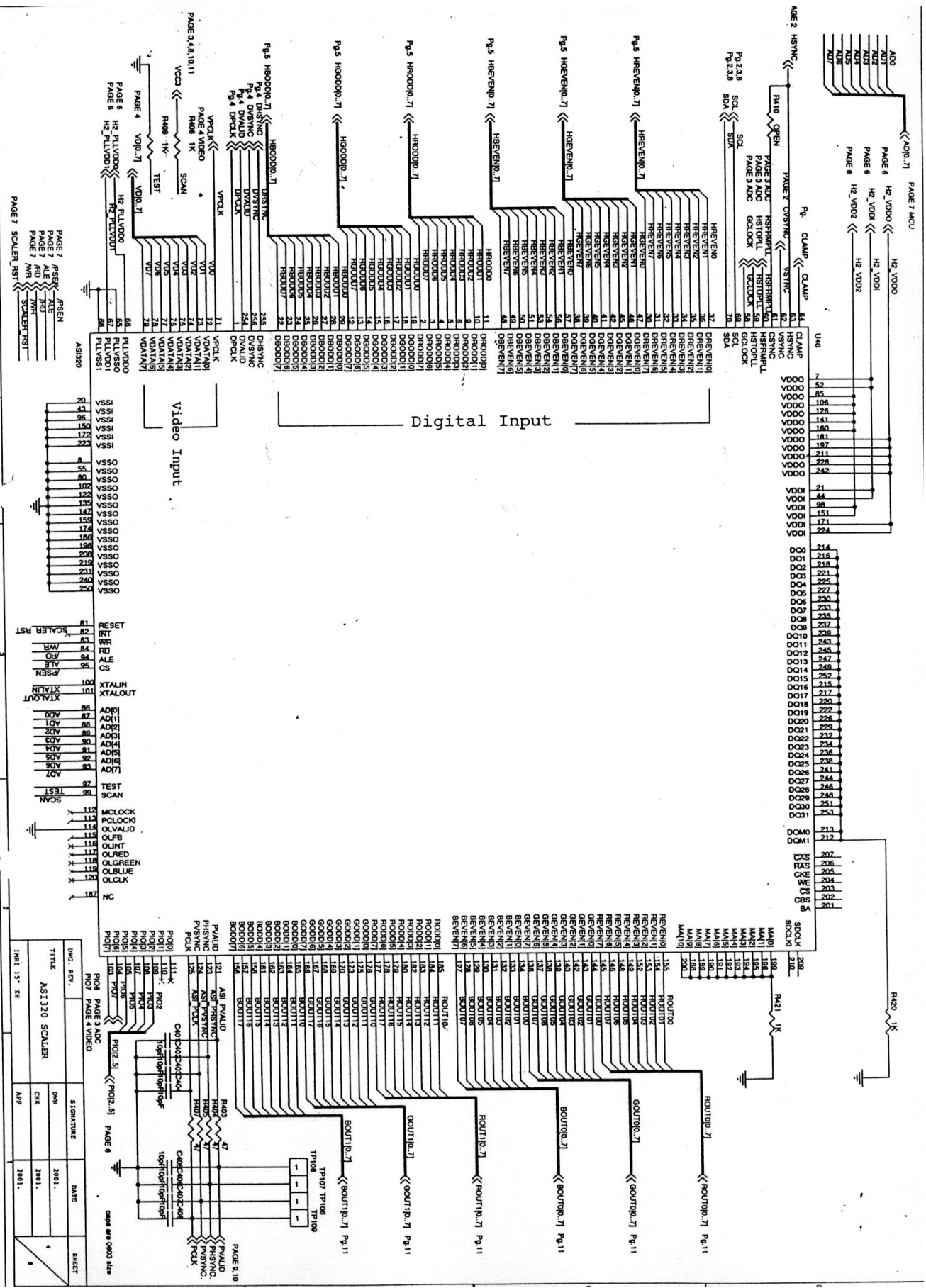




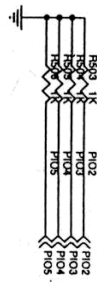
DES. REV.	DATE	SHEET
1	2001.	1
ANALOG INPUT	CHK	2001.
INEL 15' - 48	APP	2001.





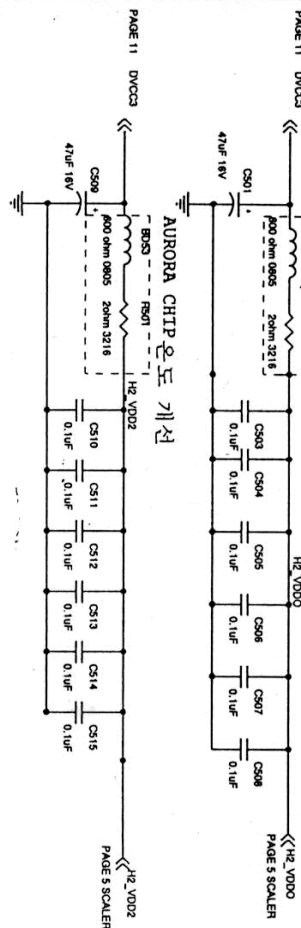


REV.	DATE	SHEET
1.0	2001.	1
2.0	2001.	2
3.0	2001.	3
4.0	2001.	4
5.0	2001.	5
6.0	2001.	6
7.0	2001.	7
8.0	2001.	8
9.0	2001.	9
10.0	2001.	10

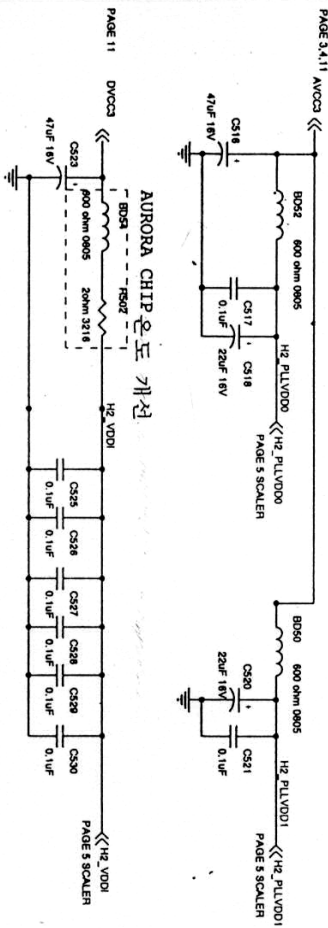


PAGE 9 SCALER

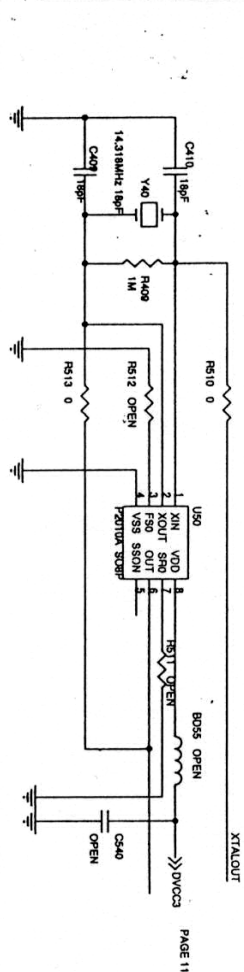
AURORA CHIP 온도 개선



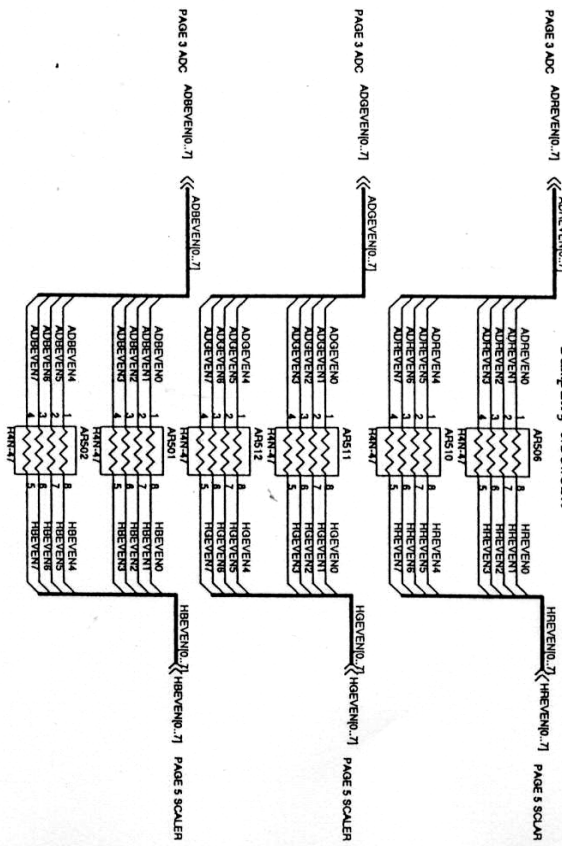
AURORA CHIP 온도 개선



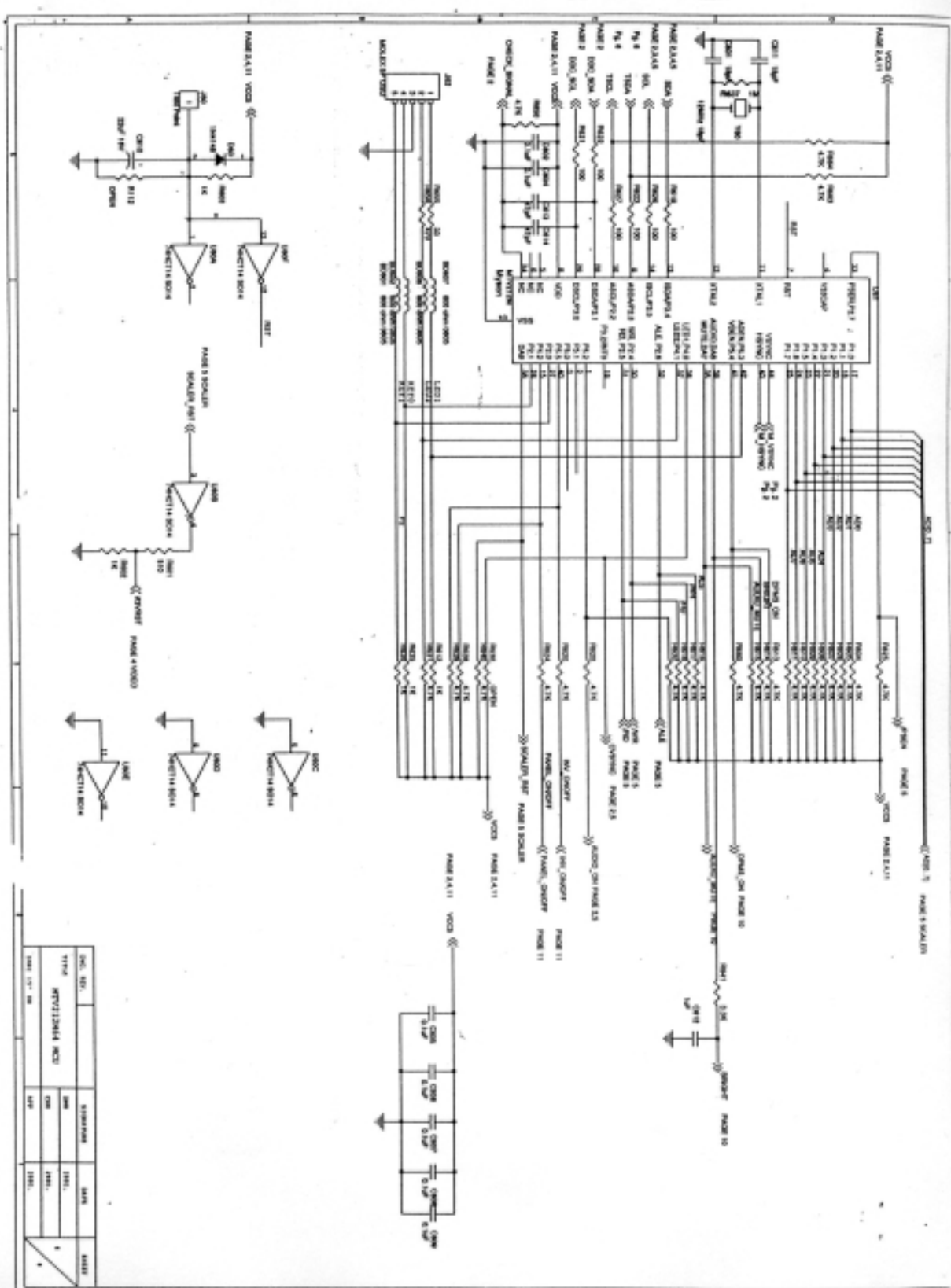
AURORA CHIP 온도 개선



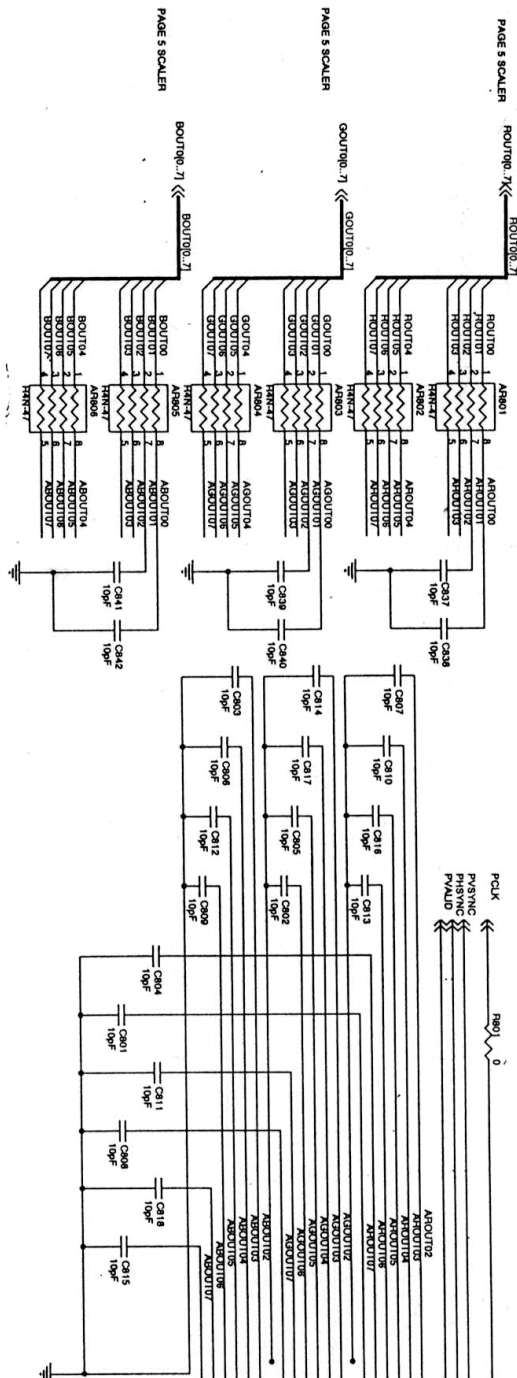
Analog Interface Damping Network



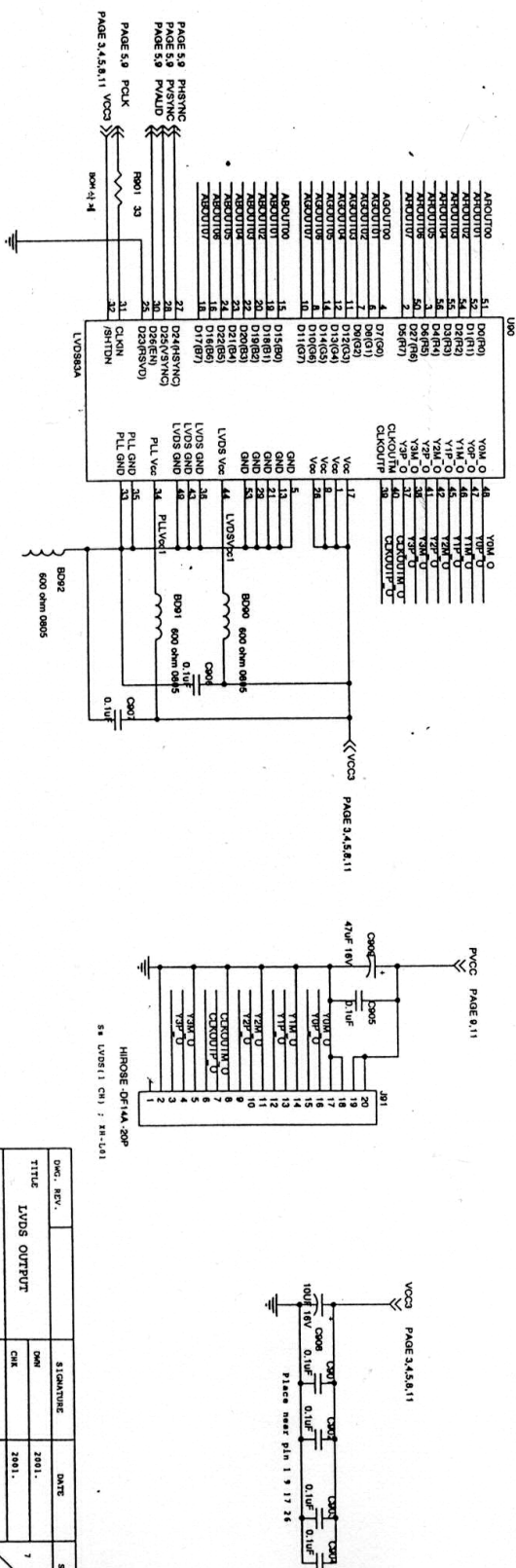
REV.	DATE	SHEET
1	2001.	5
2	2001.	6
3	2001.	7
4	2001.	8
5	2001.	9
6	2001.	10
7	2001.	11
8	2001.	12
9	2001.	13
10	2001.	14
11	2001.	15
12	2001.	16
13	2001.	17
14	2001.	18
15	2001.	19
16	2001.	20
17	2001.	21
18	2001.	22
19	2001.	23
20	2001.	24
21	2001.	25
22	2001.	26
23	2001.	27
24	2001.	28
25	2001.	29
26	2001.	30
27	2001.	31
28	2001.	32
29	2001.	33
30	2001.	34
31	2001.	35
32	2001.	36
33	2001.	37
34	2001.	38
35	2001.	39
36	2001.	40
37	2001.	41
38	2001.	42
39	2001.	43
40	2001.	44
41	2001.	45
42	2001.	46
43	2001.	47
44	2001.	48
45	2001.	49
46	2001.	50
47	2001.	51
48	2001.	52
49	2001.	53
50	2001.	54
51	2001.	55
52	2001.	56
53	2001.	57
54	2001.	58
55	2001.	59
56	2001.	60
57	2001.	61
58	2001.	62
59	2001.	63
60	2001.	64
61	2001.	65
62	2001.	66
63	2001.	67
64	2001.	68
65	2001.	69
66	2001.	70
67	2001.	71
68	2001.	72
69	2001.	73
70	2001.	74
71	2001.	75
72	2001.	76
73	2001.	77
74	2001.	78
75	2001.	79
76	2001.	80
77	2001.	81
78	2001.	82
79	2001.	83
80	2001.	84
81	2001.	85
82	2001.	86
83	2001.	87
84	2001.	88
85	2001.	89
86	2001.	90
87	2001.	91
88	2001.	92
89	2001.	93
90	2001.	94
91	2001.	95
92	2001.	96
93	2001.	97
94	2001.	98
95	2001.	99
96	2001.	100



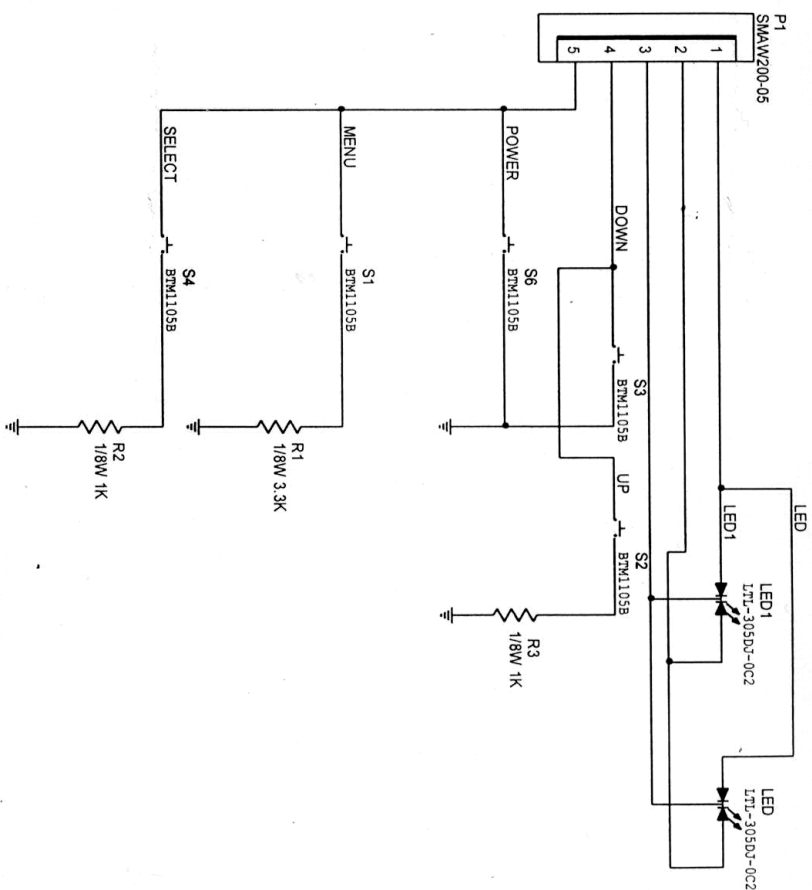
Panel Output Damping Network



LVDS

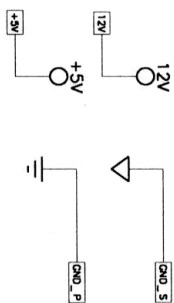


DES. REV.	SIGNATURE	DATE	SHEET
TITLE	LVDS OUTPUT	2001.	7
CHK		2001.	1
APP		2001.	



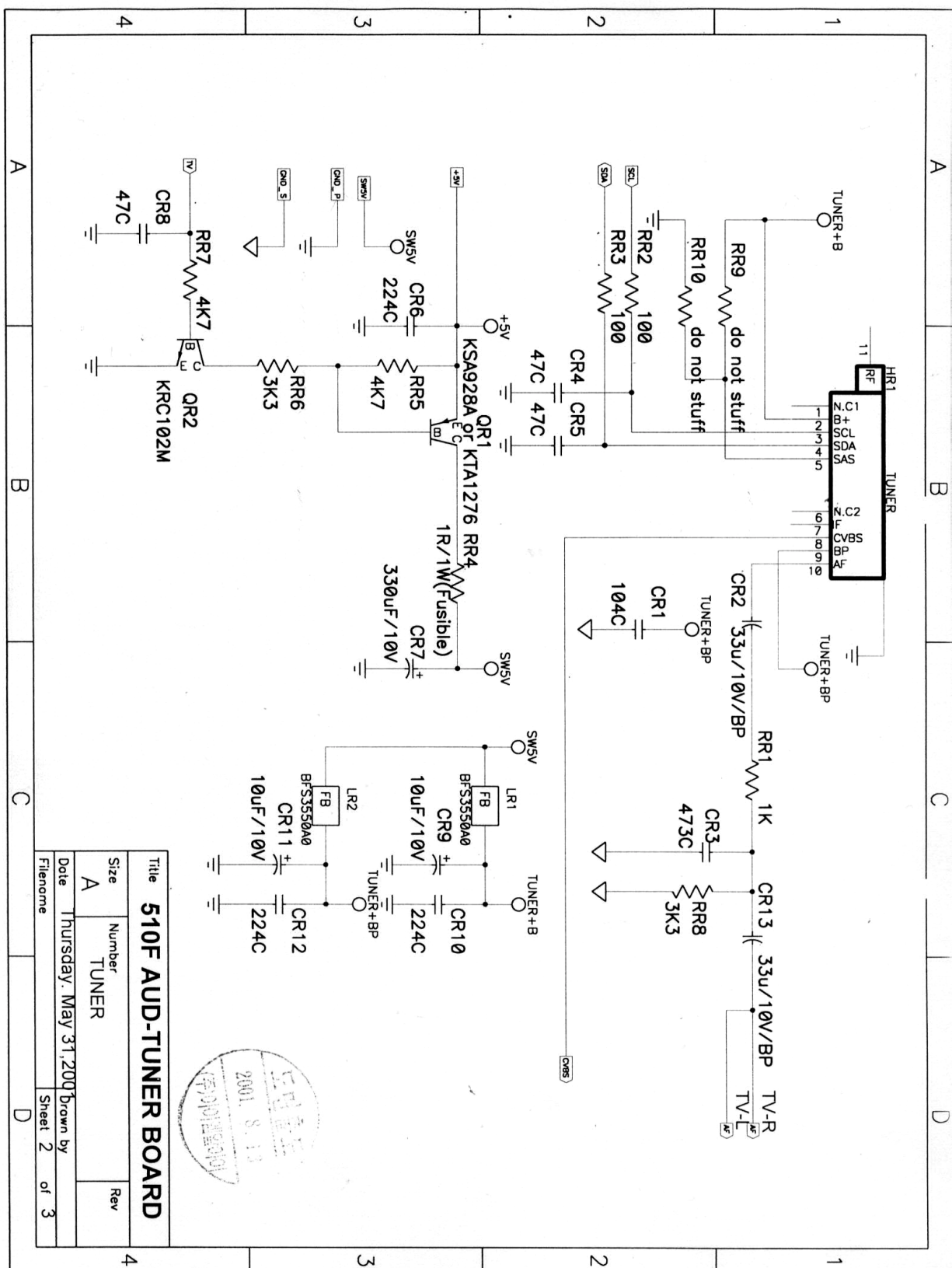
NOTE: 1. RESISTANCE IS SHOWN IN OHM R-1,000 M-1,000,000 RATED POWER OF RESISTOR NOT NOTED IN SCHEMATIC DIAGRAM IS 1/8W SIZE CHIP RESISTOR.
 2. CAPACITANCE IS SHOWN PF AND NOTED CAPACITANCE IS SHOWN μ F, μ F-1,000,000PF RATED VOLTAGE OF CONDENSER NOT NOTED IN SCHEMATIC DIAGRAM IS 50V.
 3. THIS SCHEMATIC DIAGRAM IS SUBJECTED TO CHANGE WITHOUT NOTICE FOR FURTHER IMPROVEMENT.

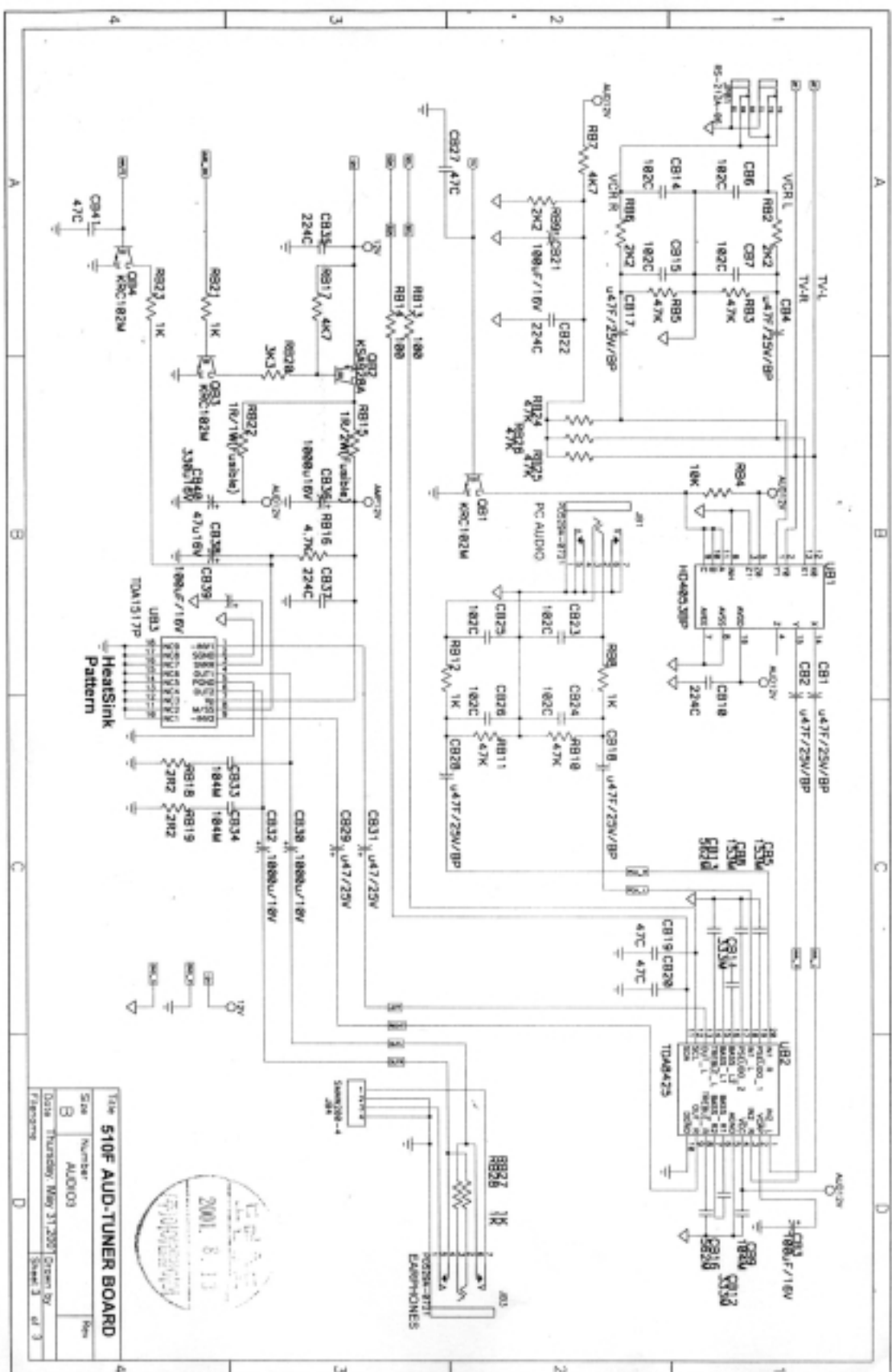
REVISED		DATE		SHEET
NO.	DESCRIPTION	DATE	BY	
1	INITIAL			
2	CHK			
3	APP			



Title 510F AUD-TUNER BOARD		
Size	Number	Rev
A	POWER	
Date Thursday, May 31, 2001		
Filename	Drawn by	
	Sheet 1	of 3

2001. 8. 13
(주)이이비이이





Size	Number	Rev
B	AUDIO3	Rev
Date	Thursday May 31 2001	Green TV
Page	3	of 3

