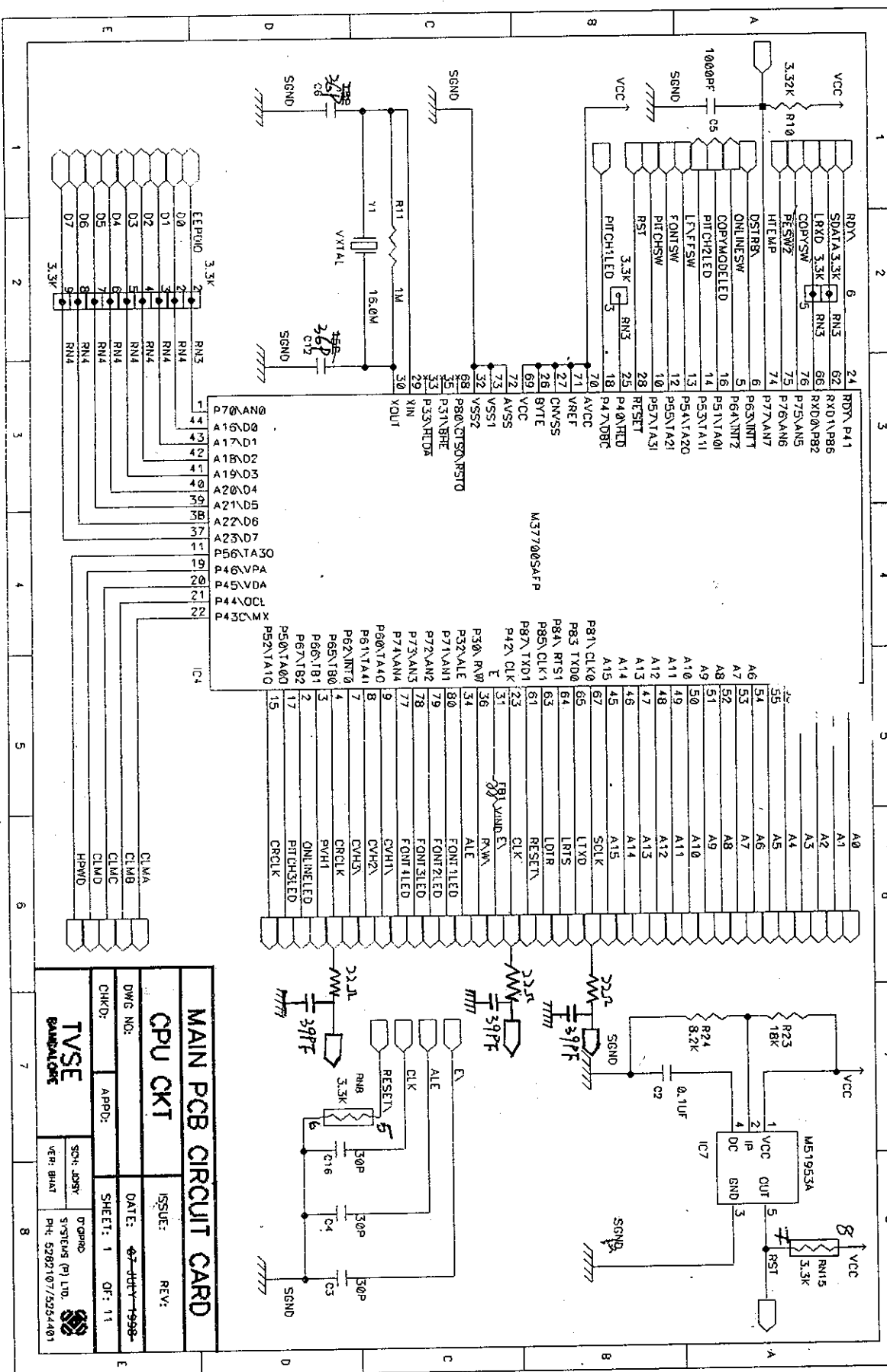
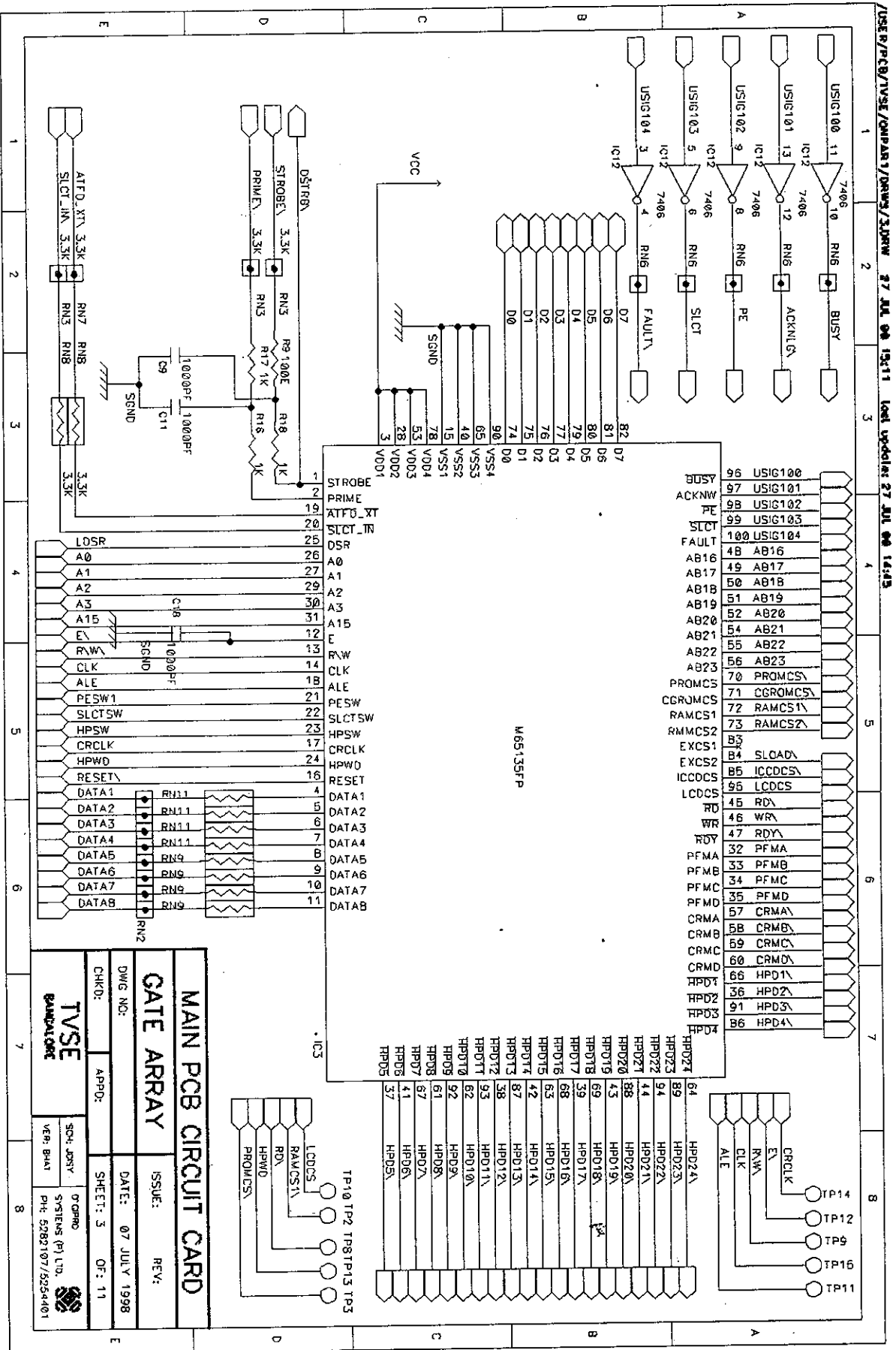


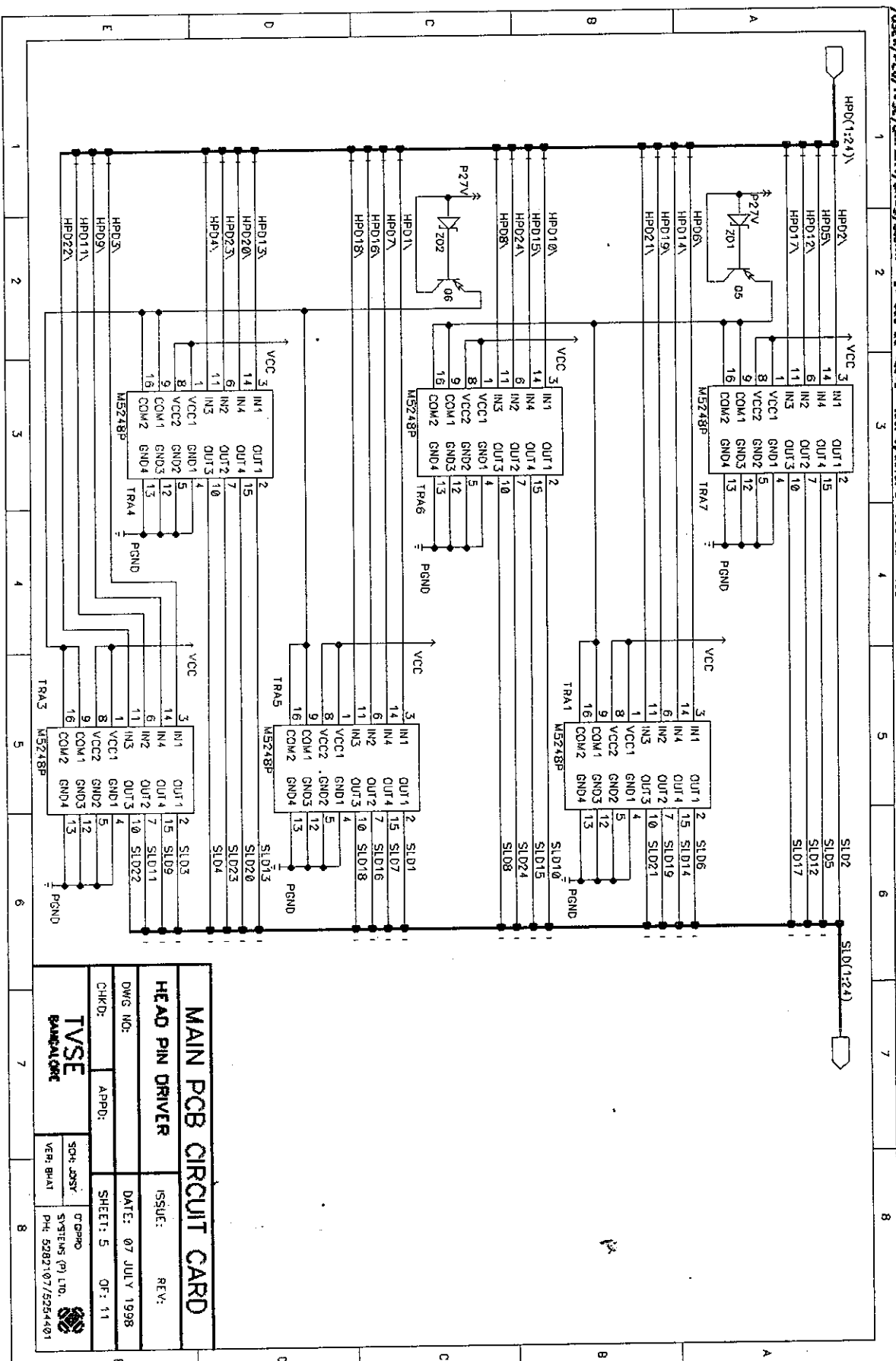
EXHIBIT D

Circuit Diagram





MAIN PCB CIRCUIT CARD			
GATE ARRAY		ISSUE: REV:	
DWG NO:		DATE: 07 JULY 1998	
CHKD:		SHEET: 3 OF: 11	
APPR:		SCH: JSTY	
TVSE		SYSTEMS (P) LTD.	
BANDALCOT		P/N: 5282107/5254401	



MAIN PCB CIRCUIT CARD

HEAD PIN DRIVER

ISSUE: REV:

DWG NO:

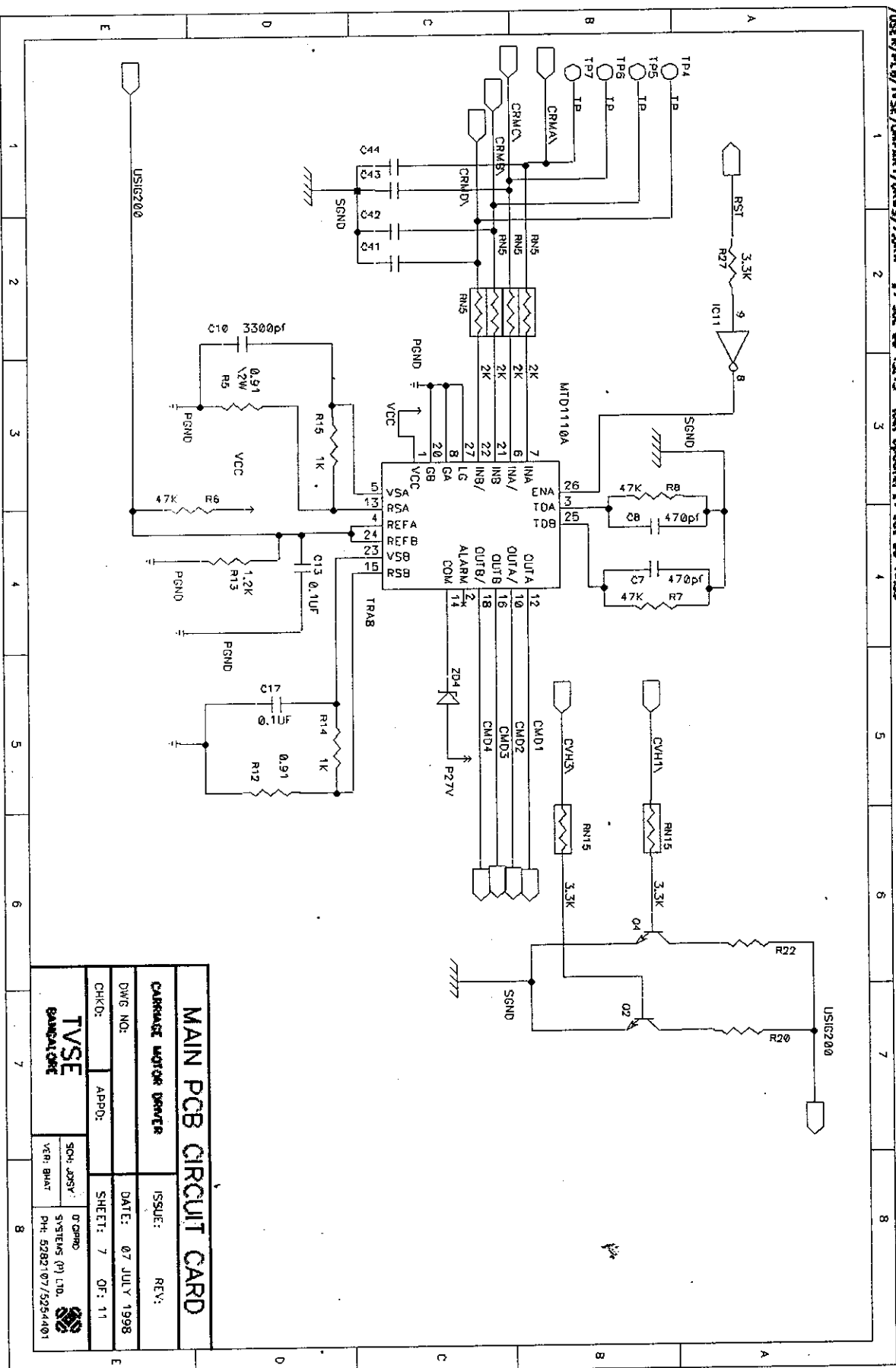
DATE: 07 JULY 1998

CHKD: APPD:

SHEET: 5 OF: 11

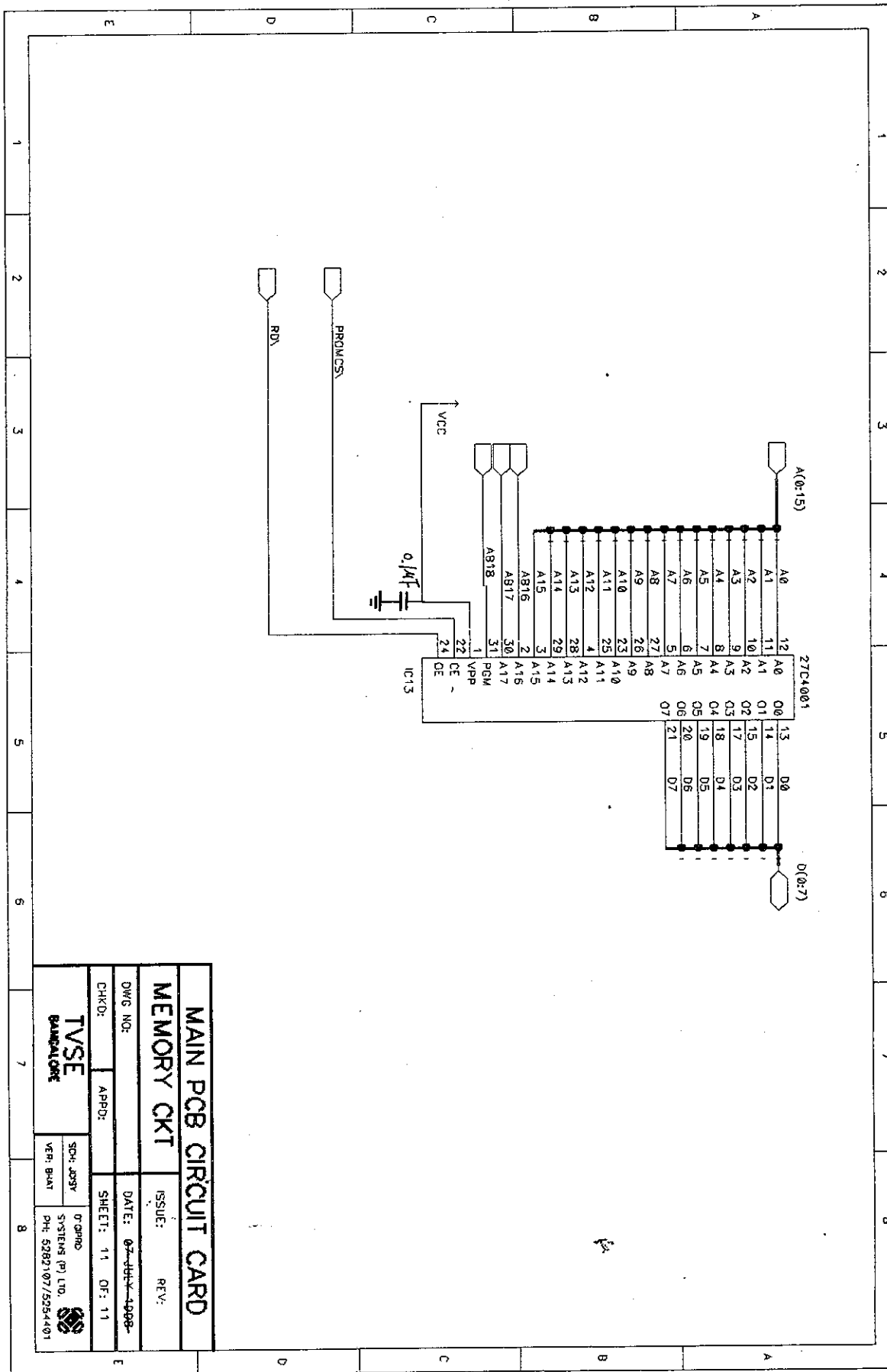
TISE
BANKALORE

SOA: 205T
VER: BHAT
SYSTEMS (P) LTD.
PH: 5282107/5254401



MAIN PCB CIRCUIT CARD

CARRIAGE MOTOR DRIVER		ISSUE:	REV:
DWG NO:		DATE:	07 JULY 1998
CHKD:	APPD:	SHEET:	7 OF 11
TVSE		SCH: JDSV	07 CHNO
GARCIA/DET		VER: BHAT	SYSTEMS (P) LTD.
			PH: 5282107/5254401



MAIN PCB CIRCUIT CARD			
MEMORY CKT		ISSUE:	REV:
DWG NO:	DATE: 07-08-99		
CHKD:	APPD:	SHEET: 11 OF 11	
TVSE		SCH: JDSY	OF CHRO
BANGALORE		VER: BANT	SYSTEMS (P) LTD.
		PH: 5282107/5254401	