

11/02/2000 11:39:54

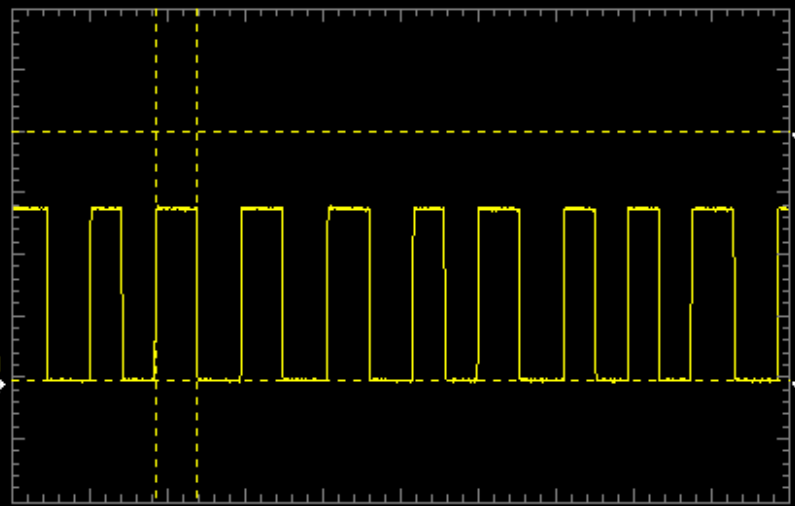
2.000ms/div

← 0.000 s

2k pts

CHAN1
10.00 V/div

C1



Y1

Y2

X1 X2

Marker
source

CHAN1

Y1 40.625 V

X1 1.700ms

CHAN1

Y2 625.000mV

X2 2.750ms

Δ -40.000 V

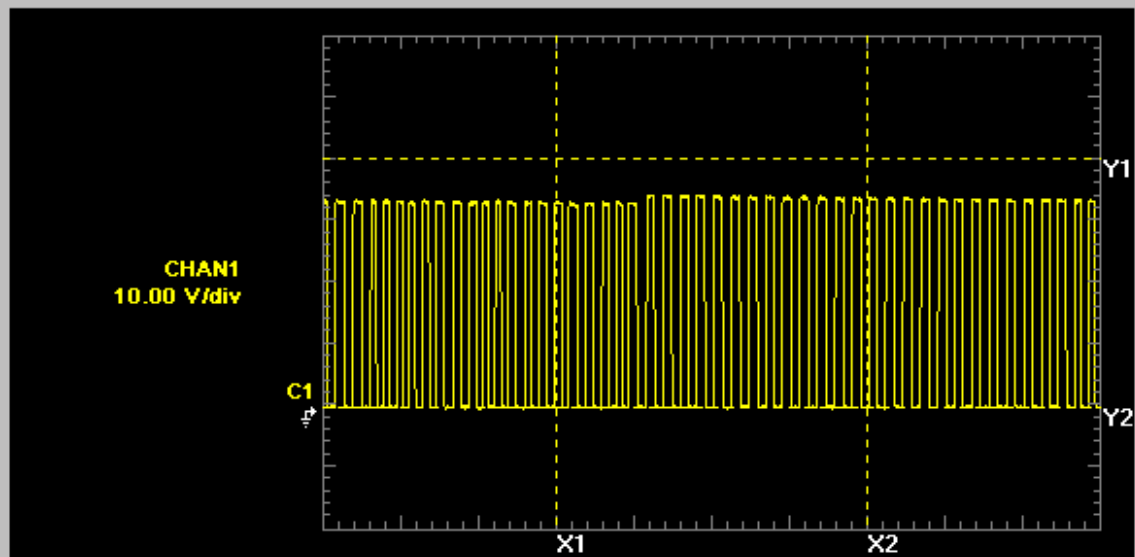
Δ 1.050ms

11/02/2000 12:22:51

10.00ms/div

← 10.60ms

2k pts



Marker
source

CHAN1

Y1 40.625 V

X1 30.600ms

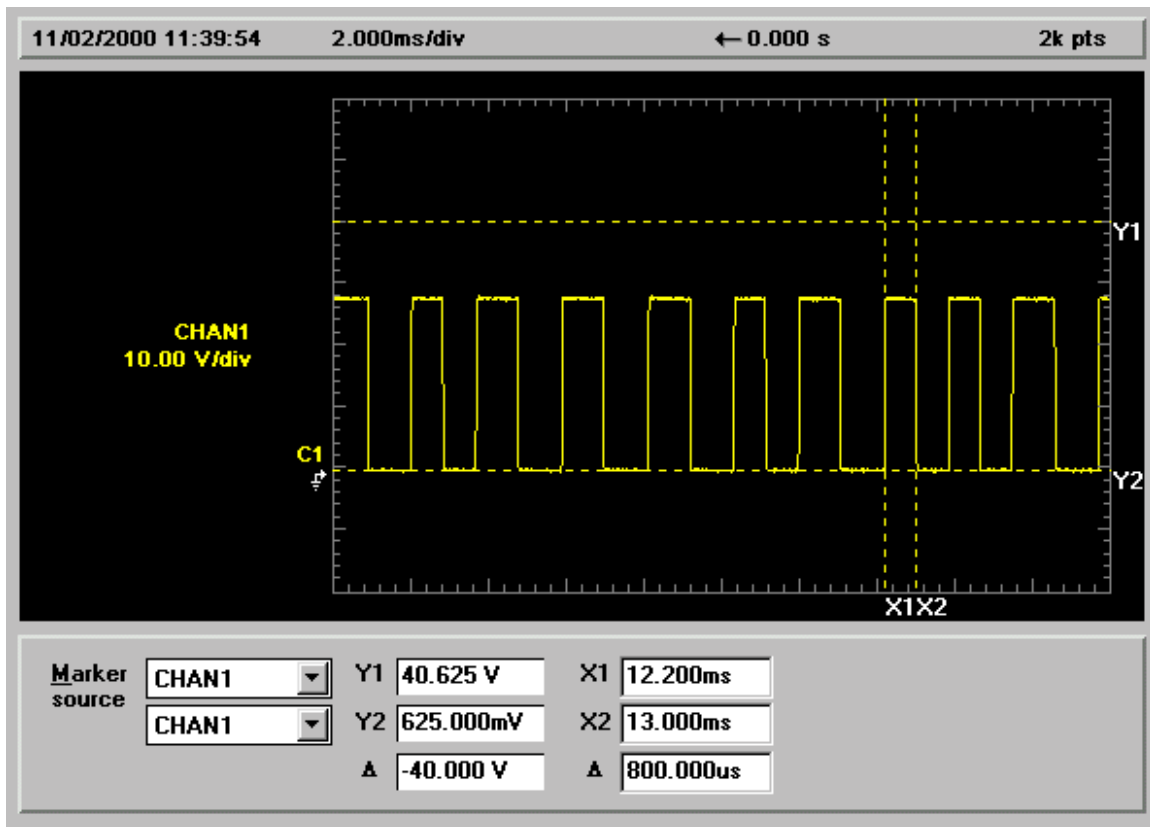
CHAN1

Y2 625.000mV

X2 70.600ms

Δ -40.000 V

Δ 40.000ms



The duty cycle is 50% as declared by the manufacturer in in exhibit MS30-053. The wider pulse is 1.05 msec wide and the narrower pulse is 0.8 msec wide. There are 48 pulses in the 100 msec span depicted above, it is hard to differentiate between wide and narrow. If all were assumed to be wide, which they are not, the duty cycle would be $48 \times 1.05 / 100$, or 50.4%. The previous test report calculation of duty cycle was based on a 20 msec sampling, and did not catch any of the narrower pulses, thus the 51.6% number.