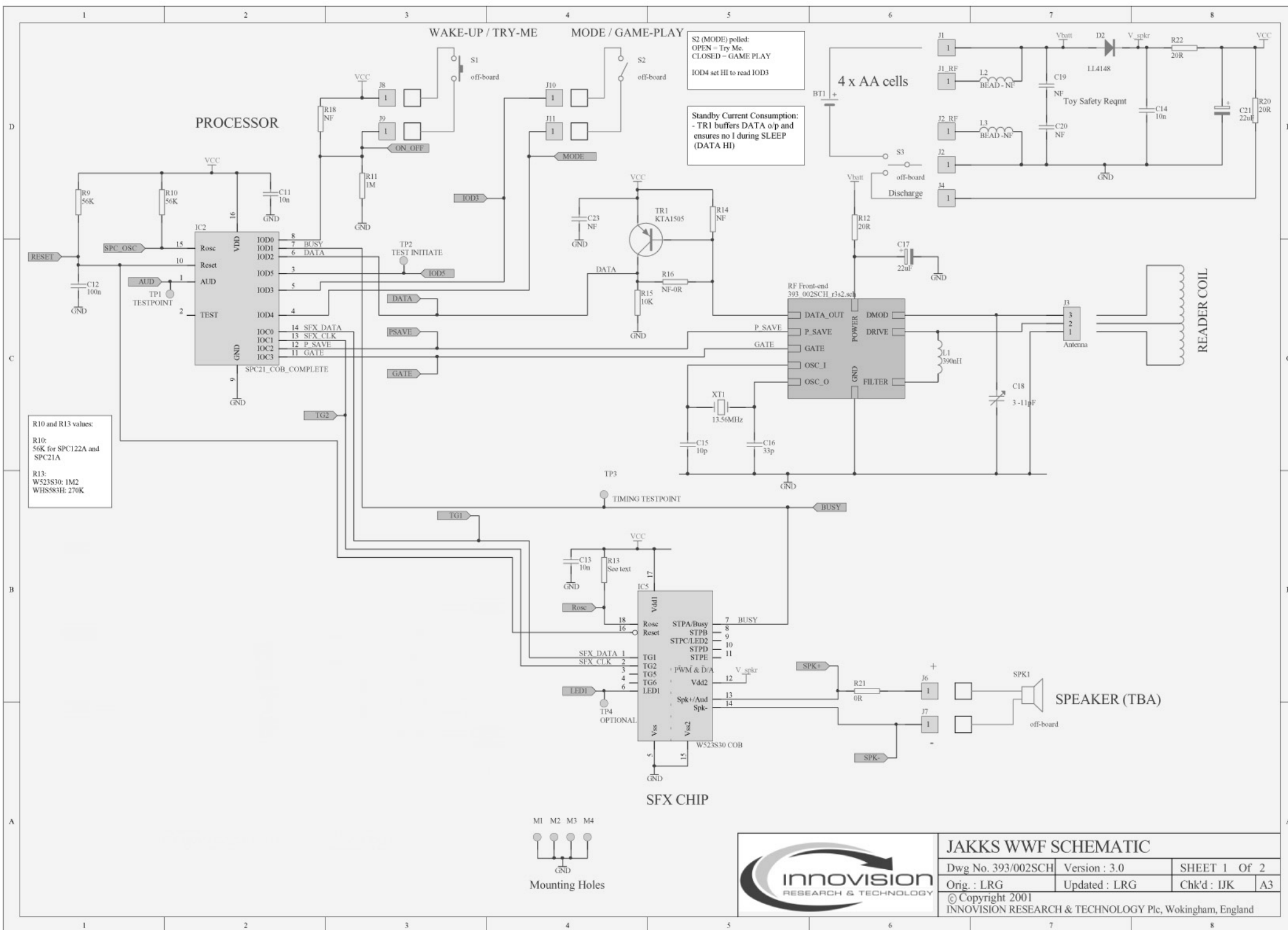
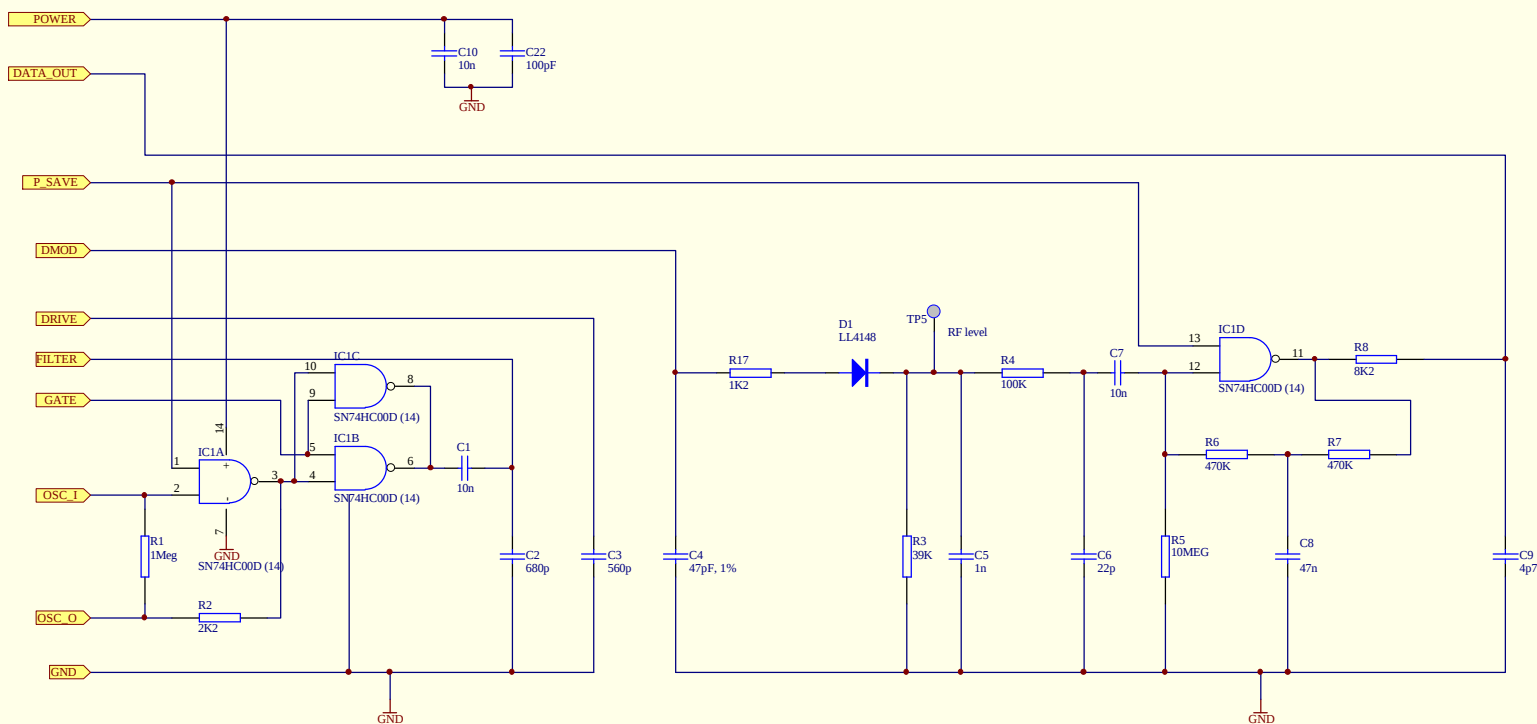




NOTE: THIS PART OF THE CIRCUIT TO BE ENCAPSULATED ON PCB



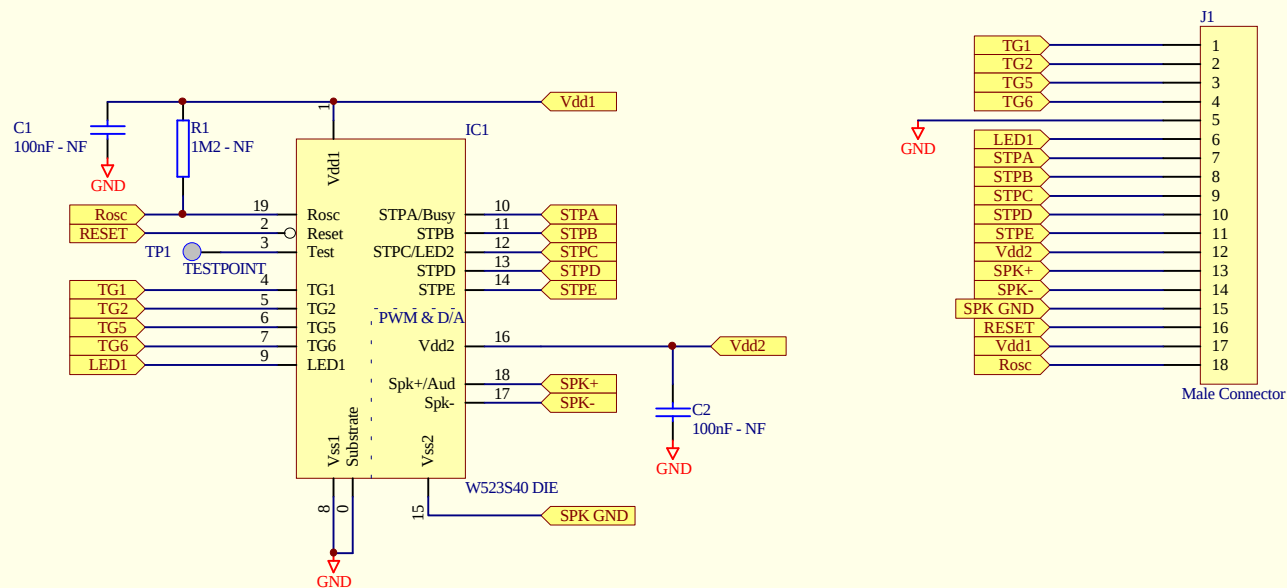
Previous values:

C5=10n
C8=100n (setup time too long)



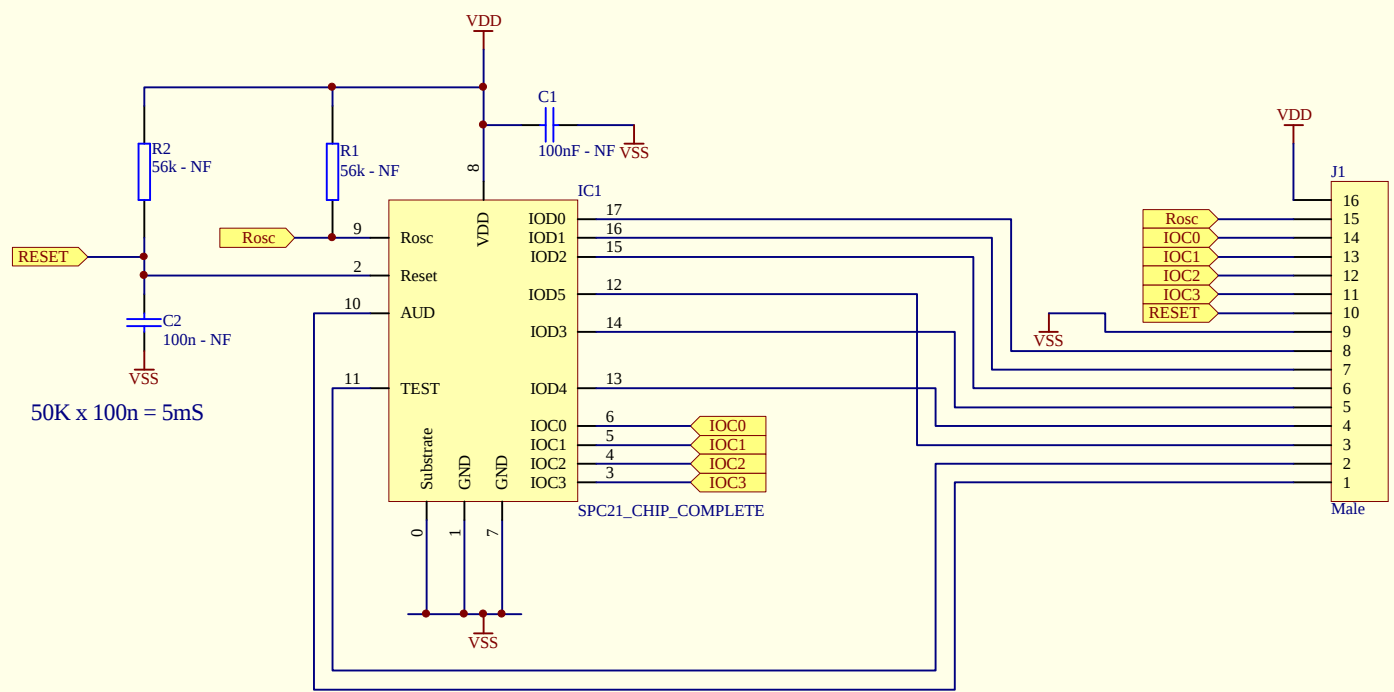
Reader Front-end (13.56 MHz)

Dwg No. 393_002SCH	Version : 3.0	SHEET 2 Of 2
Orig : LRG	Updated : LRG	Chk'd : IJK
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Jakks Sound Processor COB

Dwg No. 393/003SCH	Version : 1	SHEET 1 Of 1
Orig. : LRG	Updated :	Chk'd : TC A4
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50K x 100n = 5mS



SPC21A COB, all pins out

Dwg No. 393/004SCH	Version : A	SHEET 1 Of 1
Orig. : LRG	Updated :	Chk'd : LJK A4

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