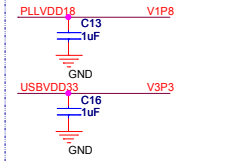
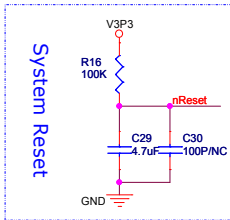
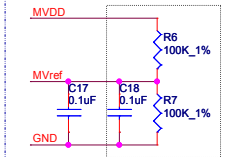


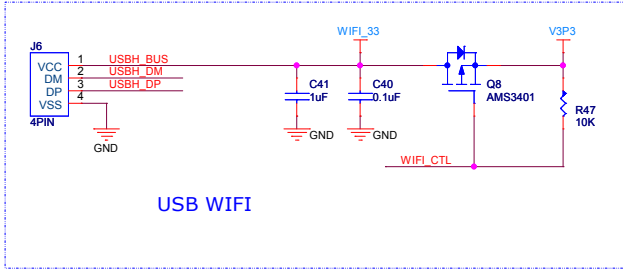
Cap.



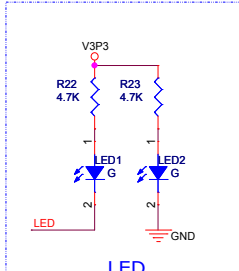
Close to SOC as possible



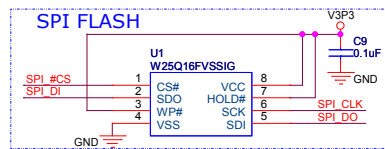
System Reset



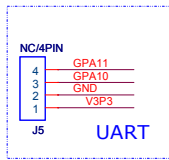
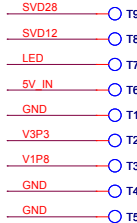
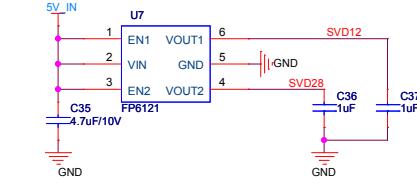
USB WIFI



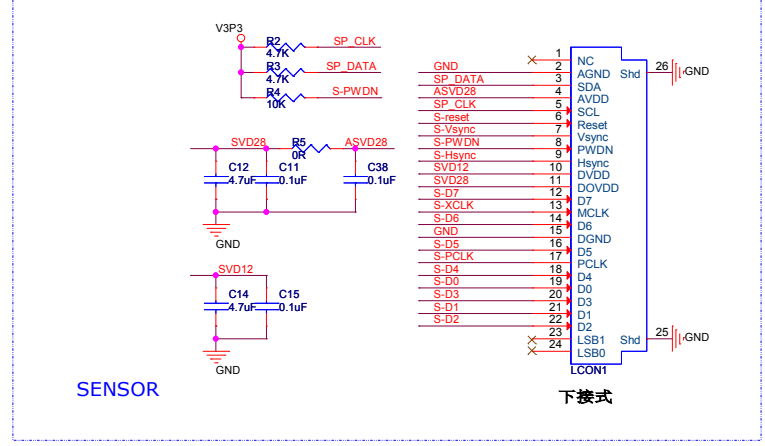
LED



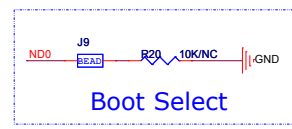
SPI FLASH



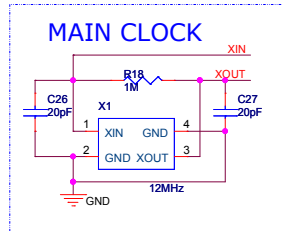
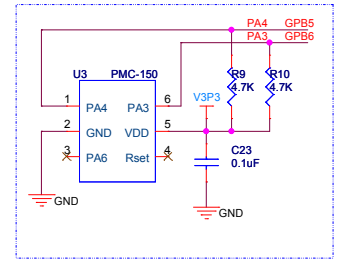
UART



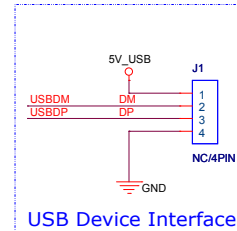
SENSOR



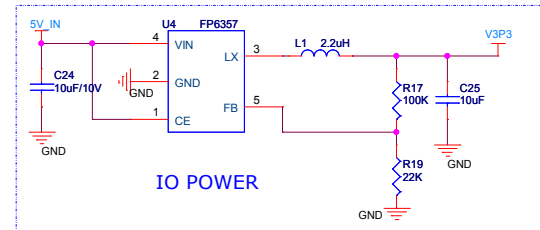
Boot Select



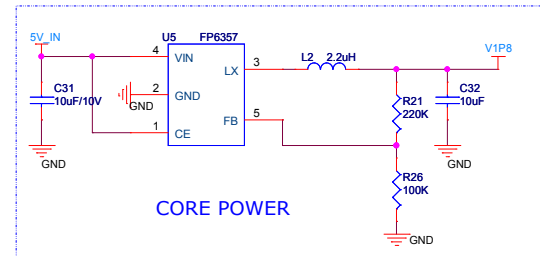
MAIN CLOCK



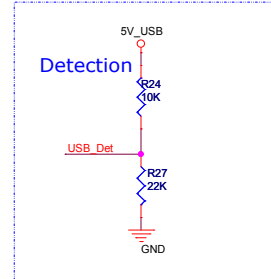
USB Device Interface



IO POWER



CORE POWER



Detection

<OrgName>		
Size	Document Number	Rev
Custom	LINK-A8	4.03
Date:	Thursday, July 12, 2018	Sheet 1 of 1

