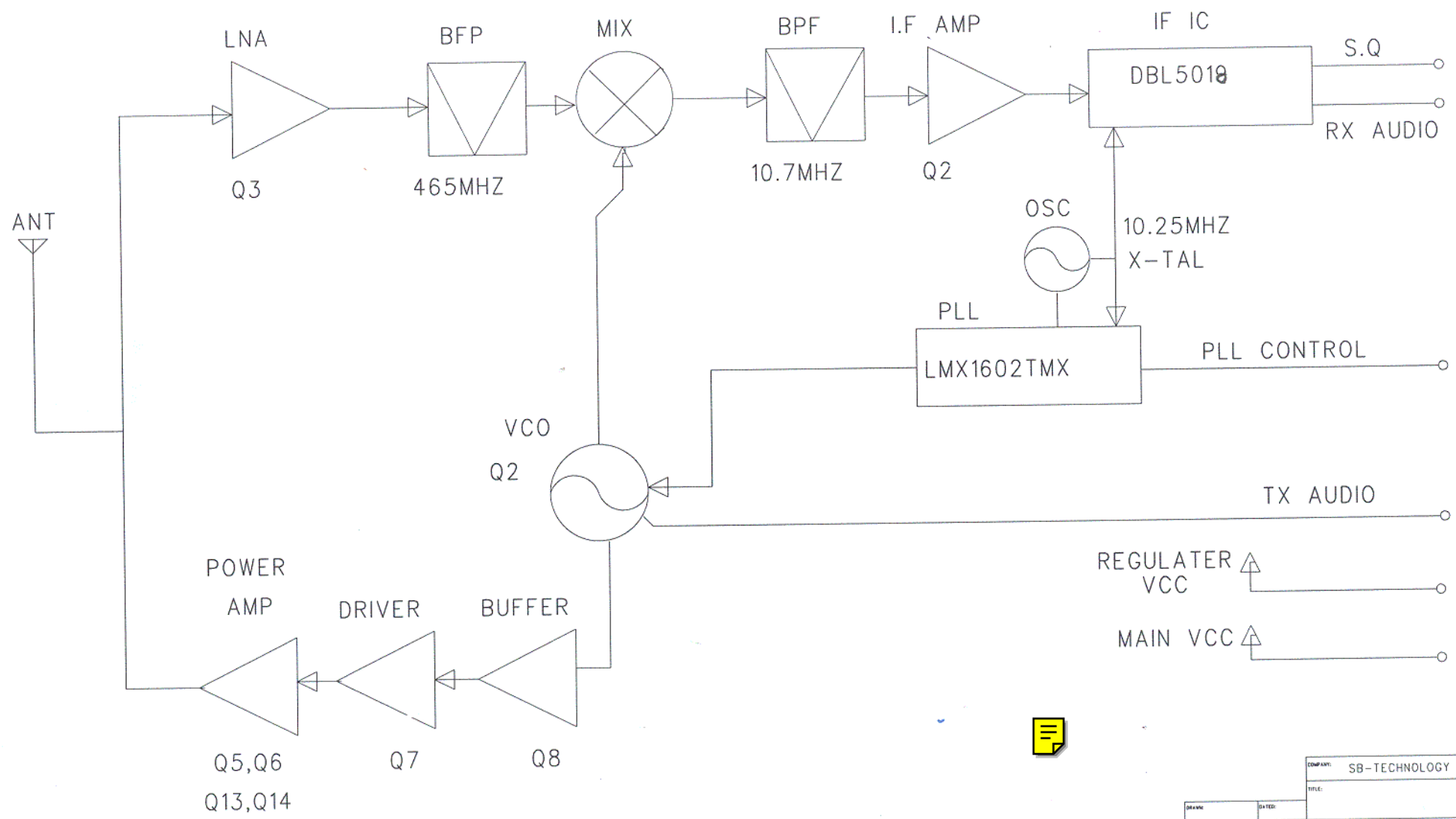
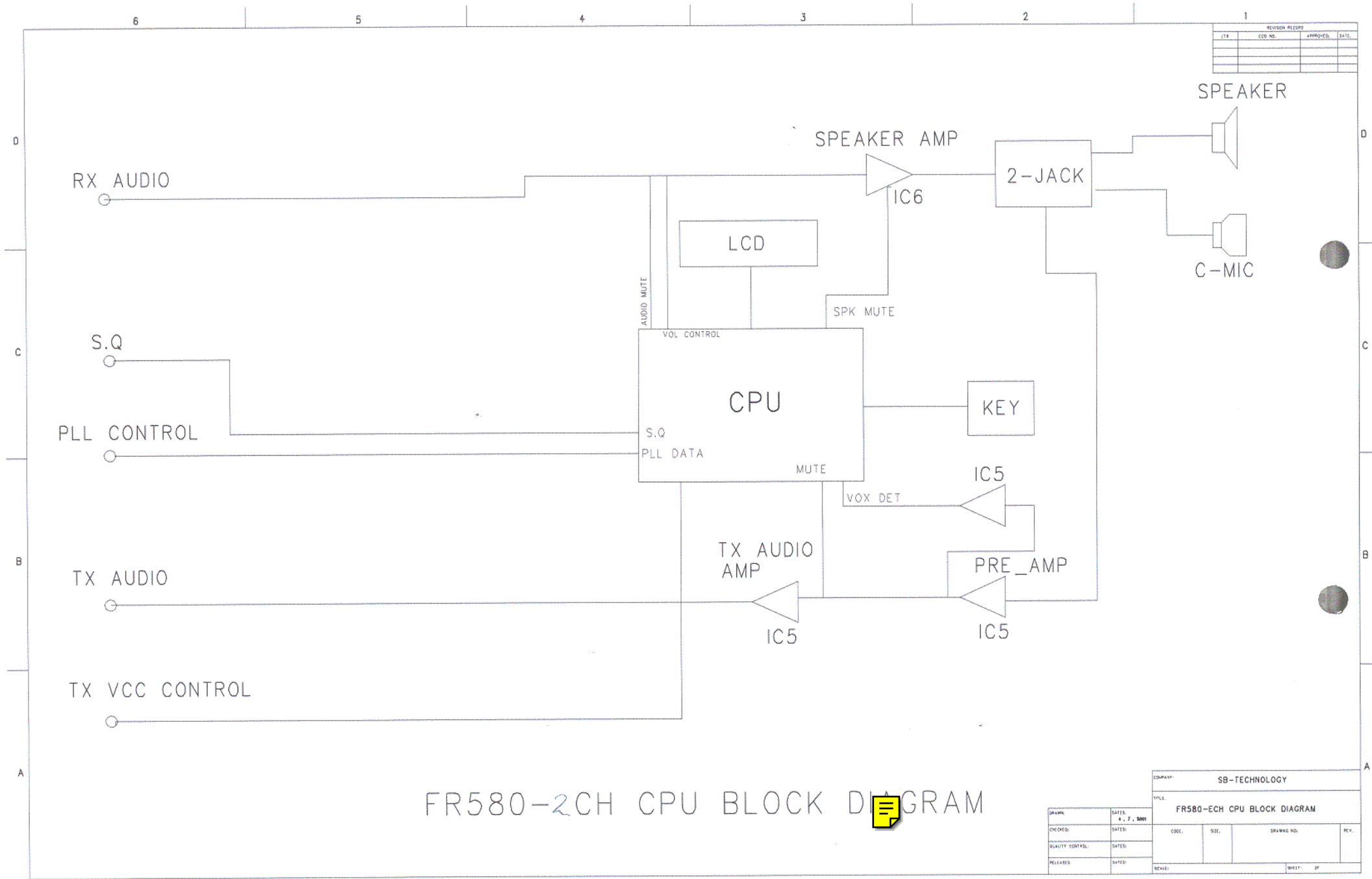


| REVISION RECORD |         |          |      |
|-----------------|---------|----------|------|
| LTW             | ECO NO. | APPROVED | DATE |
|                 |         |          |      |
|                 |         |          |      |
|                 |         |          |      |



FR580-2CH RF BLOCK DIAGRAM

|                        |                 |                                        |       |
|------------------------|-----------------|----------------------------------------|-------|
| COMPANY: SB-TECHNOLOGY |                 |                                        |       |
| TITLE:                 |                 |                                        |       |
| DRAWN:                 | DATED:          | CODE:                                  | DATE: |
| CHECKED:               | DATED:          | SCALE:                                 | REV:  |
| QUALITY CONTROL:       | DATED:          | DRAWING NO: FR580-ECH RF BLOCK DIAGRAM |       |
| RELEASED:              | DATED: 7.4.2001 | SHEET: 01                              |       |



FR580-2CH CPU BLOCK DIAGRAM