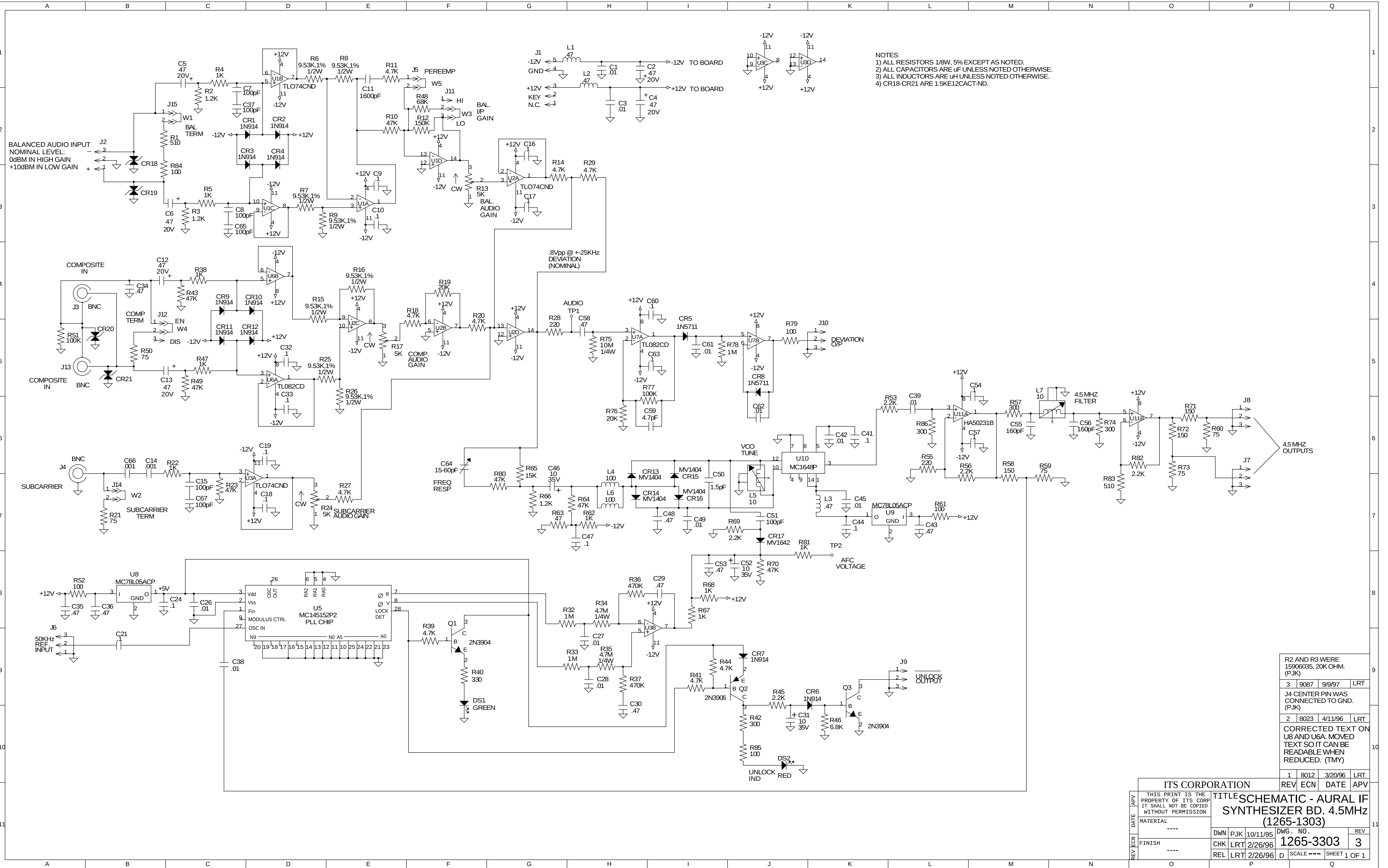




NOTES:
1) ALL RESISTORS 1/8W, 5% EXCEPT AS NOTED.
2) ALL CAPACITORS ARE uF UNLESS NOTED OTHERWISE.
3) ALL INDUCTORS ARE uH UNLESS NOTED OTHERWISE.
4) CR18-CR21 ARE 1.5KE12CACT-ND.

R2 AND R3 WERE 15906035, 20K OHM. (PJK)			
3	9087	9/9/97	LRT
J4 CENTER PIN WAS CONNECTED TO GND. (PJK)			
2	8023	4/11/96	LRT
CORRECTED TEXT ON U8 AND U6A. MOVED TEXT SO IT CAN BE READABLE WHEN REDUCED. (TMY)			
1	8012	3/20/96	LRT

ITS CORPORATION				REV	ECN	DATE	APV
APV	THIS PRINT IS THE PROPERTY OF ITS CORP IT SHALL NOT BE COPIED WITHOUT PERMISSION			TITLE SCHEMATIC - AURAL IF SYNTHESIZER BD. 4.5MHz (1265-1303)			
DATE	MATERIAL			DWN	PJK	10/11/95	DWG. NO.
REV	ECN	FINISH	---	CHK	LRT	2/26/96	1265-3303
				REL	LRT	2/26/96	3
SCALE ---				SHEET 1 OF 1			