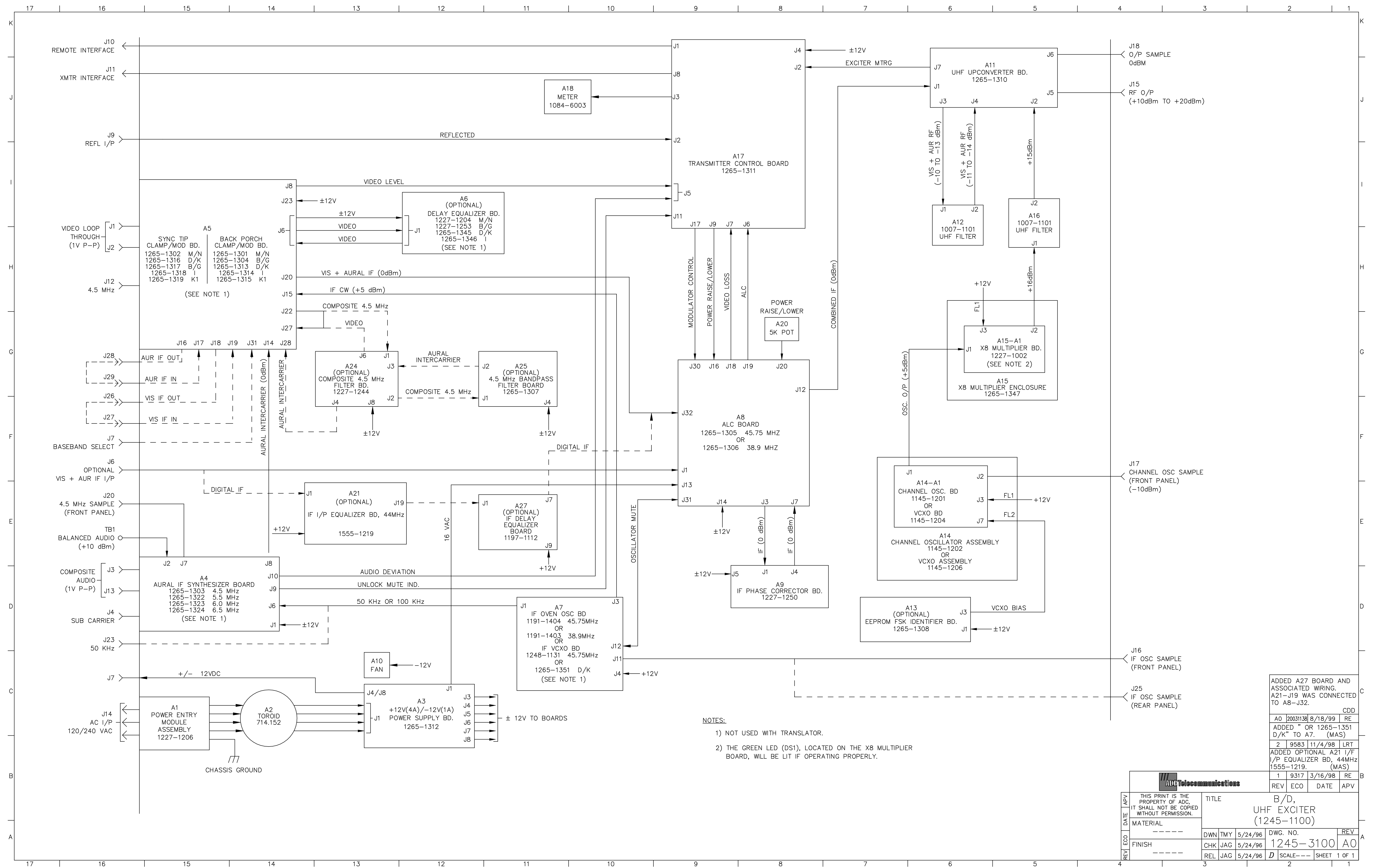
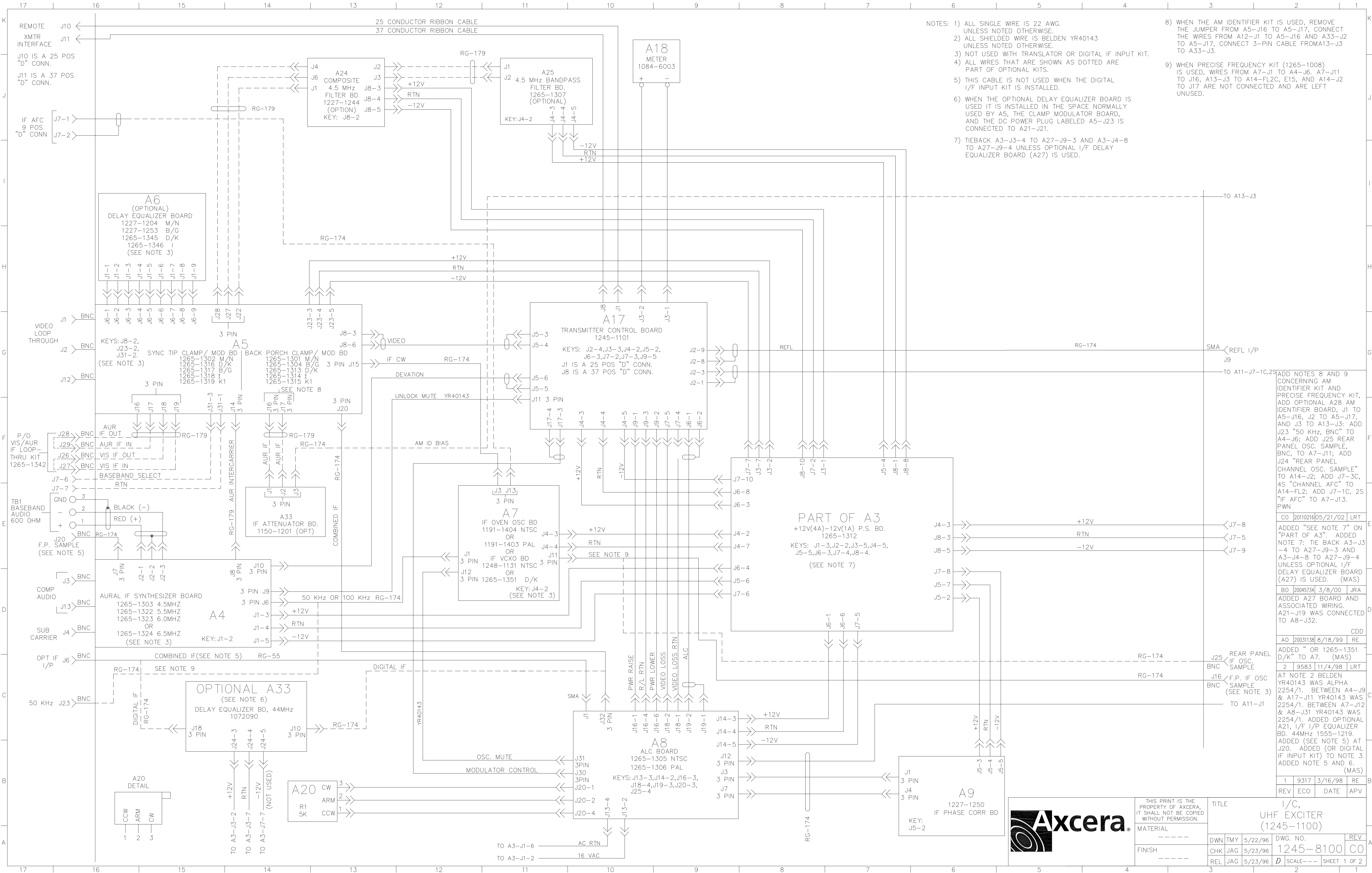






- NOTES: 1) ALL SINGLE WIRE IS 22 AWG. UNLESS NOTED OTHERWISE.
2) ALL SHIELDED WIRE IS BELDEN YR40143 UNLESS NOTED OTHERWISE.
3) NOT USED WITH TRANSLATOR OR DIGITAL IF INPUT KIT.
4) ALL WIRES THAT ARE SHOWN AS DOTTED ARE PART OF OPTIONAL KITS.
5) THIS CABLE IS NOT USED WHEN THE DIGITAL I/F INPUT KIT IS INSTALLED.
6) WHEN THE OPTIONAL DELAY EQUALIZER BOARD IS USED IT IS INSTALLED IN THE SPACE NORMALLY USED BY A5, THE CLAMP MODULATOR BOARD, AND THE DC POWER PLUG LABELED A5-J23 IS CONNECTED TO A21-J21.
7) TIEBACK A3-J3-4 TO A27-J9-3 AND A3-J4-8 TO A27-J9-4 UNLESS OPTIONAL I/F DELAY EQUALIZER BOARD (A27) IS USED.
- 8) WHEN THE AM IDENTIFIER KIT IS USED, REMOVE THE JUMPER FROM A5-J16 TO A5-J17. CONNECT THE WIRES FROM A12-J1 TO A5-J16 AND A33-J2 TO A5-J17. CONNECT 3-PIN CABLE FROM A13-J3 TO A33-J3.
9) WHEN PRECISE FREQUENCY KIT (1265-1008) IS USED, WIRES FROM A7-J1 TO A4-J6. A7-J11 TO J16, A13-J3 TO A14-FL2C, E15, AND A14-J2 TO J17 ARE NOT CONNECTED AND ARE LEFT UNUSED.

ADD NOTES 8 AND 9 CONCERNING AM IDENTIFIER KIT AND PRECISE FREQUENCY KIT. ADD OPTIONAL A28 AM IDENTIFIER BOARD. J1 TO A5-J16, J2 TO A5-J17, AND J3 TO A13-J3: ADD J23 "50 KHz, BNC" TO A4-J6; ADD J25 REAR PANEL OSC. SAMPLE, BNC, TO A7-J11; ADD J24 "REAR PANEL CHANNEL OSC. SAMPLE" TO A14-J2; ADD J7-3C, 4S "CHANNEL AFC" TO A14-FL2; ADD J7-1C, 2S "IF AFC" TO A7-J13. PWN

CO [20110216] 05/21/02 LRT

ADDED "SEE NOTE 7" ON "PART OF A3". ADDED NOTE 7: TIE BACK A3-J3-4 TO A27-J9-3 AND A3-J4-8 TO A27-J9-4 UNLESS OPTIONAL I/F DELAY EQUALIZER BOARD (A27) IS USED. (MAS)

BO [20045734] 3/8/00 JRA

ADDED A27 BOARD AND ASSOCIATED WIRING. A21-J19 WAS CONNECTED TO A8-J32.

CDD

AO [20031138] 8/18/99 RE

ADDED " OR 1265-1351 D/K" TO A7. (MAS)

2 [9583] 11/4/98 LRT

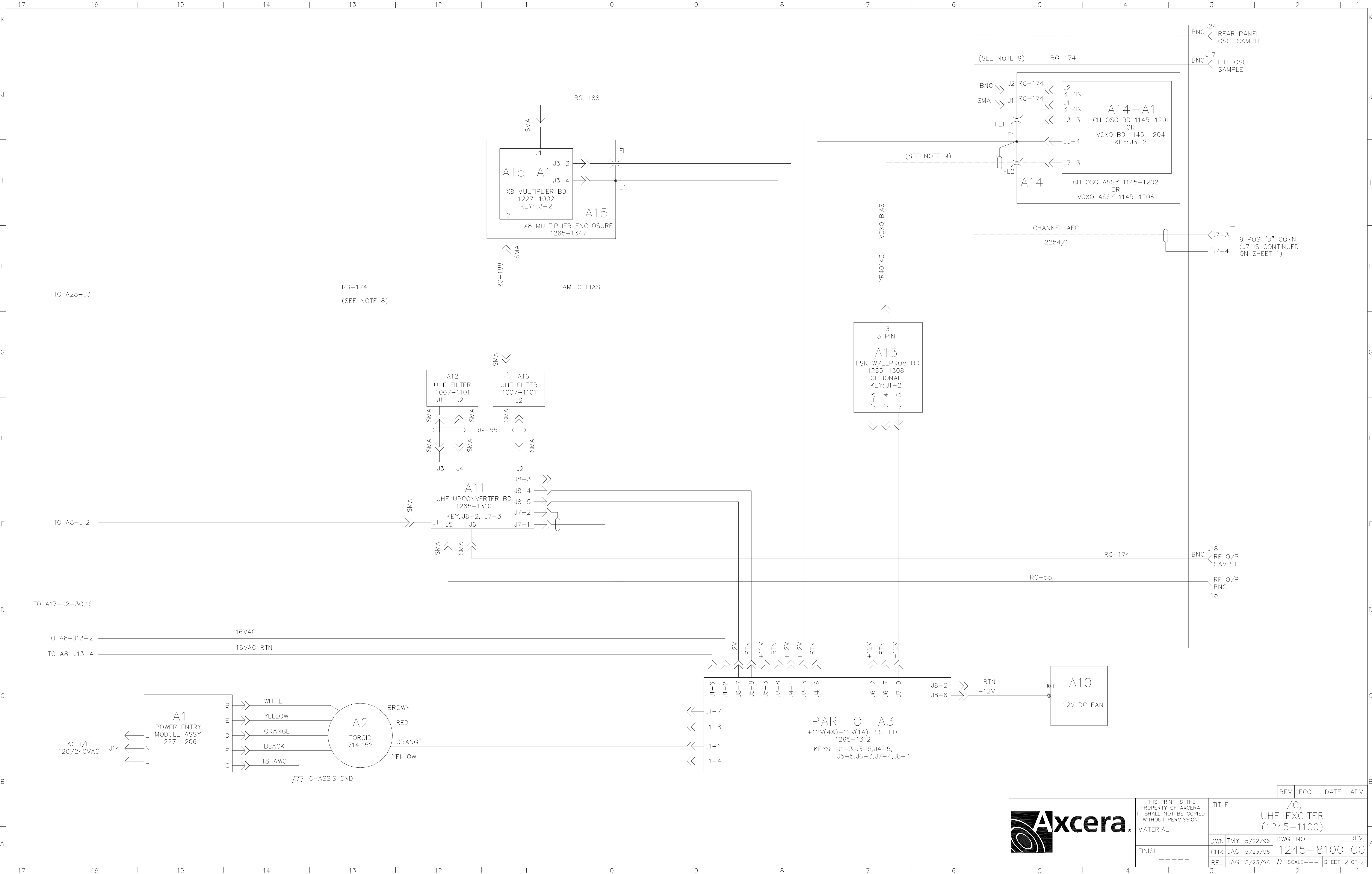
AT NOTE 2 BELDEN YR40143 WAS ALPHA 2254/1. BETWEEN A4-J9 & A17-J11 YR40143 WAS 2254/1. BETWEEN A7-J12 & A8-J31 YR40143 WAS 2254/1. ADDED OPTIONAL A21, I/F I/P EQUALIZER BD, 44MHz 1555-1219. ADDED (SEE NOTE 5) AT J20. ADDED (OR DIGITAL IF INPUT KIT) TO NOTE 3. ADDED NOTE 5 AND 6. (MAS)

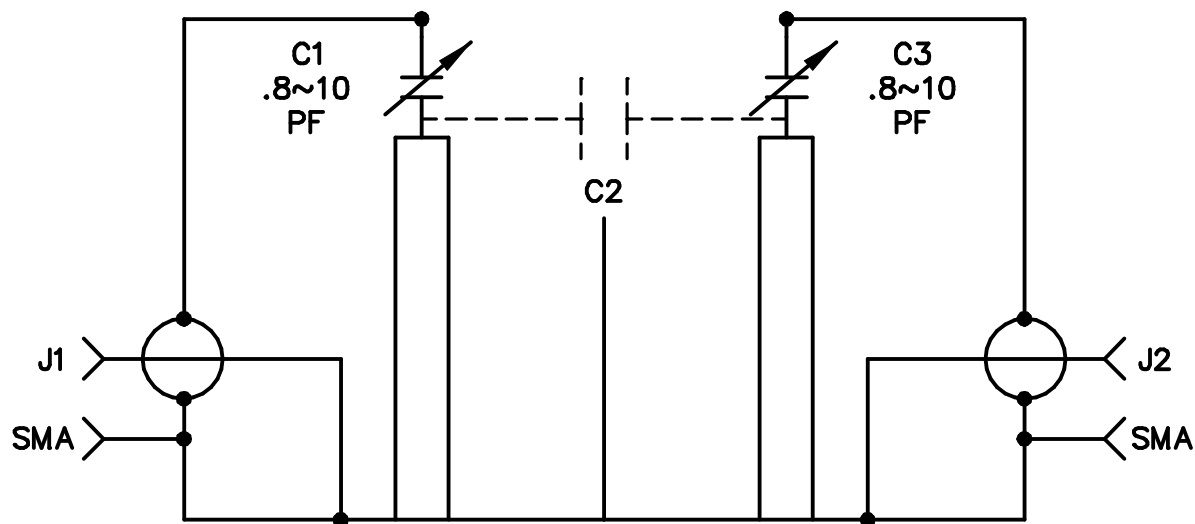
1 [9317] 3/16/98 RE

REV ECO DATE APV



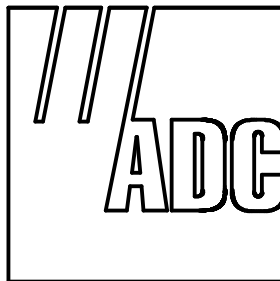
THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION.		TITLE		I/C, UHF EXCITER (1245-1100)	
MATERIAL	DWN	TMY	5/22/96	DWG. NO.	REV
FINISH	CHK	JAG	5/23/96	1245-8100	C0
REL	JAG	5/23/96	D	SCALE---	SHEET 1 OF 2





NOTE: C2 IS STRAY CAPACITANCE
BETWEEN LINE SECTIONS.

REV	ECO	DATE	APV
-----	-----	------	-----



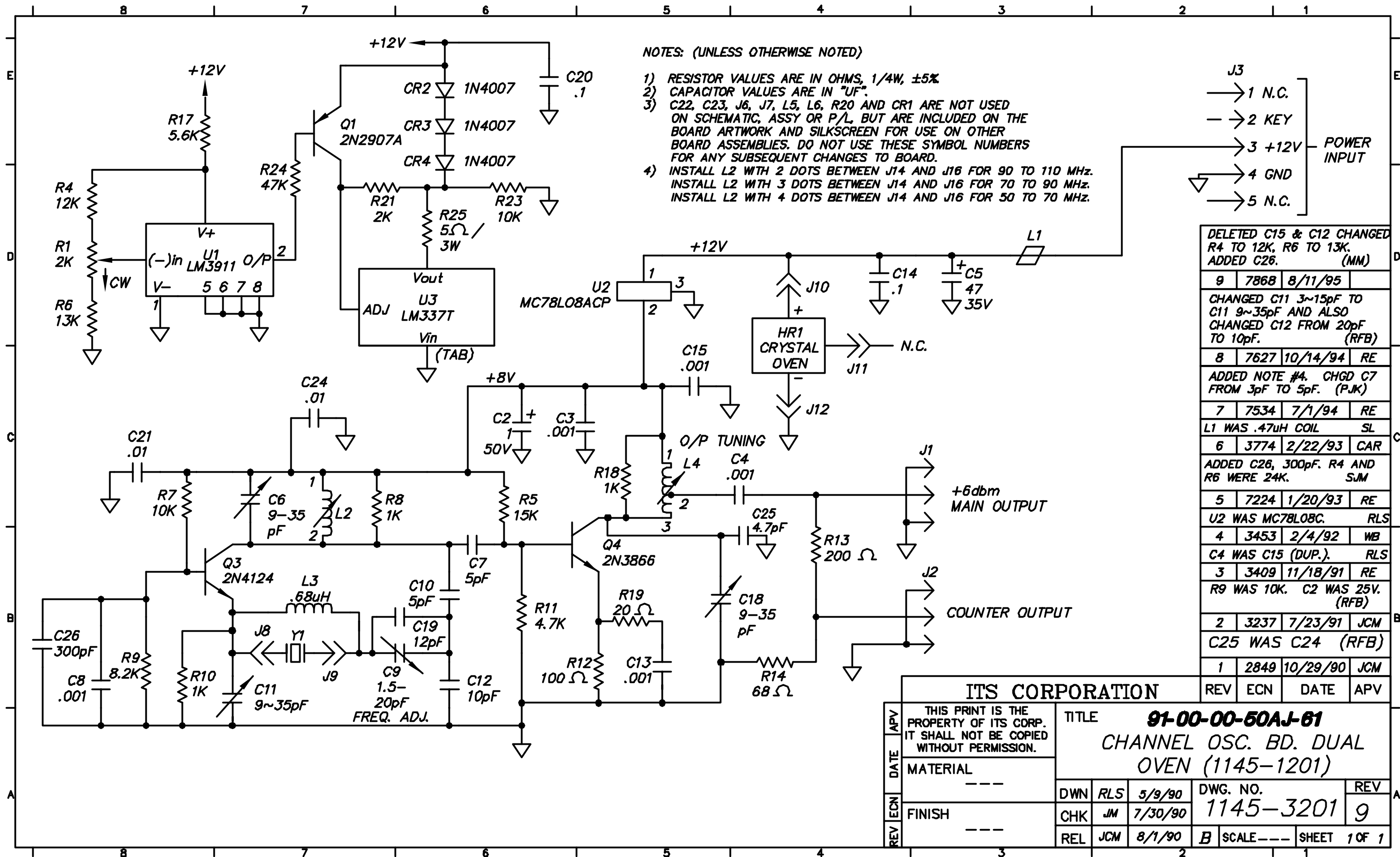
THIS PRINT IS THE
PROPERTY OF ADC,
IT SHALL NOT BE COPIED
WITHOUT PERMISSION.

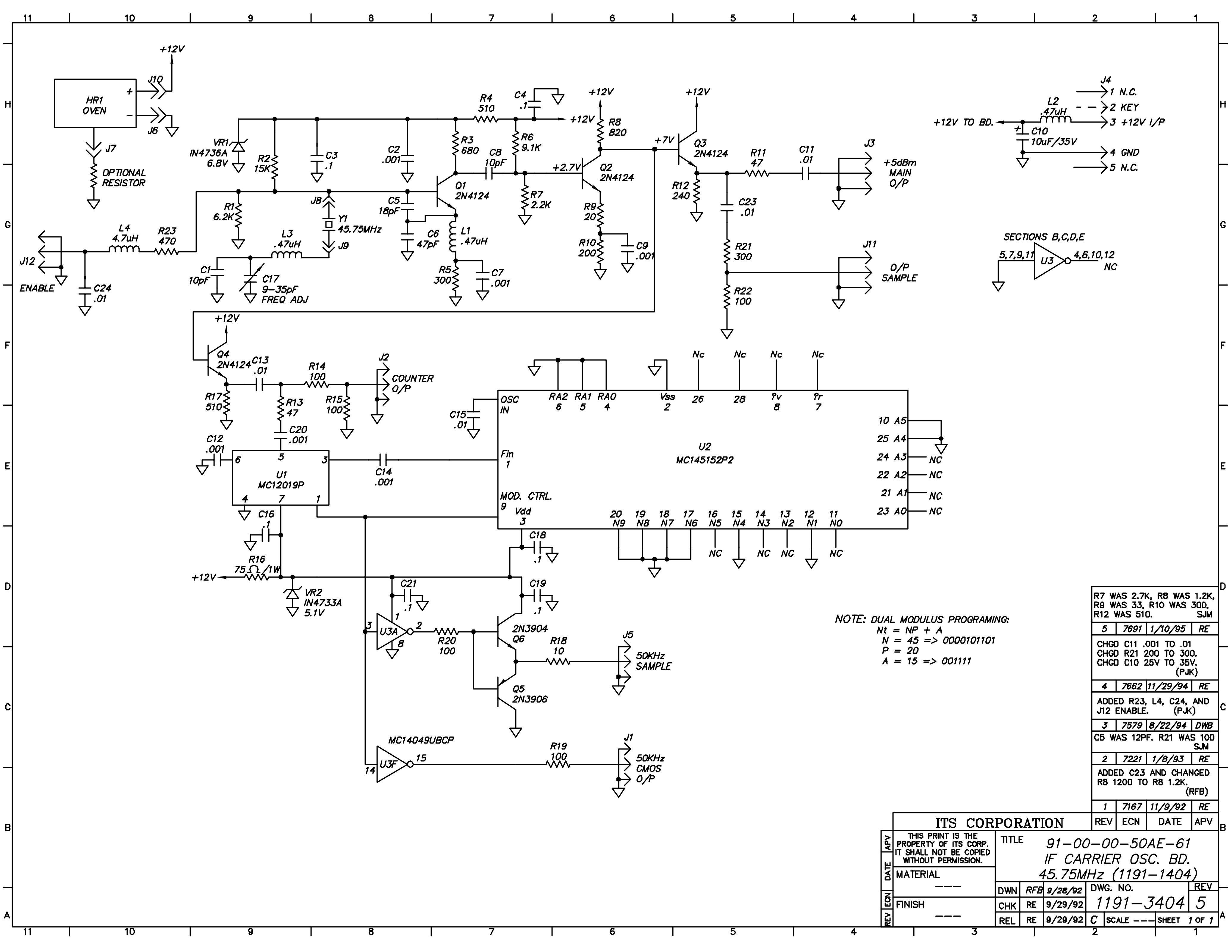
MATERIAL

FINISH

TITLE **91-00-00-50Z-61**
UHF FILTER

DWN	DD	9/13/83	DWG. NO.	REV
CHK	RWZ	9/13/83	1007-3101	0
REL	RWZ	7/20/90	A SCALE ---	SHEET 1 OF 1





NOTE: DUAL MODULUS PROGRAMMING:
Nt = NP + A
N = 45 => 0000101101
P = 20
A = 15 => 001111

R7 WAS 2.7K, R8 WAS 1.2K,
R9 WAS 33, R10 WAS 300,
R12 WAS 510. SJM

5	7691	1/10/95	RE
---	------	---------	----

CHGD C11 .001 TO .01
CHGD R21 200 TO 300.
CHGD C10 25V TO 35V.
(PJK)

4	7662	11/29/94	RE
---	------	----------	----

ADDED R23, L4, C24, AND
J12 ENABLE. (PJK)

3	7579	8/22/94	DWB
---	------	---------	-----

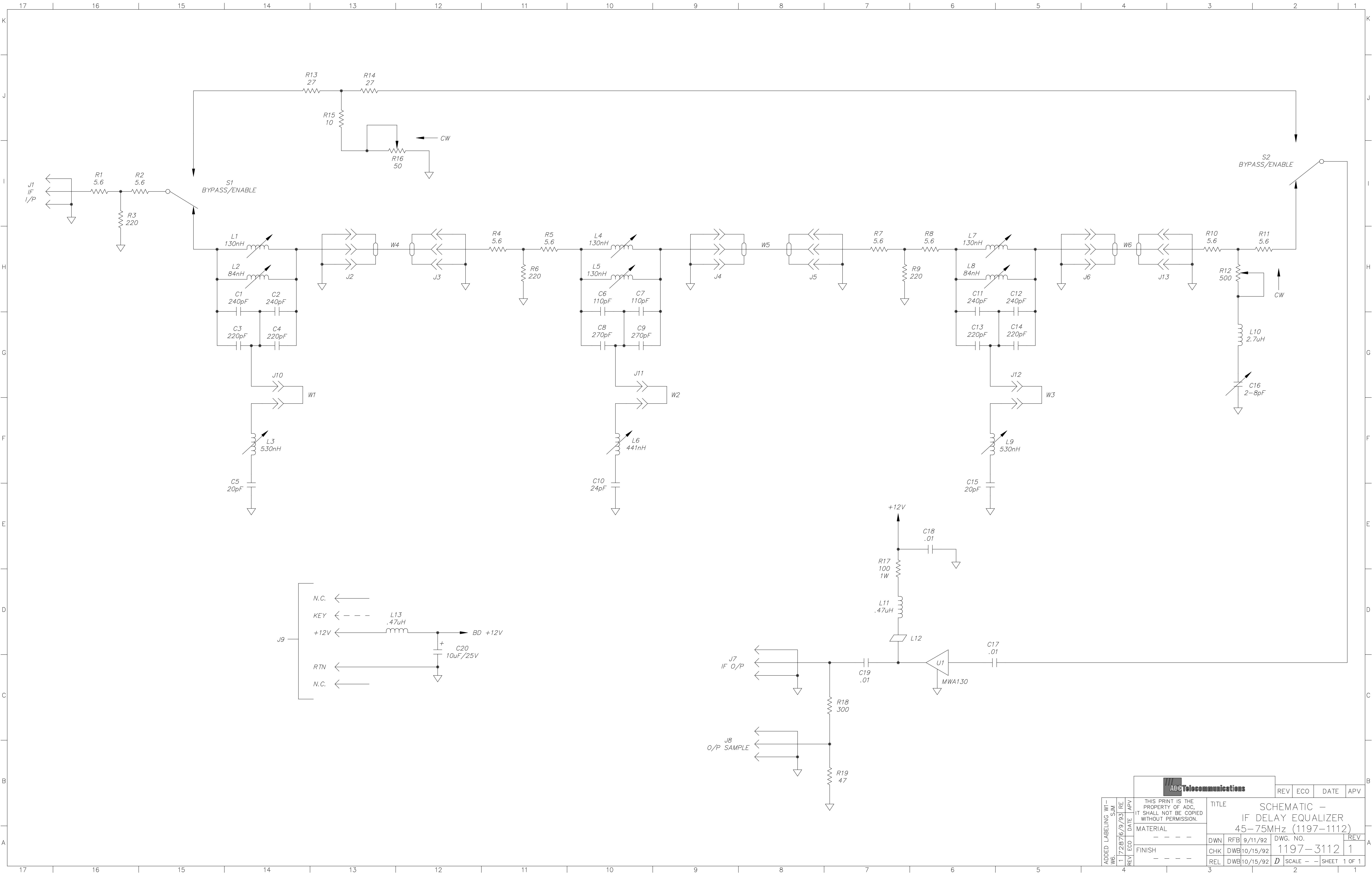
C5 WAS 12PF. R21 WAS 100
SJM

2	7221	1/8/93	RE
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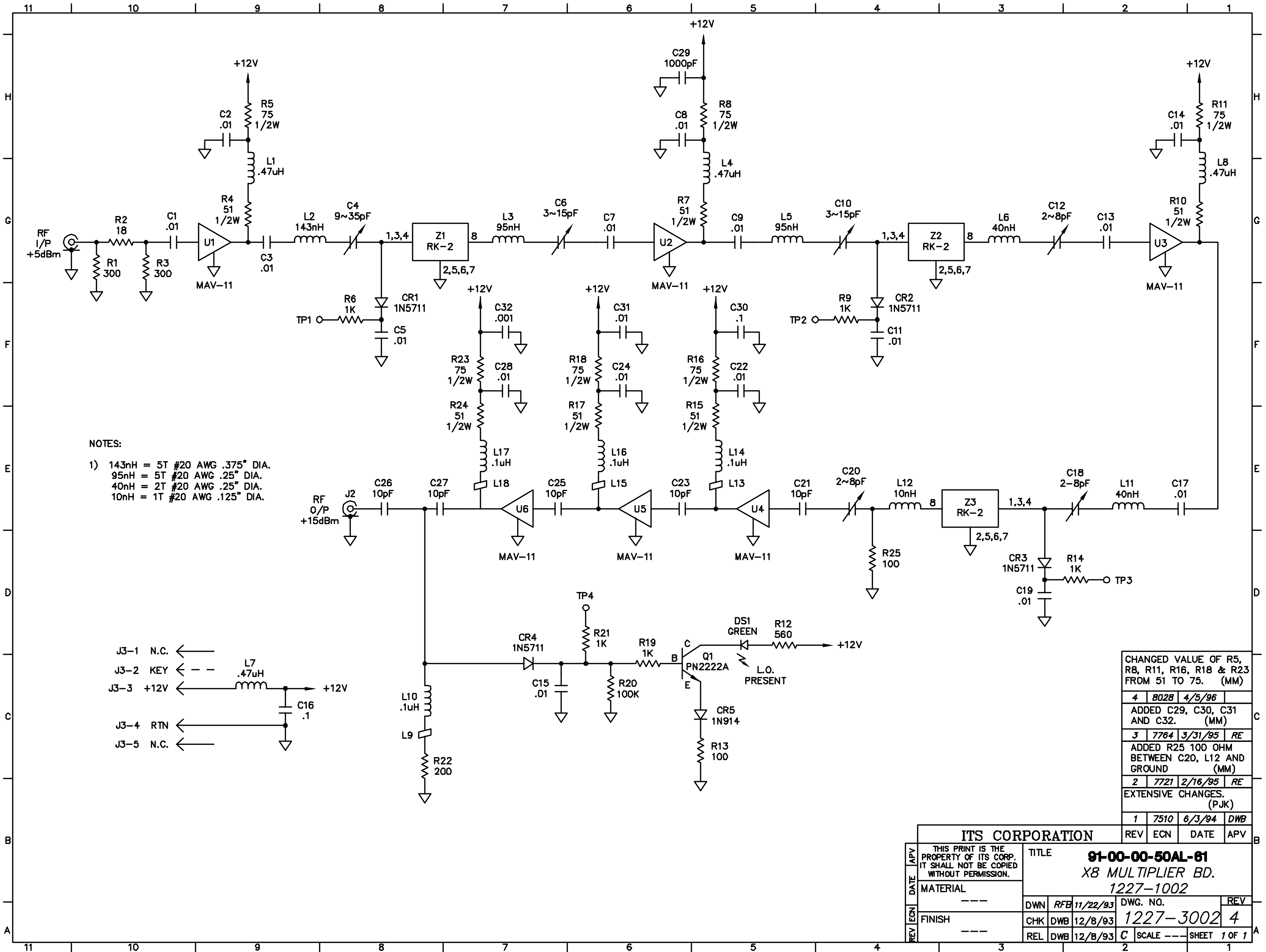
ADDED C23 AND CHANGED
R8 1200 TO R8 1.2K.
(RFB)

1	7167	11/9/92	RE
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ITS CORPORATION				REV	ECN	DATE	APV
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				TITLE			
				91-00-00-50AE-61 IF CARRIER OSC. BD. 45.75MHz (1191-1404)			
MATERIAL				DWN	RFB	9/28/92	DWG. NO.
FINISH				CHK	RE	9/29/92	1191-3404
				REL	RE	9/29/92	C
				SCALE		SHEET 1 OF 1	



				ADCTelecommunications				REV		ECO		DATE		APV												
ADDED LABELING WT - SSM	1 172876/9/93			RE			TITLE			SCHEMATIC -																
	REV			ECO			DATE			IF DELAY EQUALIZER																
	MATERIAL						45-75MHz (1197-1112)																			
	FINISH						DWN			RFB			9/11/92			DWG. NO.			REV							
									CHK			DWB			10/15/92			1197-3112			1					
									REL			DWB			10/15/92			D			SCALE - -			SHEET 1 OF 1		



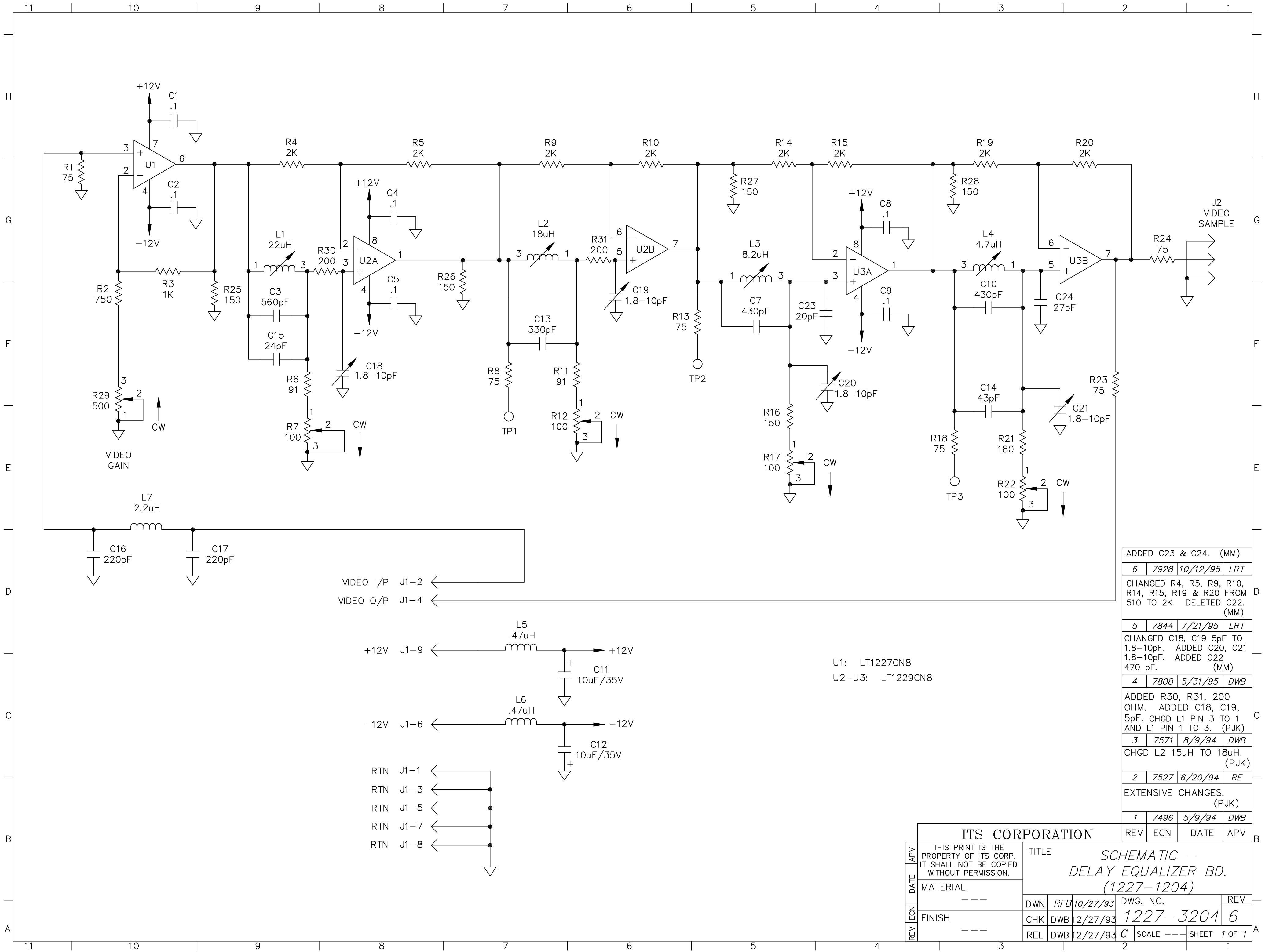
NOTES:
1) 143nH = 5T #20 AWG .375" DIA.
95nH = 5T #20 AWG .25" DIA.
40nH = 2T #20 AWG .25" DIA.
10nH = 1T #20 AWG .125" DIA.

J3-1 N.C. ←
J3-2 KEY ← --
J3-3 +12V ←
J3-4 RTN ←
J3-5 N.C. ←

CHANGED VALUE OF R5, R8, R11, R16, R18 & R23 FROM 51 TO 75. (MM)			
4	8028	4/5/96	
ADDED C29, C30, C31 AND C32. (MM)			
3	7764	3/31/95	RE
ADDED R25 100 OHM BETWEEN C20, L12 AND GROUND (MM)			
2	7721	2/16/95	RE
EXTENSIVE CHANGES. (PJK)			
1	7510	6/3/94	DWB

ITS CORPORATION			
REV	ECN	DATE	APV
1	7510	6/3/94	DWB
2	7721	2/16/95	RE
3	7764	3/31/95	RE
4	8028	4/5/96	

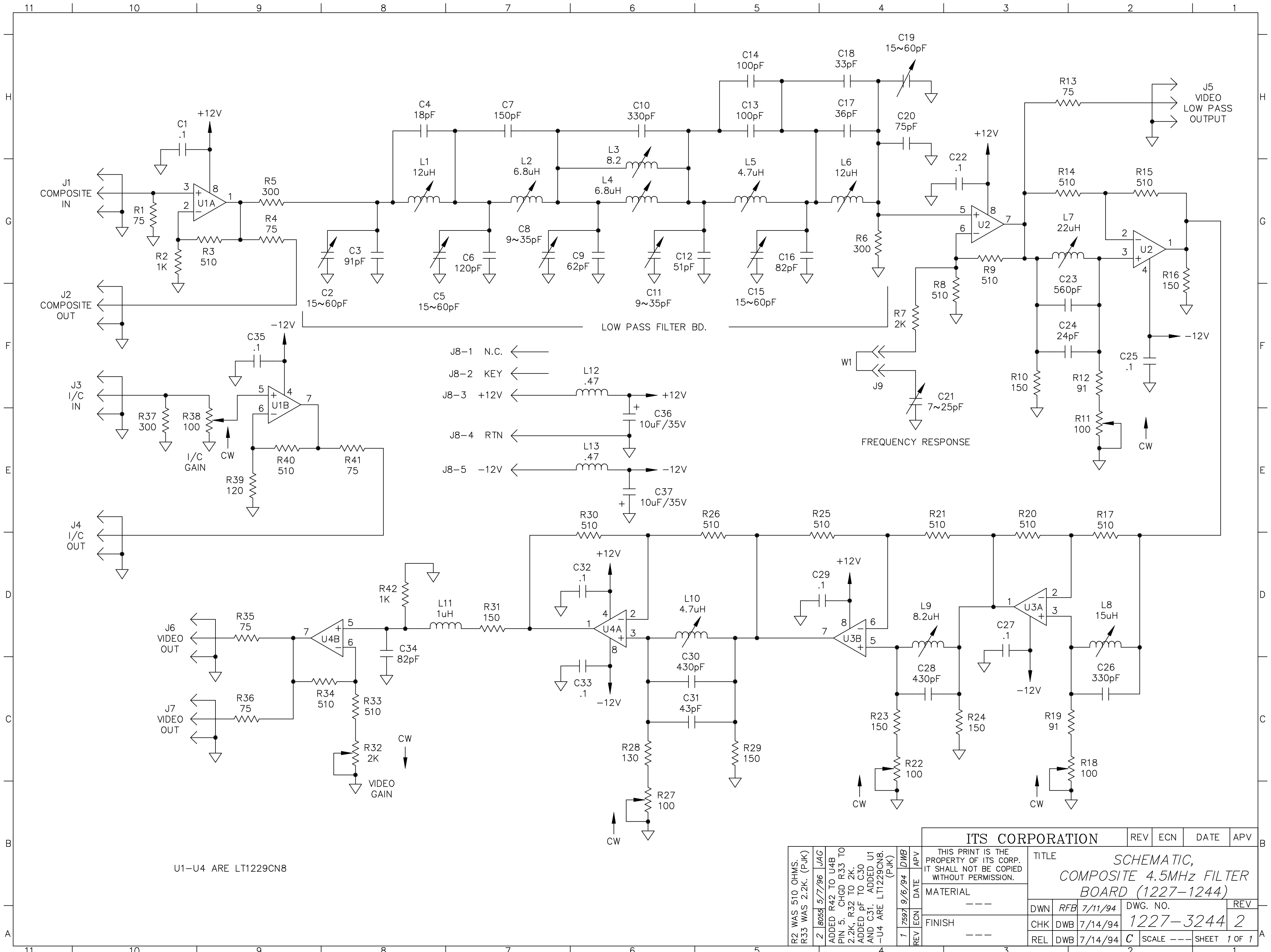
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.		TITLE	
MATERIAL		91-00-00-50AL-61	
FINISH		X8 MULTIPLIER BD.	
		1227-1002	
		1227-3002	
		4	
		1 OF 1	



ADDED C23 & C24. (MM)			
6	7928	10/12/95	LRT
CHANGED R4, R5, R9, R10, R14, R15, R19 & R20 FROM 510 TO 2K. DELETED C22. (MM)			
5	7844	7/21/95	LRT
CHANGED C18, C19 5pF TO 1.8-10pF. ADDED C20, C21 1.8-10pF. ADDED C22 470 pF. (MM)			
4	7808	5/31/95	DWB
ADDED R30, R31, 200 OHM. ADDED C18, C19, 5pF. CHGD L1 PIN 3 TO 1 AND L1 PIN 1 TO 3. (PJK)			
3	7571	8/9/94	DWB
CHGD L2 15uH TO 18uH. (PJK)			
2	7527	6/20/94	RE
EXTENSIVE CHANGES. (PJK)			
1	7496	5/9/94	DWB

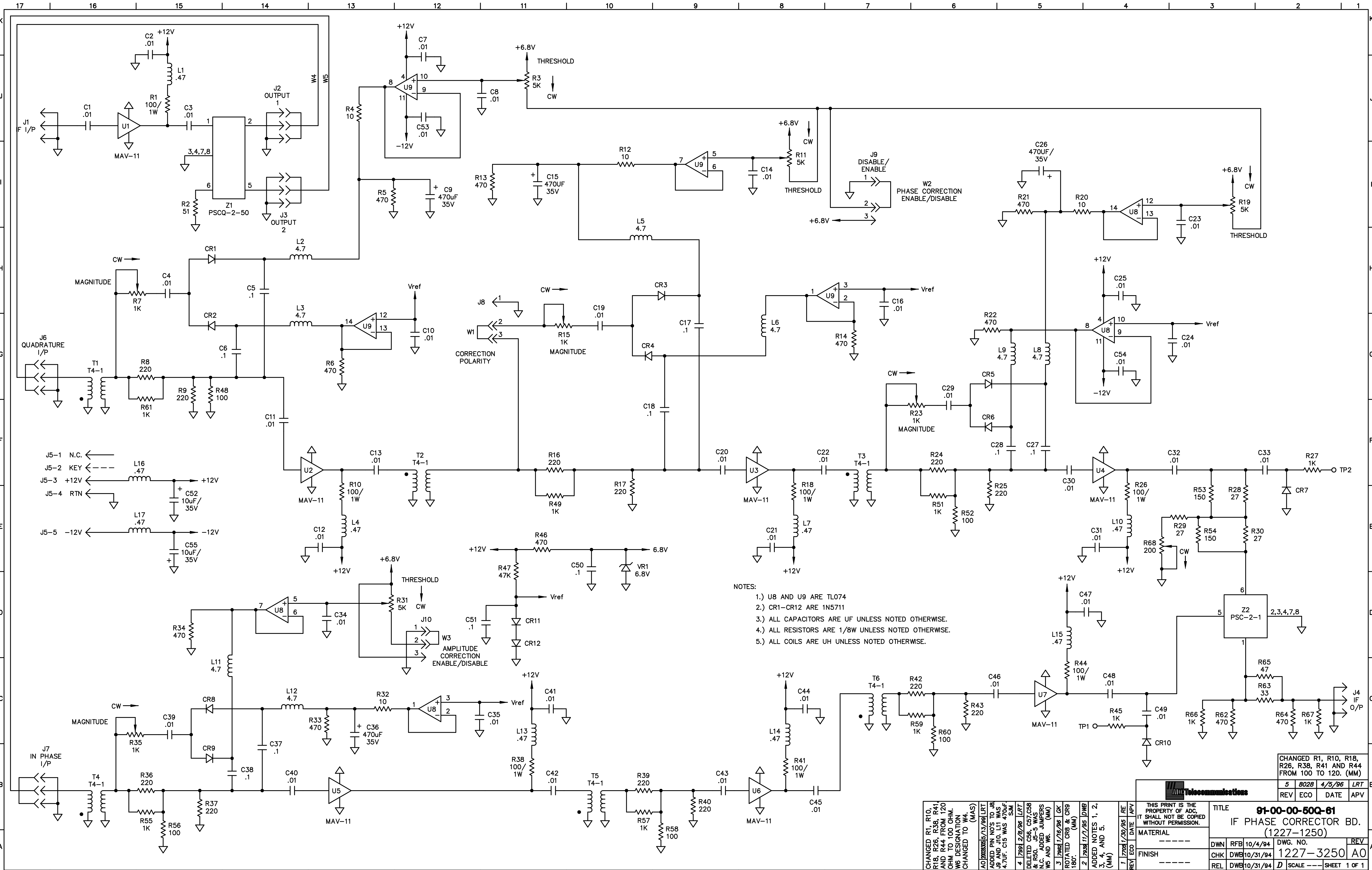
U1: LT1227CN8
U2-U3: LT1229CN8

ITS CORPORATION				REV	ECN	DATE	APV
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				TITLE SCHEMATIC - DELAY EQUALIZER BD. (1227-1204)			
MATERIAL ---				DWN	RFB	10/27/93	REV
FINISH ---				CHK	DWB	12/27/93	1227-3204
				REL	DWB	12/27/93	6
				DWG. NO.		SHEET 1 OF 1	



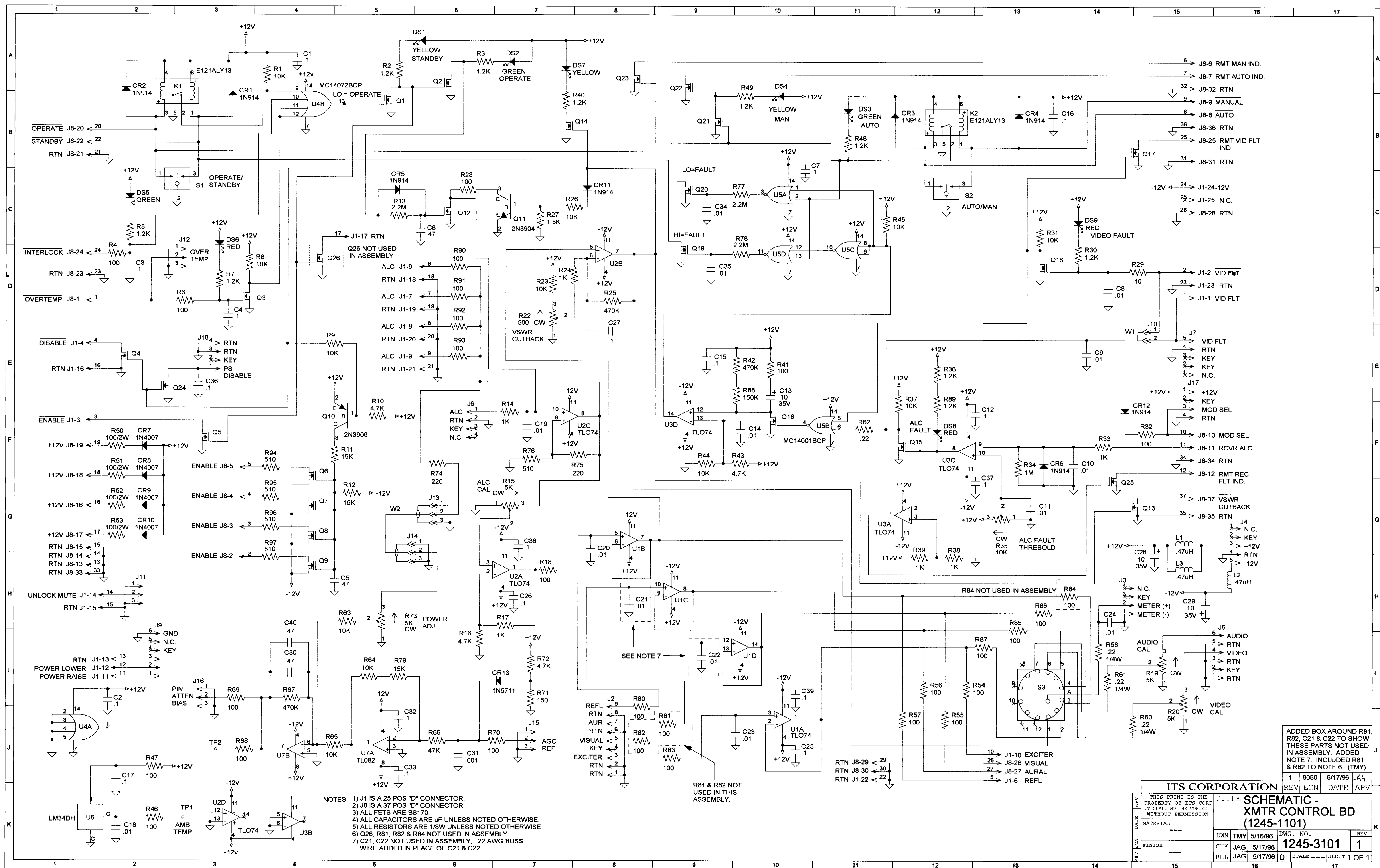
U1-U4 ARE LT1229CN8

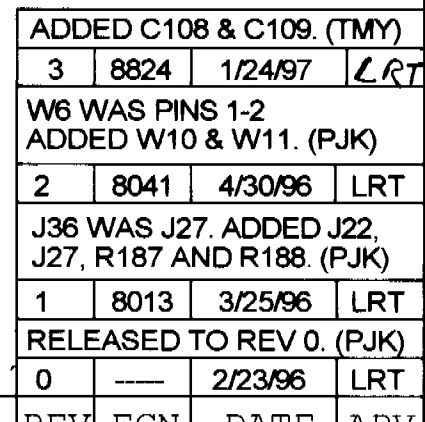
ITS CORPORATION				REV	ECN	DATE	APV
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				TITLE			
				SCHEMATIC, COMPOSITE 4.5MHz FILTER BOARD (1227-1244)			
MATERIAL				DWN	RFB	7/11/94	DWG. NO.
FINISH				CHK	DWB	7/14/94	1227-3244
				REL	DWB	7/14/94	2
				SCALE --- SHEET 1 OF 1			



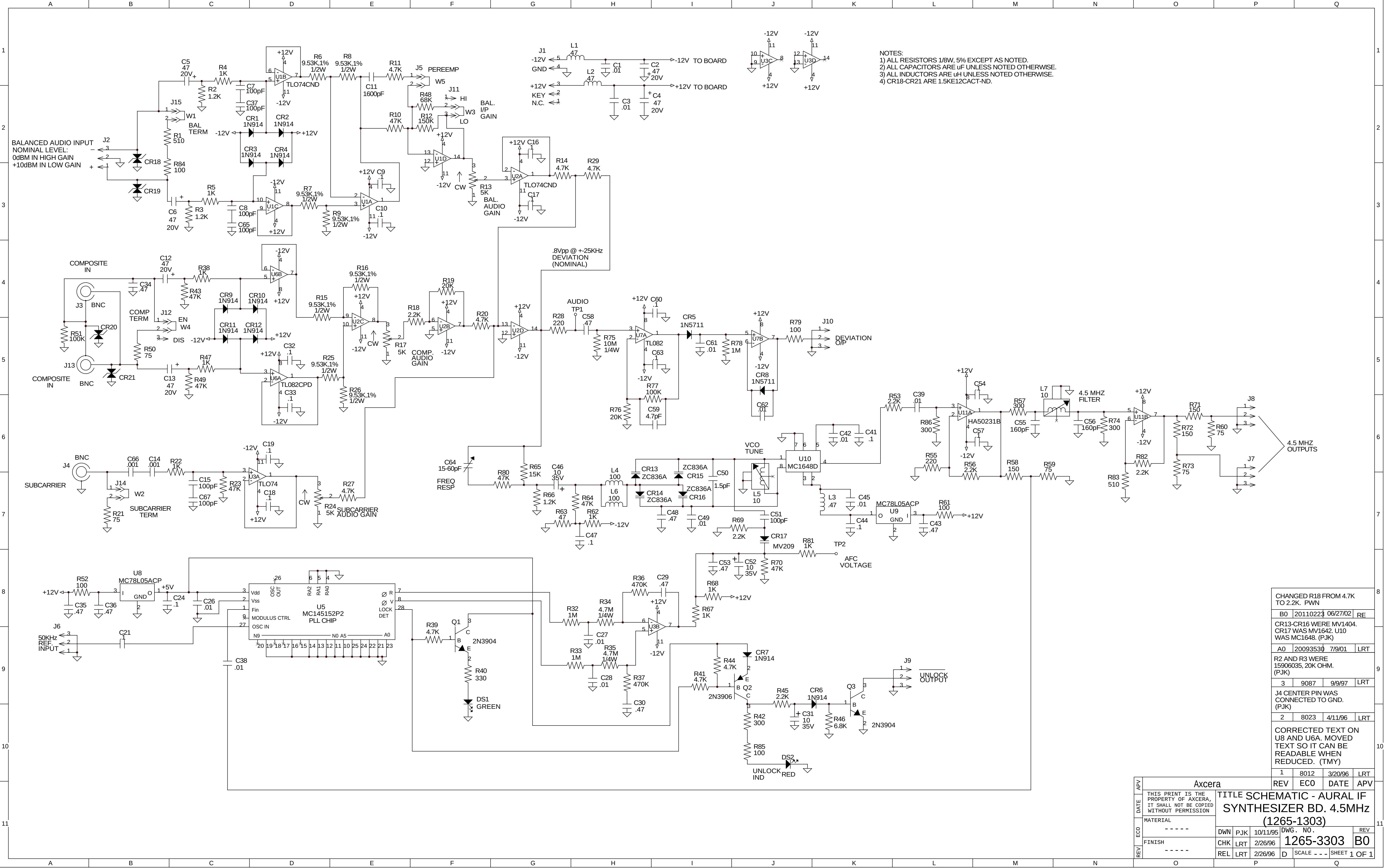
- NOTES:
- 1.) U8 AND U9 ARE TL074
 - 2.) CR1-CR12 ARE 1N5711
 - 3.) ALL CAPACITORS ARE UF UNLESS NOTED OTHERWISE.
 - 4.) ALL RESISTORS ARE 1/8W UNLESS NOTED OTHERWISE.
 - 5.) ALL COILS ARE UH UNLESS NOTED OTHERWISE.

CHANGED R1, R10, R18, R26, R38, R41 AND R44 FROM 120 OHM TO 100 OHM. W6 DESIGNATION CHANGED TO W4. (MAS)										A0 179915/13/99 LRT										ADDED PIN NOS TO J8, J9 AND J10. L11 WAS 470UF. C15 WAS 470UF. SUM										4 17991 2/9/96 LRT										DELETED C56, C57, C58 & R50. J5-5 WAS N.C. ADDED JUMPERS W5 AND W6. (MM)										3 17991 1/16/96 LK										ROTATED CR8 & CR9 180°. (MM)										2 17991 11/1/95 DWB										ADDED NOTES 1, 2, 3, 4, AND 5. (MM)										1 17769 1/30/95 RE										REV ECO DATE APV										THIS PRINT IS THE PROPERTY OF ADG. IT SHALL NOT BE COPIED WITHOUT PERMISSION.										MATERIAL										FINISH										TITLE										91-00-00-50Q-61										IF PHASE CORRECTOR BD. (1227-1250)										REV										DWG. NO.										1227-3250										A0										SHEET 1 OF 1									
CHANGED R1, R10, R18, R26, R38, R41 AND R44 FROM 120 OHM TO 100 OHM. W6 DESIGNATION CHANGED TO W4. (MAS)										A0 179915/13/99 LRT										ADDED PIN NOS TO J8, J9 AND J10. L11 WAS 470UF. C15 WAS 470UF. SUM										4 17991 2/9/96 LRT										DELETED C56, C57, C58 & R50. J5-5 WAS N.C. ADDED JUMPERS W5 AND W6. (MM)										3 17991 1/16/96 LK										ROTATED CR8 & CR9 180°. (MM)										2 17991 11/1/95 DWB										ADDED NOTES 1, 2, 3, 4, AND 5. (MM)										1 17769 1/30/95 RE										REV ECO DATE APV										THIS PRINT IS THE PROPERTY OF ADG. IT SHALL NOT BE COPIED WITHOUT PERMISSION.										MATERIAL										FINISH										TITLE										91-00-00-50Q-61										IF PHASE CORRECTOR BD. (1227-1250)										REV										DWG. NO.										1227-3250										A0										SHEET 1 OF 1									





ITS CORPORATION										REV		ECN		DATE		APV								
REV	DATE	MATERIAL	DWN	PJK	11/1/95	DWG. NO.	REV	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	
REV	DATE	FINISH	CHK	LRT	2/23/96	1265-3302	3	FINISH	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
REV	DATE	REL	LRT	2/23/96	D	SCALE	SHEET	1 OF 1	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30

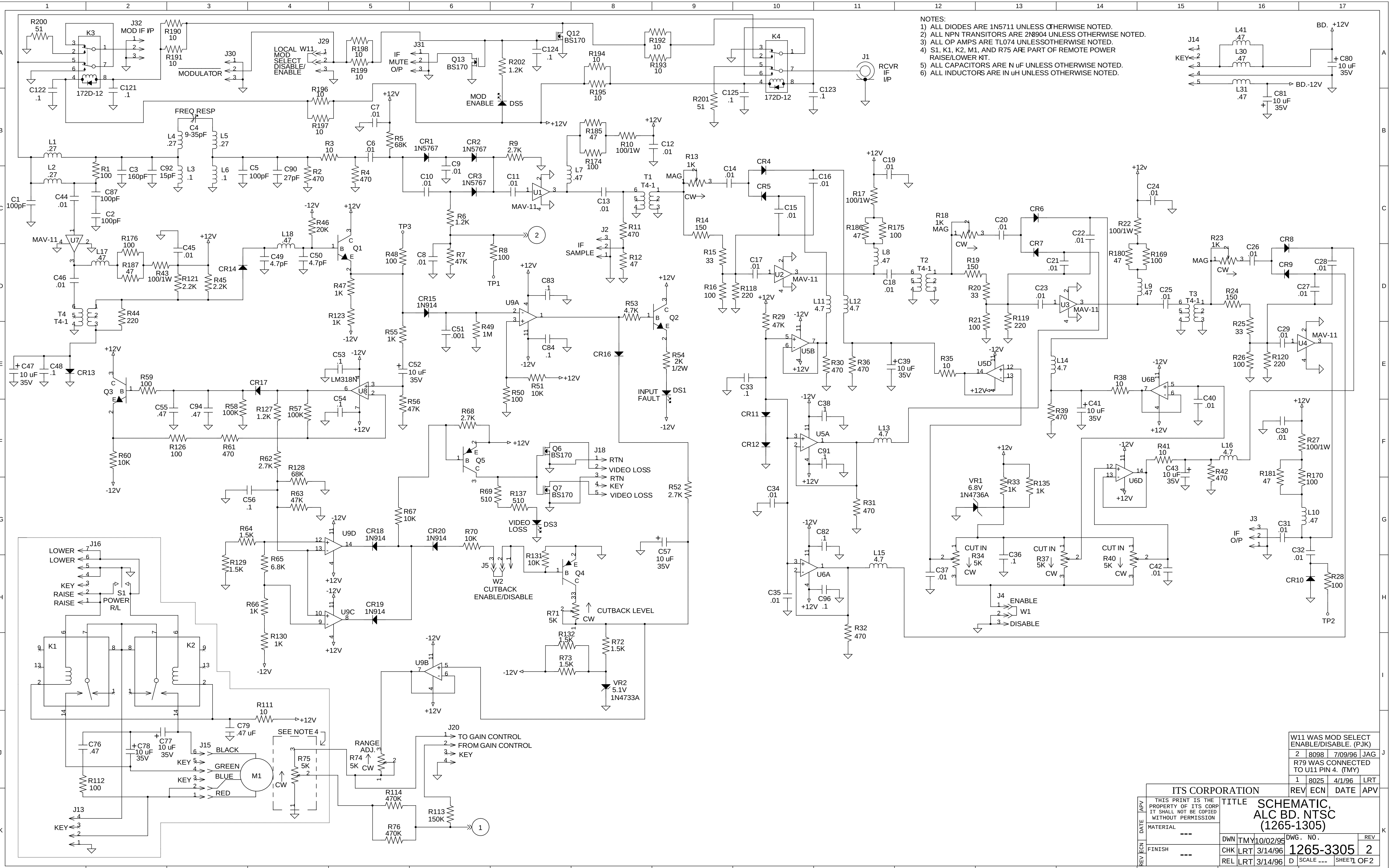
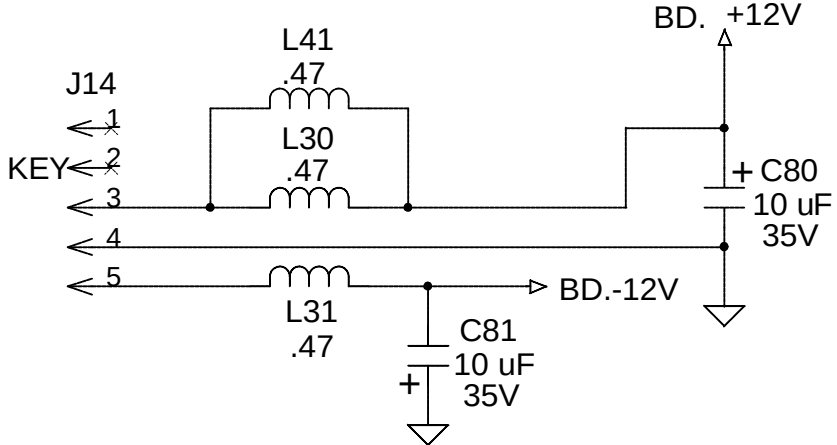


NOTES:
1) ALL RESISTORS 1/8W, 5% EXCEPT AS NOTED.
2) ALL CAPACITORS ARE uF UNLESS NOTED OTHERWISE.
3) ALL INDUCTORS ARE uH UNLESS NOTED OTHERWISE.
4) CR18-CR21 ARE 1.5KE12CACT-ND.

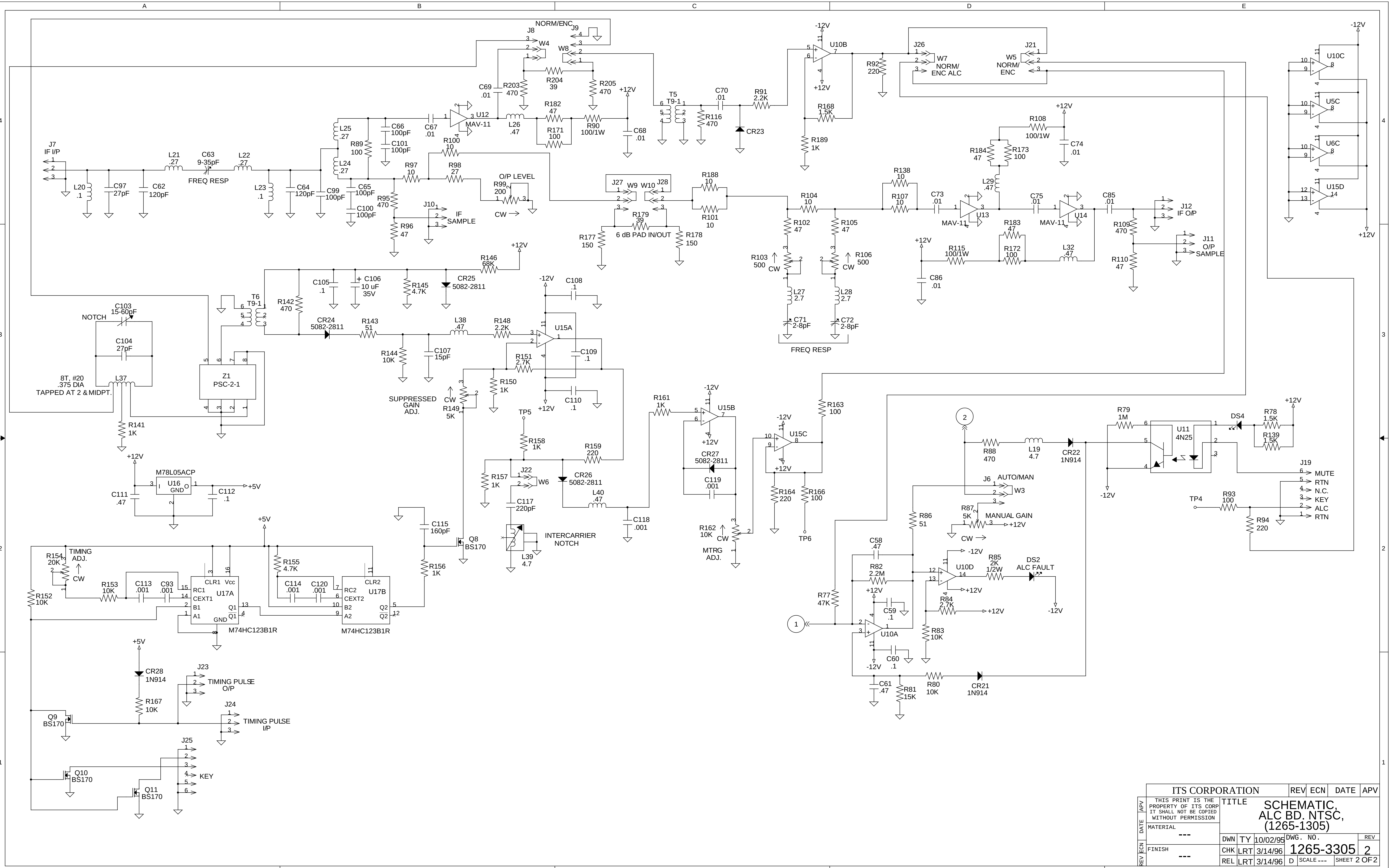
CHANGED R18 FROM 4.7K TO 2.2K. PWN
B0 | 20110223 | 06/27/02 | RE
CR13-CR16 WERE MV1404. CR17 WAS MV1642. U10 WAS MC1648. (PJK)
A0 | 20093530 | 7/9/01 | LRT
R2 AND R3 WERE 15906035, 20K OHM. (PJK)
3 | 9087 | 9/9/97 | LRT
J4 CENTER PIN WAS CONNECTED TO GND. (PJK)
2 | 8023 | 4/11/96 | LRT
CORRECTED TEXT ON U8 AND U6A. MOVED TEXT SO IT CAN BE READABLE WHEN REDUCED. (TMY)

APV	Axcera				REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION				TITLE SCHEMATIC - AURAL IF SYNTHESIZER BD. 4.5MHZ (1265-1303)			
ECO	MATERIAL				DWN	PJK	10/11/95	DWG. NO.
REV	FINISH				CHK	LRT	2/26/96	1265-3303
					REL	LRT	2/26/96	D
					SCALE	- - -	SHEET	1 OF 1

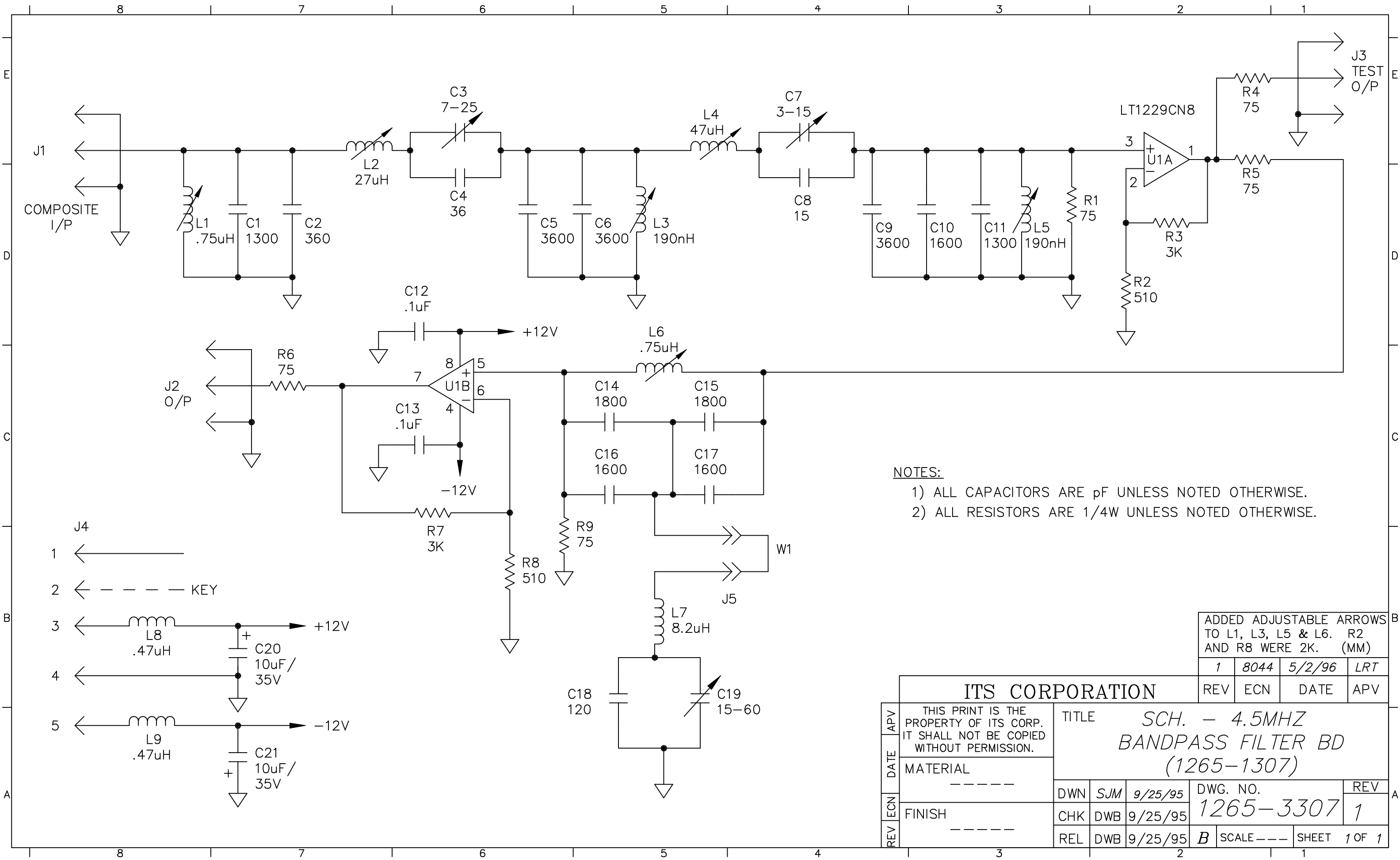
- NOTES:
1) ALL DIODES ARE 1N5711 UNLESS OTHERWISE NOTED.
2) ALL NPN TRANSISTORS ARE 2N8904 UNLESS OTHERWISE NOTED.
3) ALL OP AMPS ARE TL074 UNLESS OTHERWISE NOTED.
4) S1, K1, K2, M1, AND R75 ARE PART OF REMOTE POWER RAISE/LOWER KIT.
5) ALL CAPACITORS ARE N uF UNLESS OTHERWISE NOTED.
6) ALL INDUCTORS ARE IN uH UNLESS OTHERWISE NOTED.

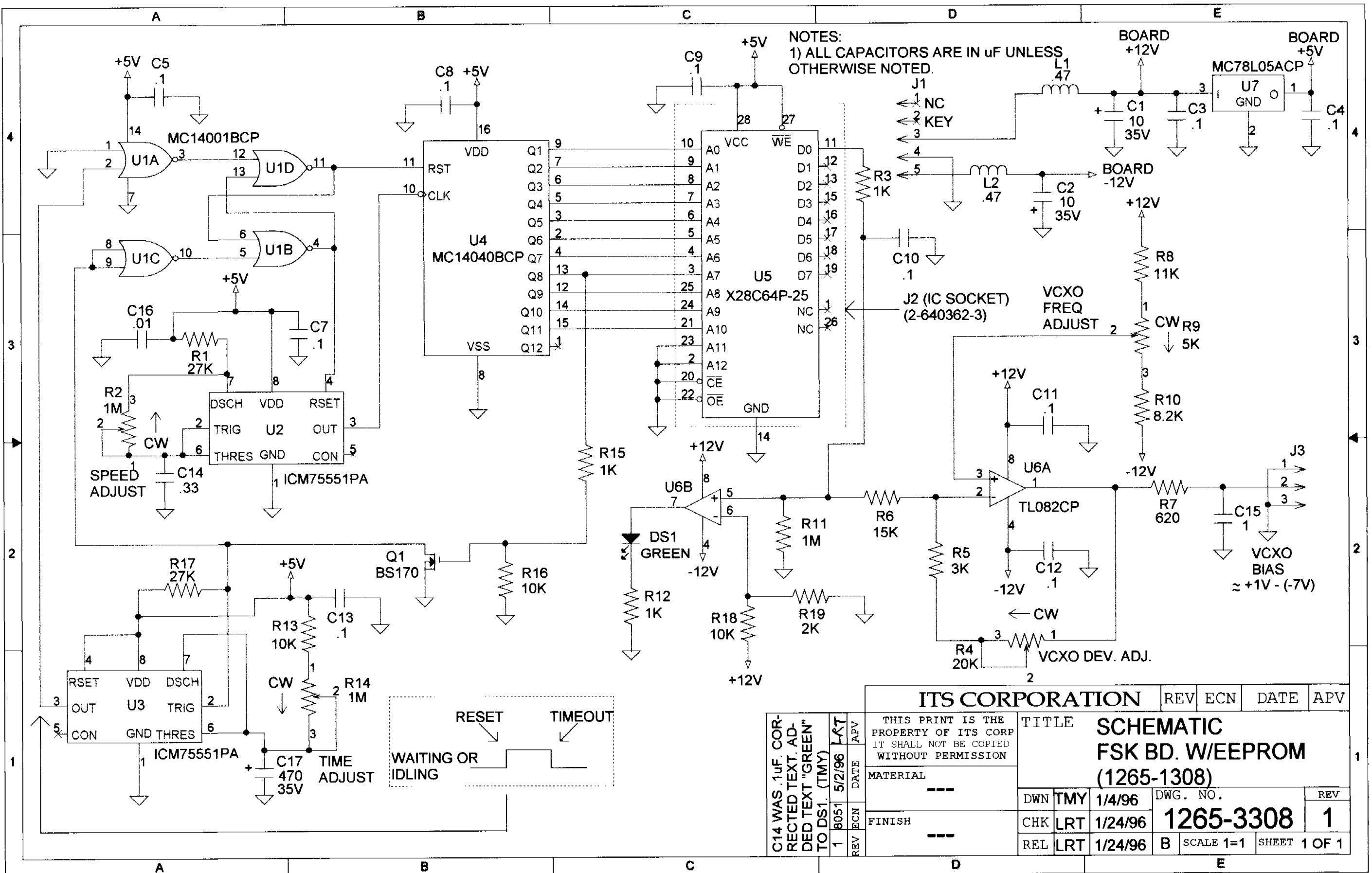


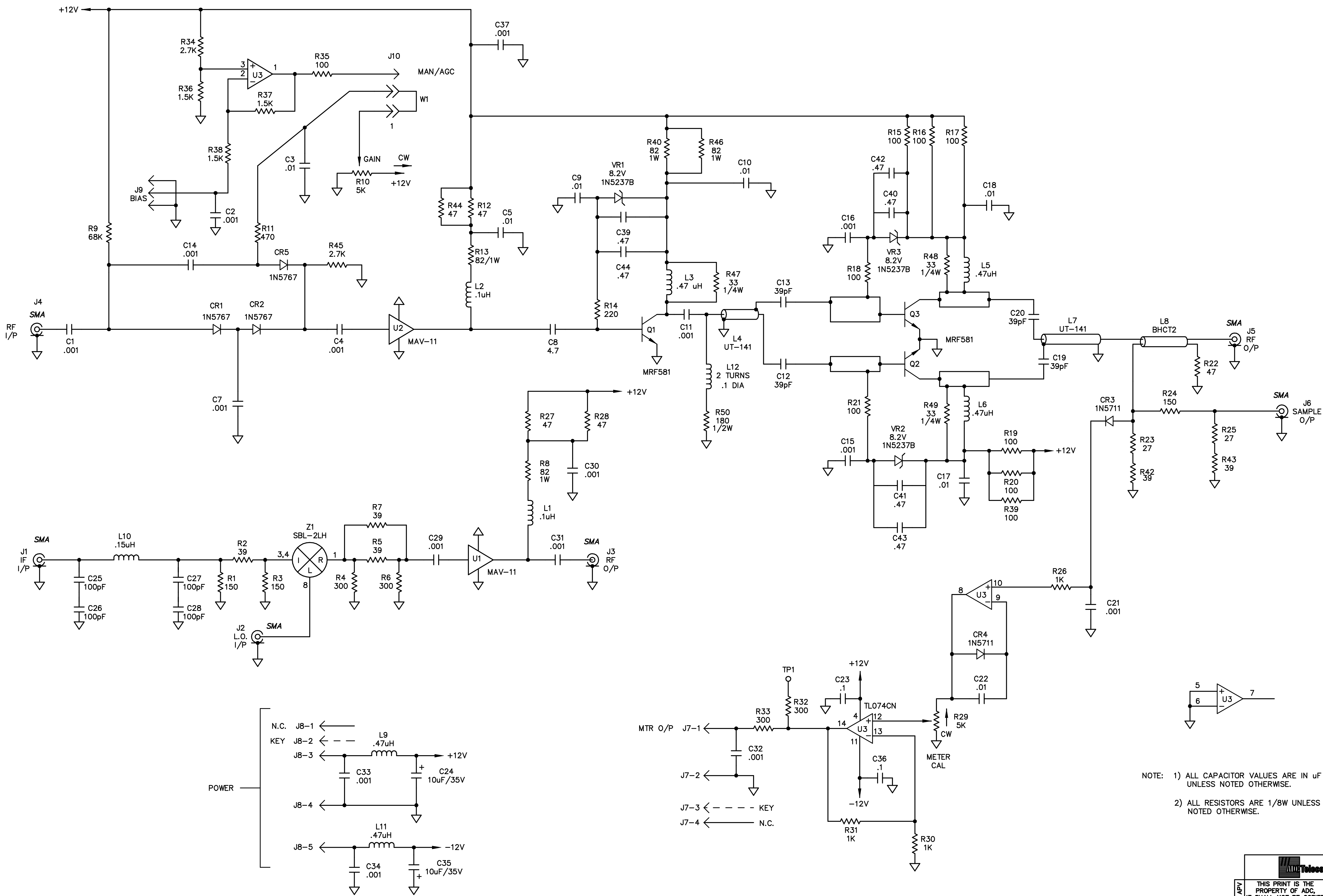
THIS PRINT IS THE PROPERTY OF ITS CORP IT SHALL NOT BE COPIED WITHOUT PERMISSION				TITLE SCHEMATIC, ALC BD. NTSC (1265-1305)			
APV	DATE	MATERIAL	---	DWN	TMY	10/02/96	DWG. NO.
REV	ECN	FINISH	---	CHK	LRT	3/14/96	1265-3305
REV				REL			
1				2			
8025				4/1/96			
LRT				APV			
DATE				SHEET 1 OF 2			
REV				SCALE			
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ITS CORPORATION				REV	ECN	DATE	APV
APV	THIS PRINT IS THE PROPERTY OF ITS CORP IT SHALL NOT BE COPIED WITHOUT PERMISSION			TITLE			
DATE	MATERIAL			SCHEMATIC, ALC BD. NTSC, (1265-1305)			
REV	ECN	FINISH	CHK	LRT	10/02/95	DWG. NO.	REV
---	---	---	---	---	---	1265-3305	2
---	---	---	---	---	---	1265-3305	2
---	---	---	---	---	---	1265-3305	2

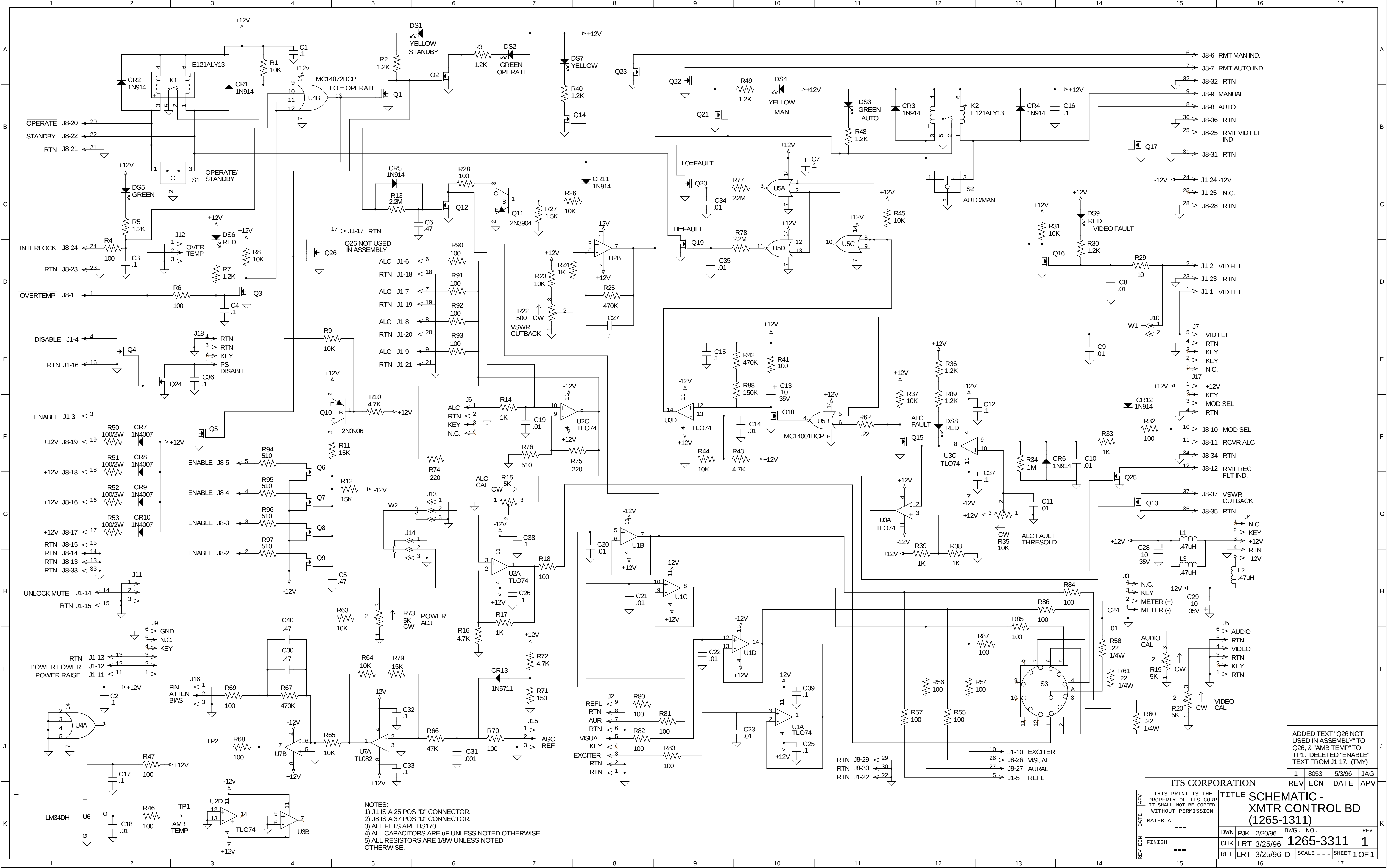


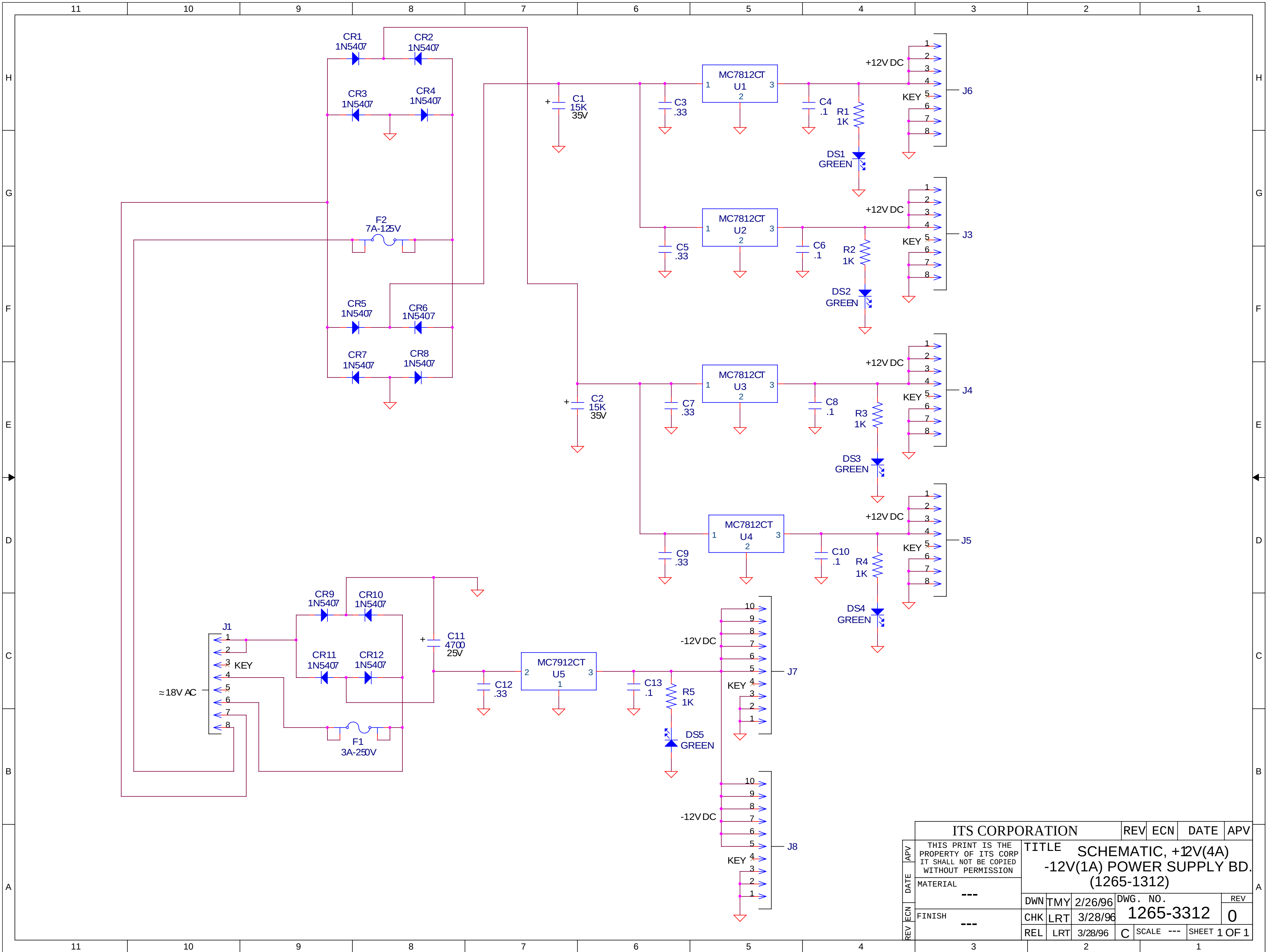




NOTE: 1) ALL CAPACITOR VALUES ARE IN uF UNLESS NOTED OTHERWISE.
2) ALL RESISTORS ARE 1/8W UNLESS NOTED OTHERWISE.

ADDED C42-C44, L12, R47-R50. C39 - C41 WERE 1uF. REMOVED C6 FROM THE OUTPUT OF U2. C8 WAS 3pF. (PJK)			
A0	20028596	6/1/99	LRT
CHANGED C8 FROM 1.8-10pF TO 3pF. ADDED R46 AND DELETED C38. (RFB)			
4	9279	1/23/98	LTR
ADDED IT-5 CY30C105P CAP AT C39. C40 AND C41 IN PARALLEL WITH VR1, VR3 AND VR2. (MAS)			
3	8816	1/23/97	LRT
DELETED R41 AND ADDED C38. (RFB)			
2	8635	11/18/96	LRT
ADDED "SMA" TO J1-J3, J5, J6. ADDED "uH" TO L1. ADDED "KEY" TO J7-3. "N.C." TO J7-4. C8 WAS .8-10pF. (PJK)			
1	8035	4/11/96	LRT
REV	ECO	DATE	APV
TITLE 91-00-00-50R-61 UHF UPCONVERTER BD.			
APV	THIS PRINT IS THE PROPERTY OF ADC. IT SHALL NOT BE COPIED WITHOUT PERMISSION.		
DATE	MATERIAL		
REV	ECO	FINISH	---
DWN	PJK	1/25/96	DWG. NO.
CHK	LRT	3/14/96	1265-3310
REL	LRT	3/14/96	SCALE 1=1 SHEET 1 OF 1
REV	ECO	DATE	APV

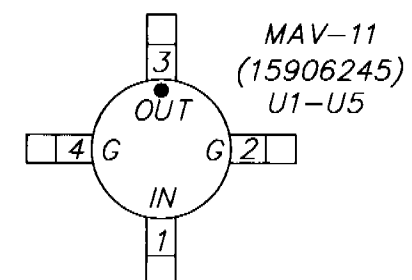




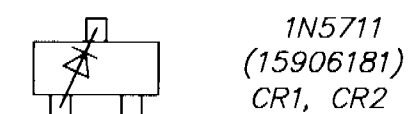
ITS CORPORATION				REV	ECN	DATE	APV
THIS PRINT IS THE PROPERTY OF ITS CORP IT SHALL NOT BE COPIED WITHOUT PERMISSION				TITLE SCHEMATIC, +12V(4A) -12V(1A) POWER SUPPLY BD. (1265-1312)			
				MATERIAL ---			
FINISH ---				DWN	TMY	2/26/96	DWG. NO.
				CHK	LRT	3/28/96	1265-3312
				REL	LRT	3/28/96	C
				SCALE --- SHEET 1 OF 1			

JUMPERABLE GAIN SETTINGS			
IF I/P RANGE	J4,J5	J6,J7	
-11 TO -6 dBm	LOW	LOW	
-16 TO -11 dBm	LOW	HIGH	
-21 TO -16 dBm	HIGH	LOW	
-26 TO -21 dBm	HIGH	HIGH	

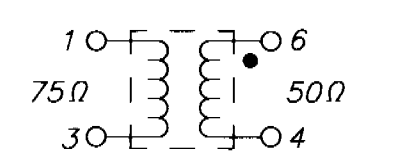
MAV-11 PACKAGE REFERENCE



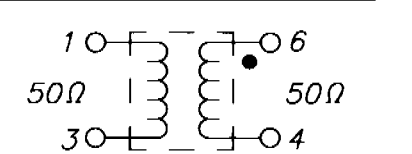
SOT-23 PACKAGE REFERENCE



TI-5-1 PACKAGE REFERENCE

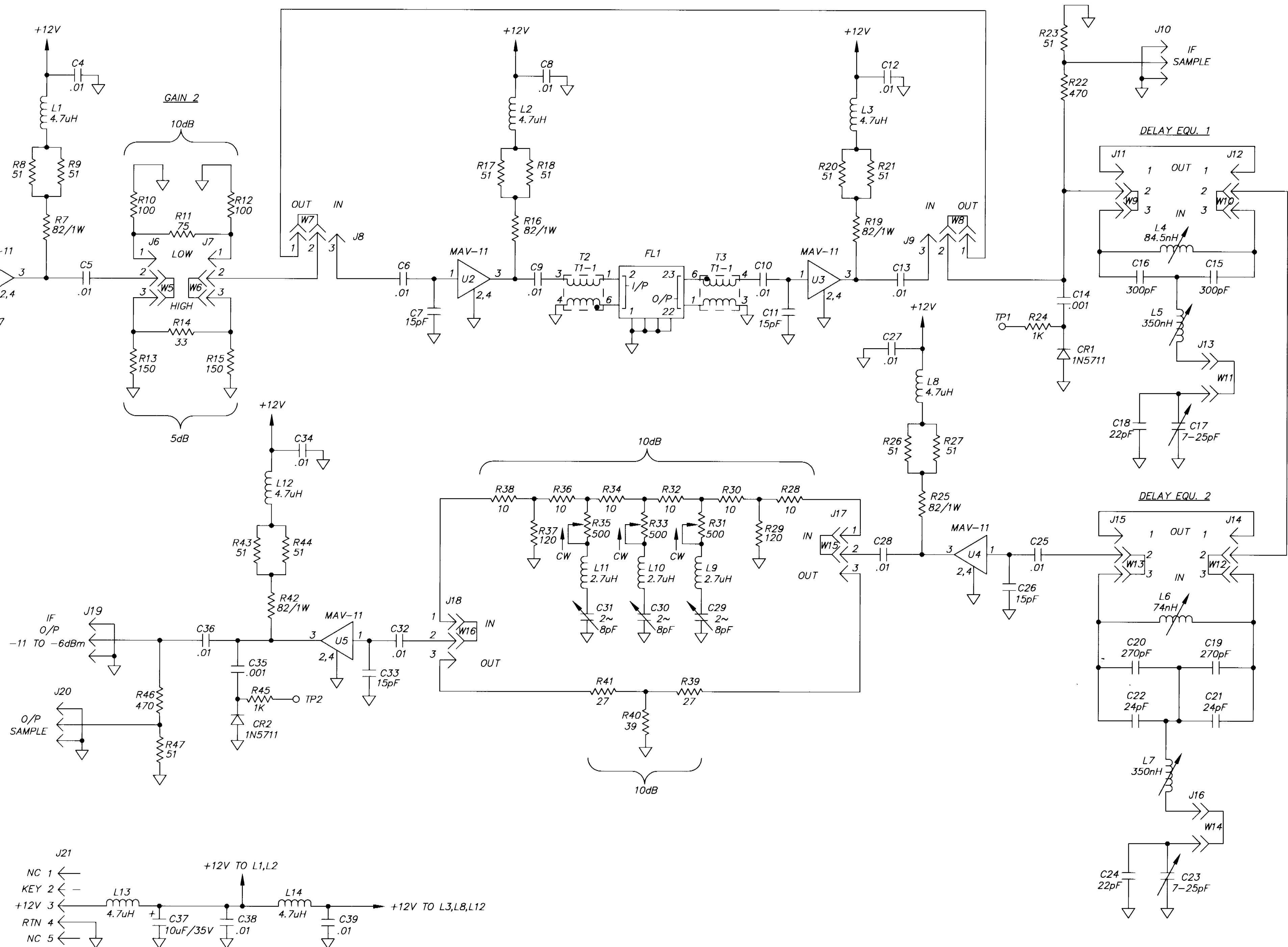


TI-1 PACKAGE REFERENCE



NOTES:

- 1) ALL CAPACITORS ARE IN MICRO FARADS AND ALL RESISTORS ARE IN OHMS UNLESS NOTED.



ITS CORPORATION				REV	ECN	DATE	APV
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				TITLE			
MATERIAL				SCH, IF I/P EQUALIZER BD., 44 MHZ (1555-1219)			
FINISH				DWN	RCB	1/3/96	DWG. NO.
				CHK	BKW	1/30/96	1555-3219
				REL	BKW	1/30/96	1
				SCALE --- SHEET 1 OF 1			