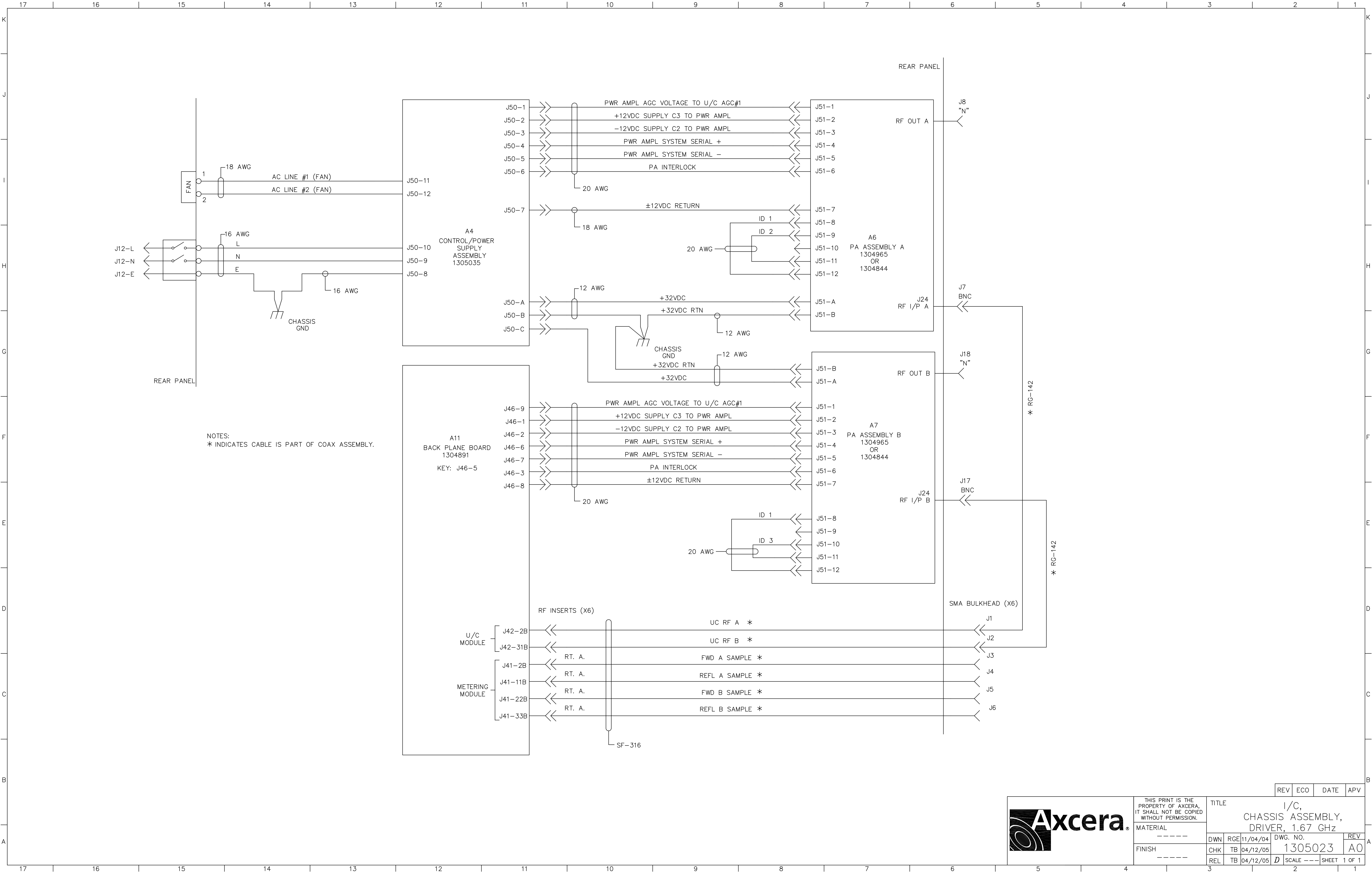
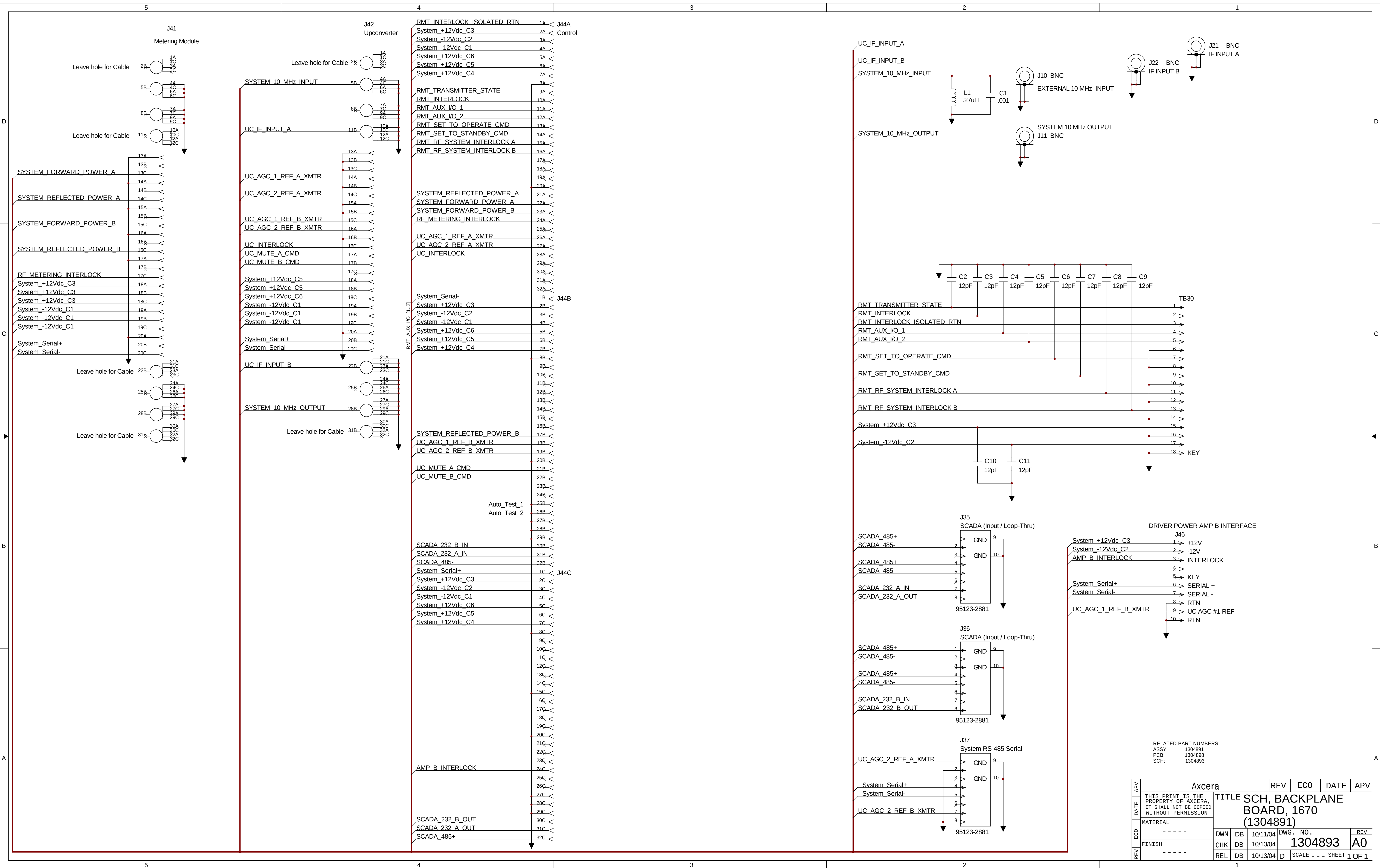
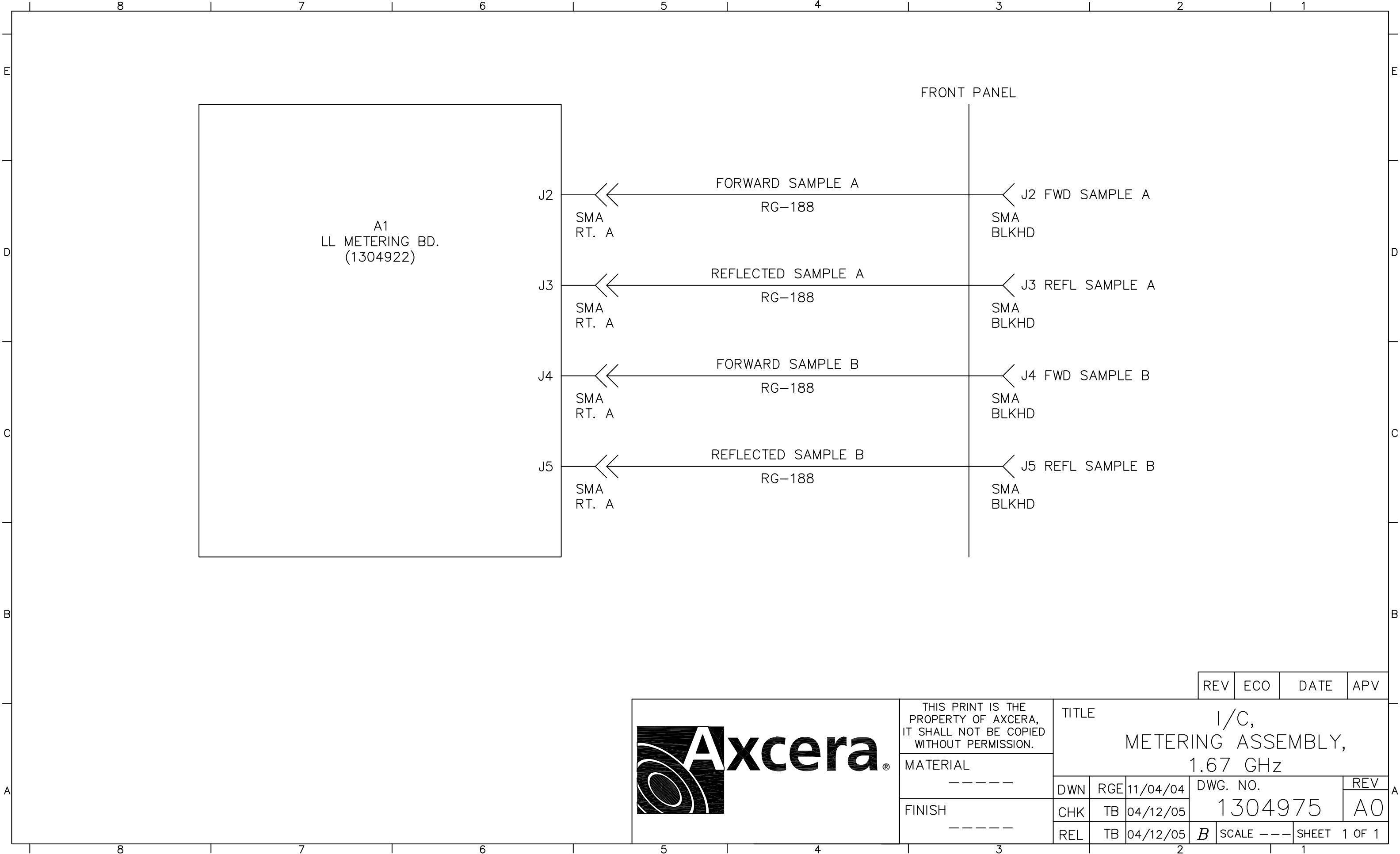
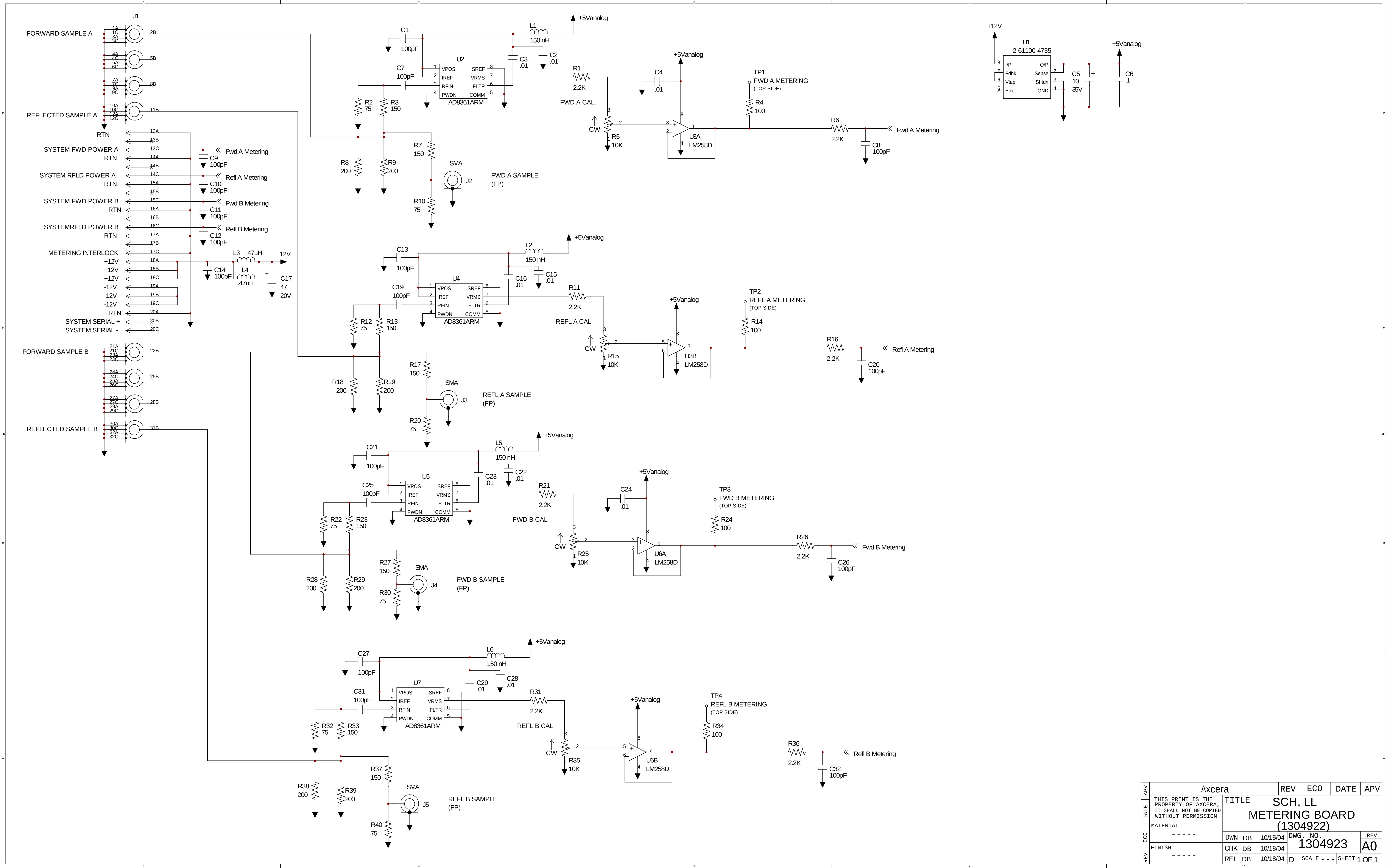


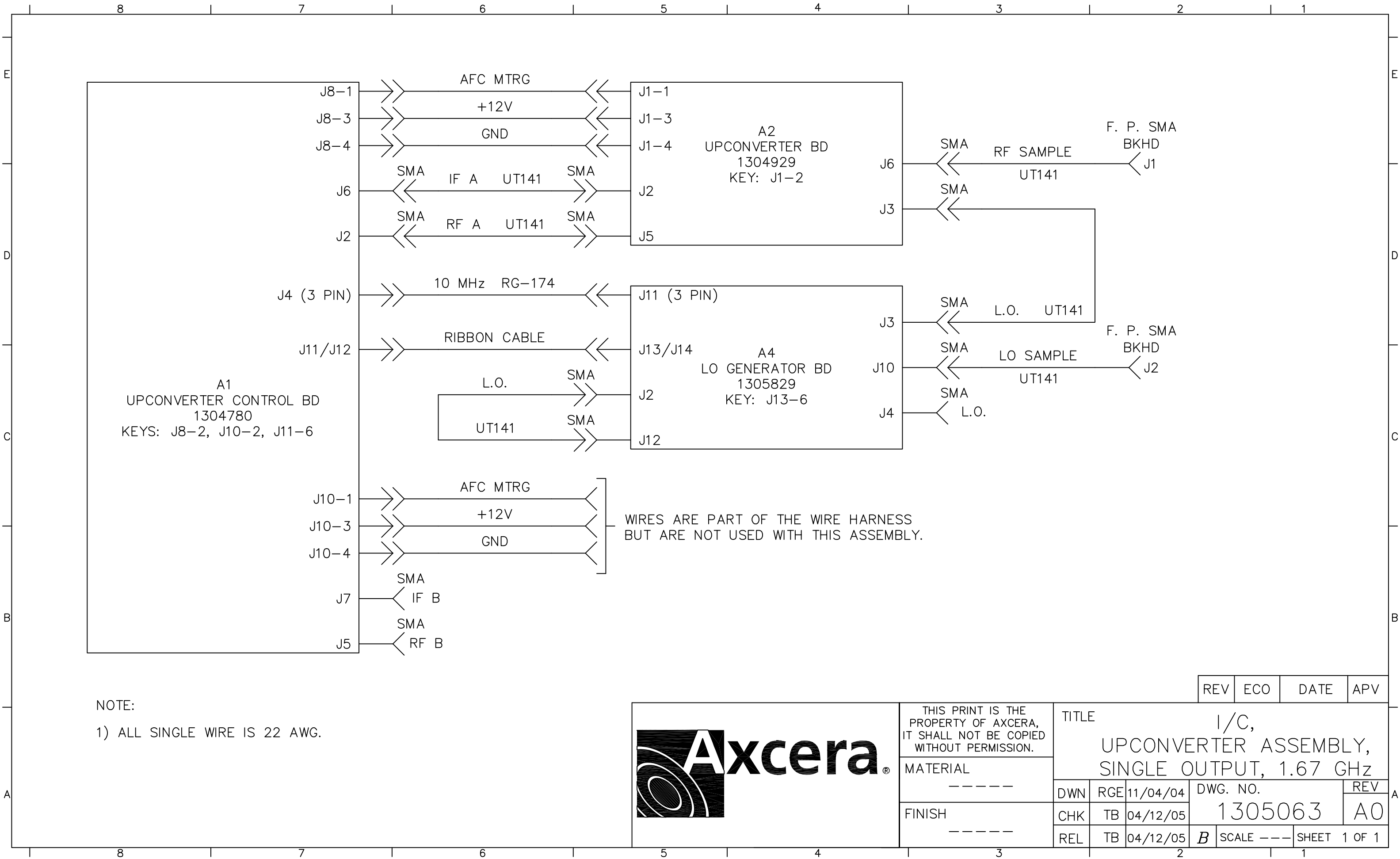








APV	Axcera				REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION				TITLE SCH, LL METERING BOARD (1304922)			
ECO					DWN	DB	10/15/04	DWG. NO. 1304923
REV					CHK	DB	10/18/04	REV A0
					REL	DB	10/18/04	SCALE - - - SHEET 1 OF 1



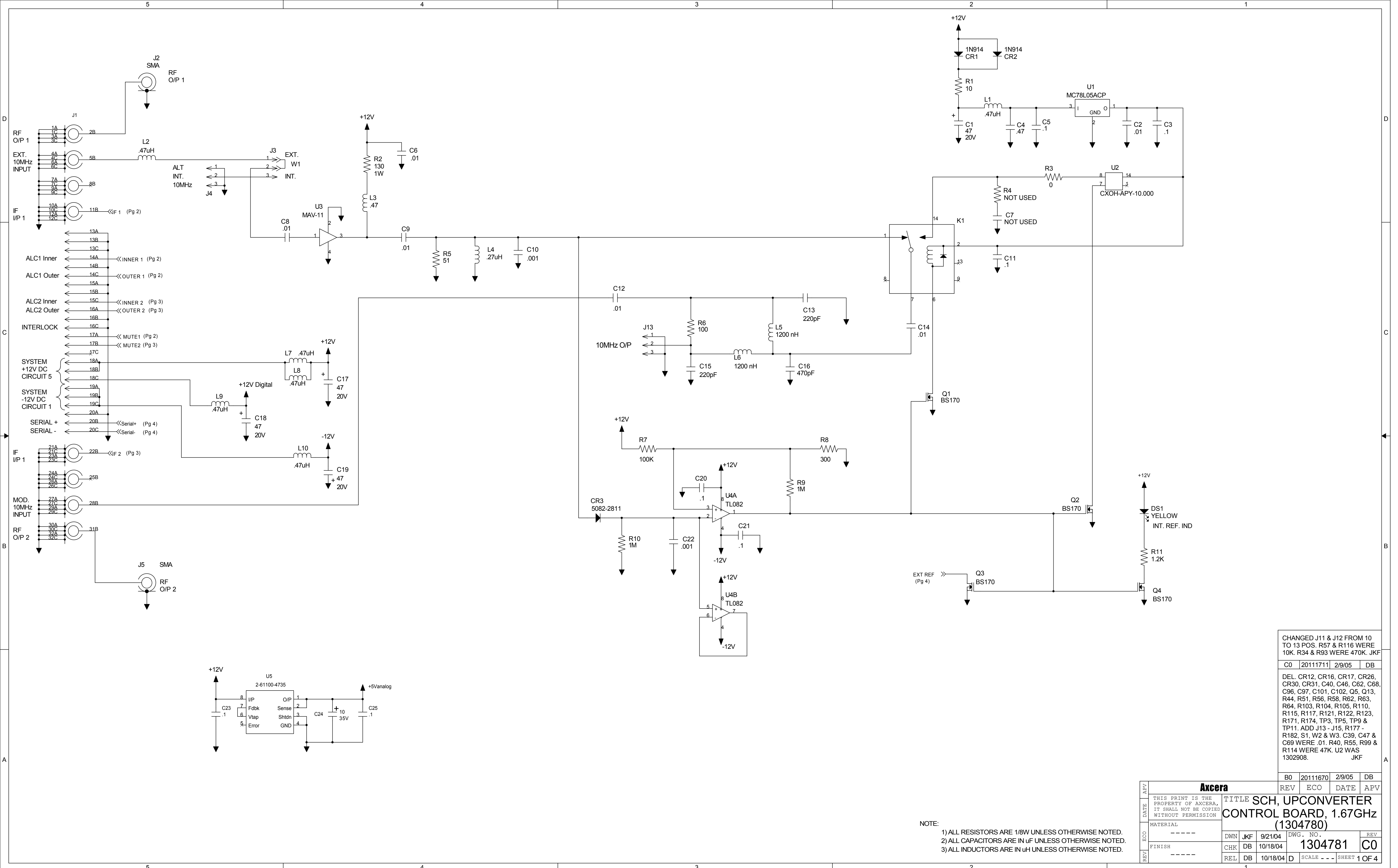
THIS PRINT IS THE
PROPERTY OF AXCERA,
IT SHALL NOT BE COPIED
WITHOUT PERMISSION.

MATERIAL

FINISH

TITLE
I/C,
UPCONVERTER ASSEMBLY,
SINGLE OUTPUT, 1.67 GHz

REV	ECO	DATE	APV
DWN	RGE	11/04/04	REV
CHK	TB	04/12/05	1305063
REL	TB	04/12/05	B SCALE --- SHEET 1 OF 1



CHANGED J11 & J12 FROM 10 TO 13 POS. R57 & R116 WERE 10K. R34 & R93 WERE 470K. JKF

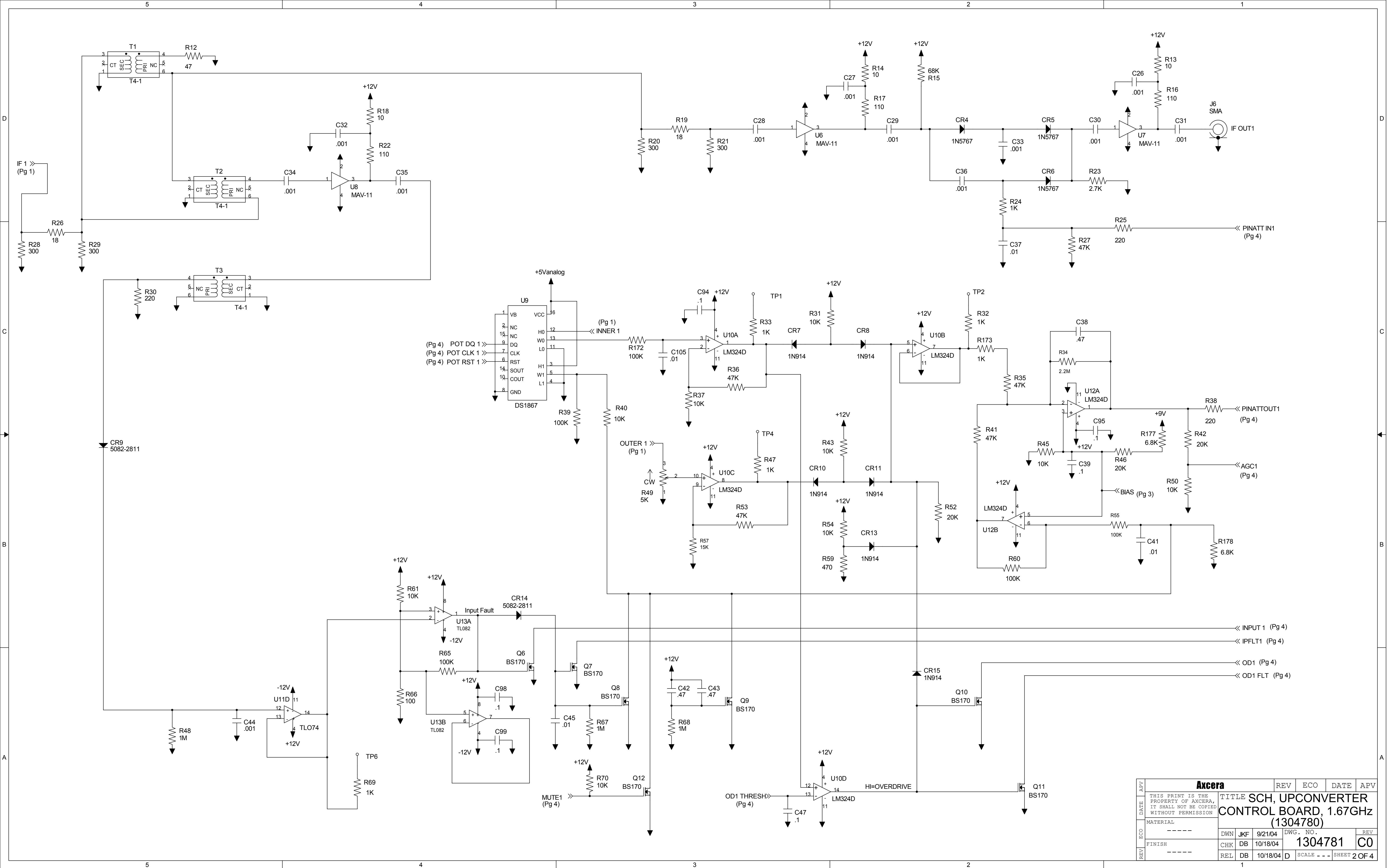
C0	20111711	2/9/05	DB
----	----------	--------	----

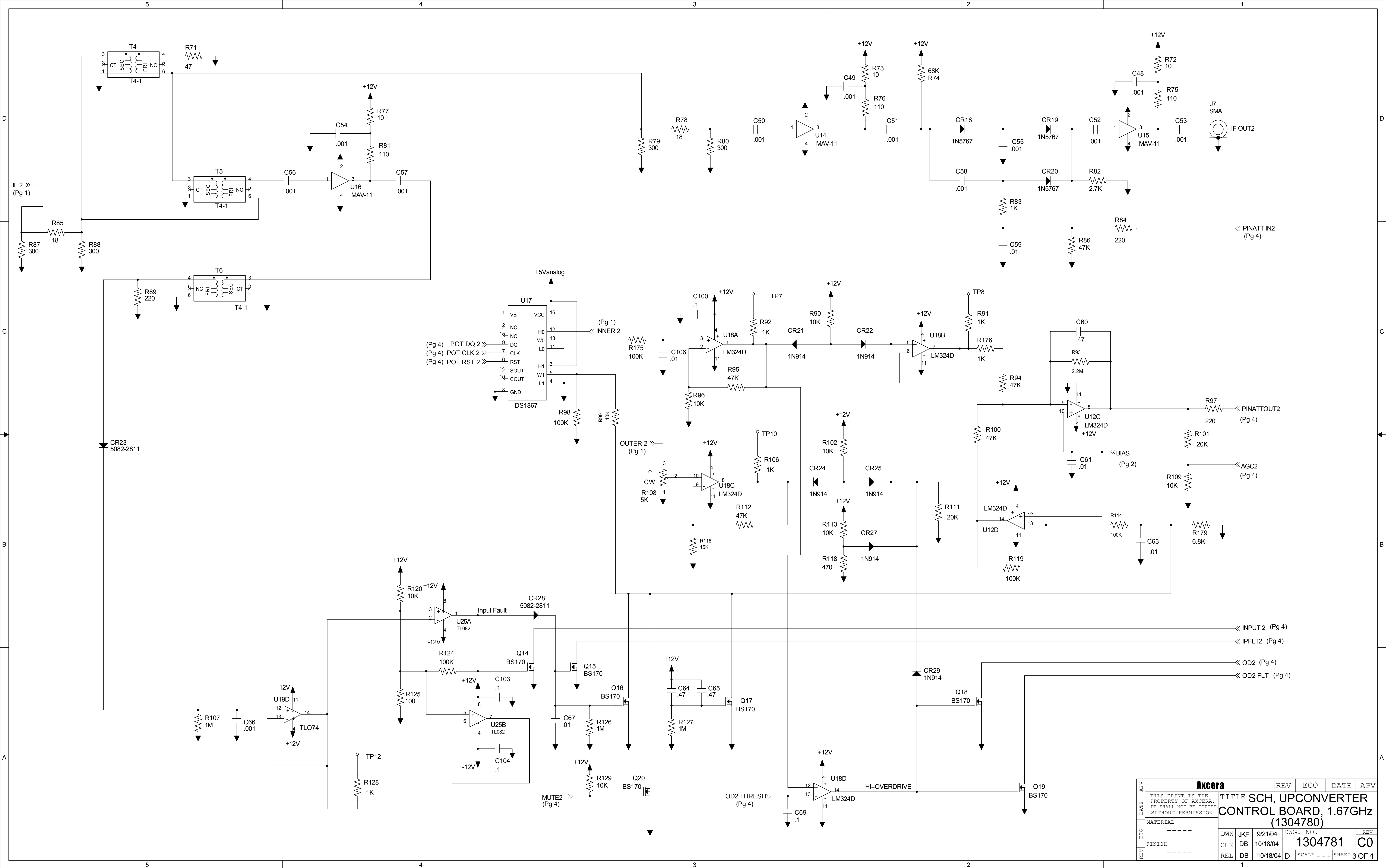
DEL. CR12, CR16, CR17, CR26, CR30, CR31, C40, C46, C62, C68, C96, C97, C101, C102, Q5, Q13, R44, R51, R56, R58, R62, R63, R64, R103, R104, R105, R110, R115, R117, R121, R122, R123, R171, R174, TP3, TP5, TP9 & TP11. ADD J13 - J15, R177 - R182, S1, W2 & W3. C39, C47 & C69 WERE .01. R40, R55, R99 & R114 WERE 47K. U2 WAS 1302908. JKF

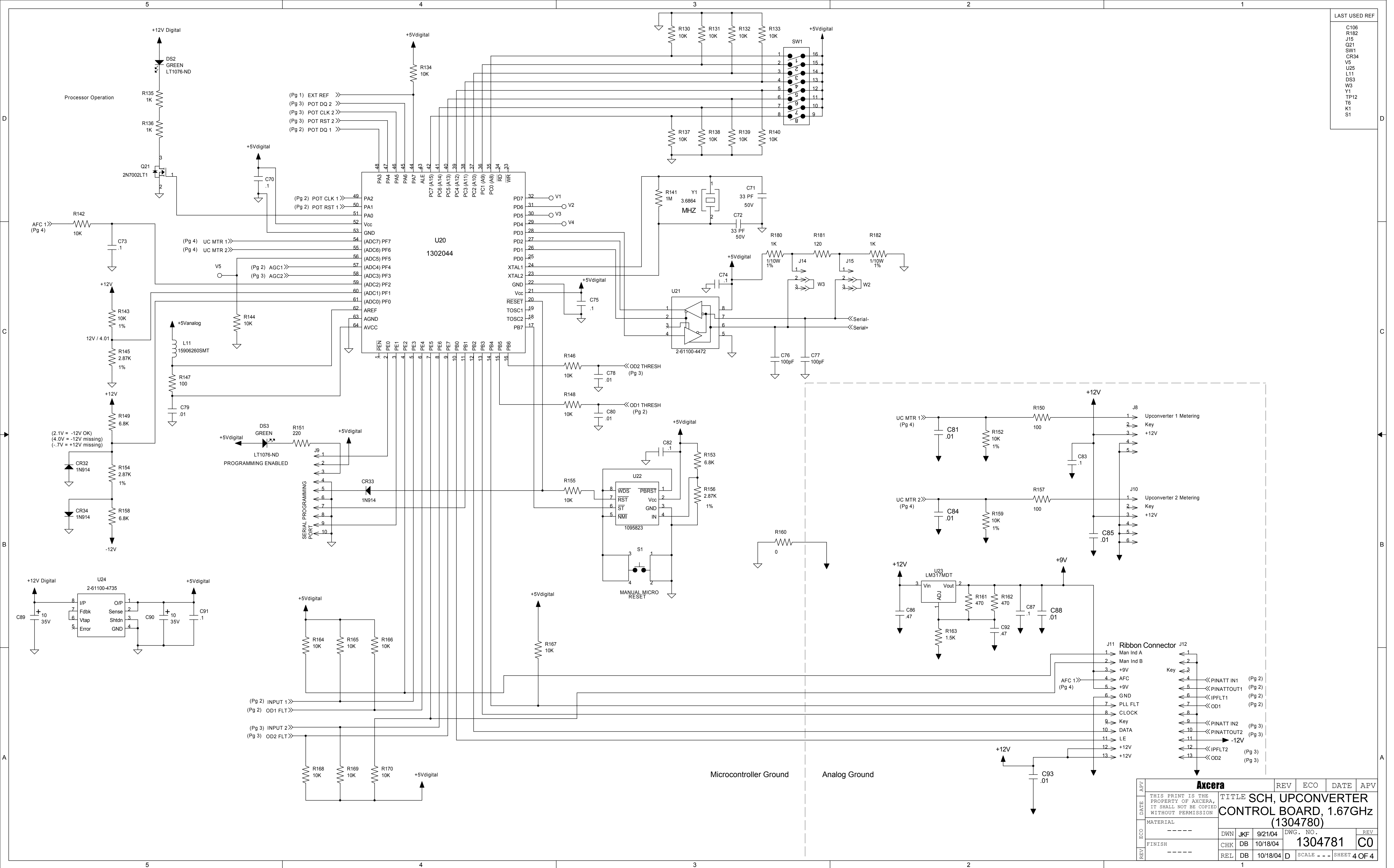
B0	20111670	2/9/05	DB
REV	ECO	DATE	APV

APV	Axcera				REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION	TITLE SCH, UPCONVERTER CONTROL BOARD, 1.67GHz (1304780)						
MATERIAL								
ECO		DWN JKF 9/21/04 DWG. NO.						REV
FINISH		CHK DB 10/18/04 1304781						C0
REV		REL DB 10/18/04 D SCALE - - - SHEET 1 OF 4						

NOTE:
1) ALL RESISTORS ARE 1/8W UNLESS OTHERWISE NOTED.
2) ALL CAPACITORS ARE IN uF UNLESS OTHERWISE NOTED.
3) ALL INDUCTORS ARE IN uH UNLESS OTHERWISE NOTED.

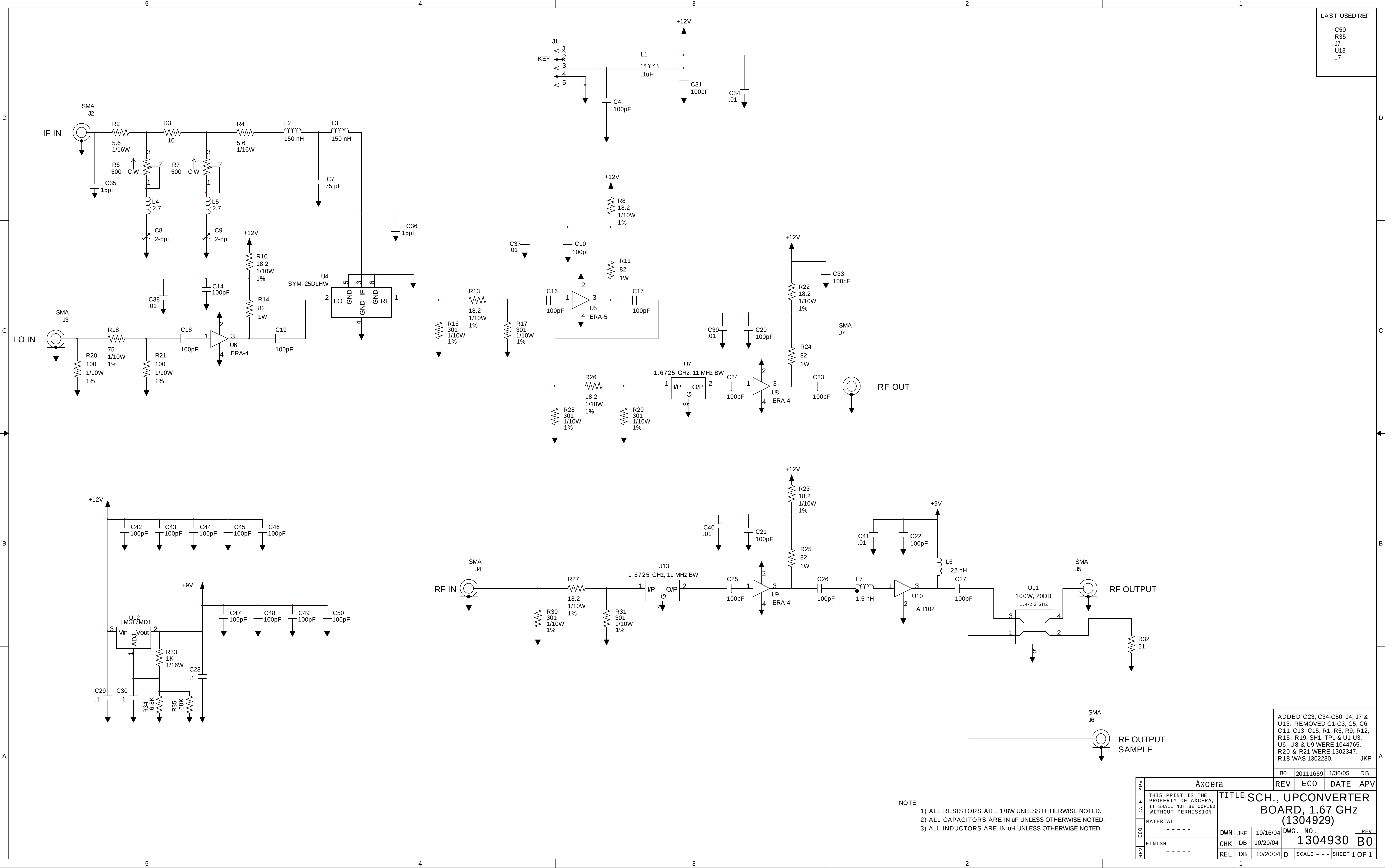






LAST USED REF	
C106	R182
J15	Q21
SW1	CR34
V5	U25
L11	DS3
W3	Y1
TP12	T6
K1	S1

Axcera		REV	ECO	DATE	APV
THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION		TITLE SCH, UPCONVERTER CONTROL BOARD, 1.67GHz (1304780)			
MATERIAL		DWN	JKF	9/21/04	DWG. NO.
FINISH		CHK	DB	10/18/04	REV
REL		DB	10/18/04	D	SCALE
		SHEET 4 OF 4			



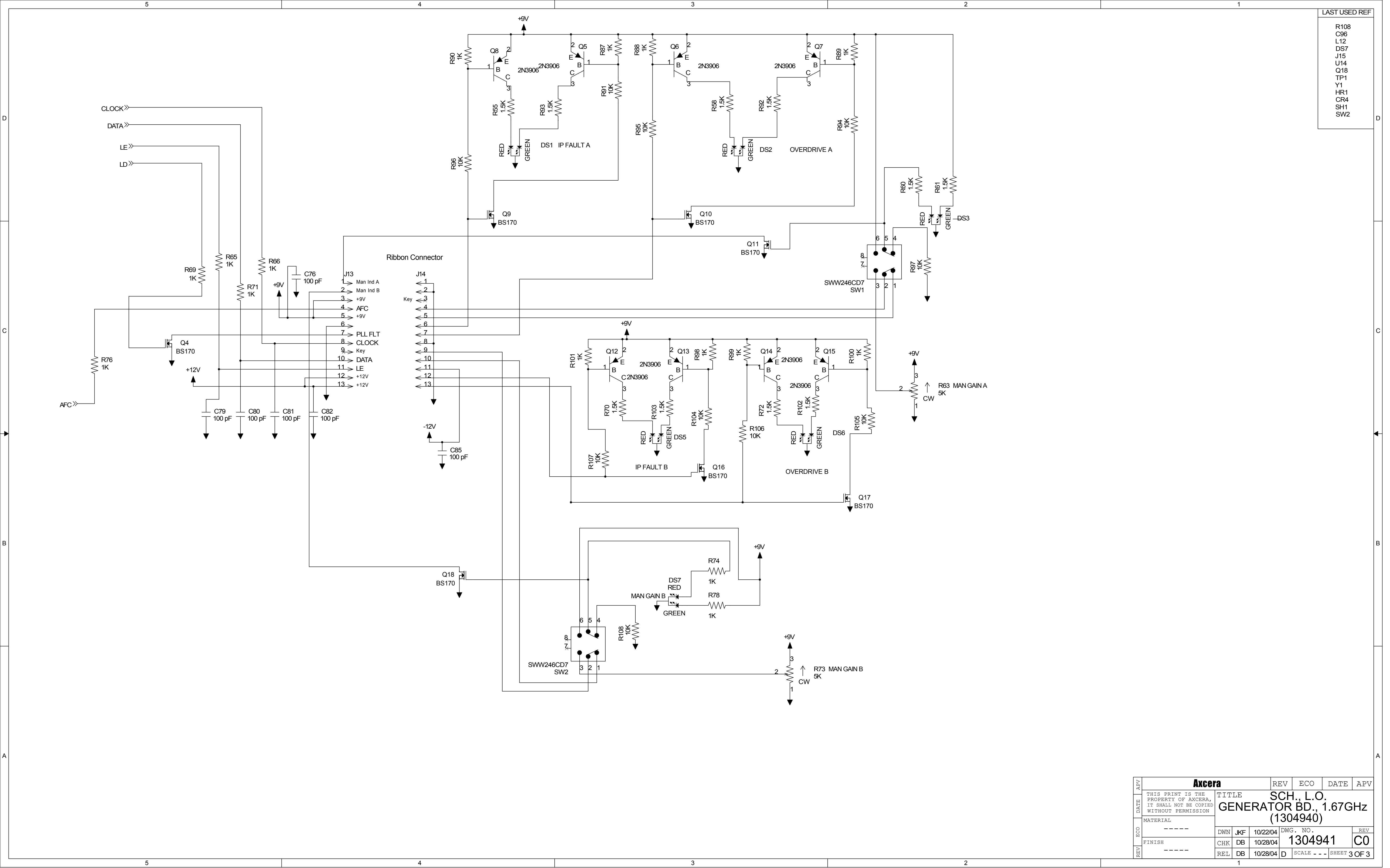
LAST USED REF

C50
R35
J7
U13
L7

ADDED C23, C34-C50, J4, J7 & U13. REMOVED C1-C3, C5, C6, C11-C13, C15, R1, R5, R9, R12, R15, R19, SH1, TP1 & U1-U3. U6, U8 & U9 WERE 1044765. R20 & R21 WERE 1302347. R18 WAS 1302230. JKF

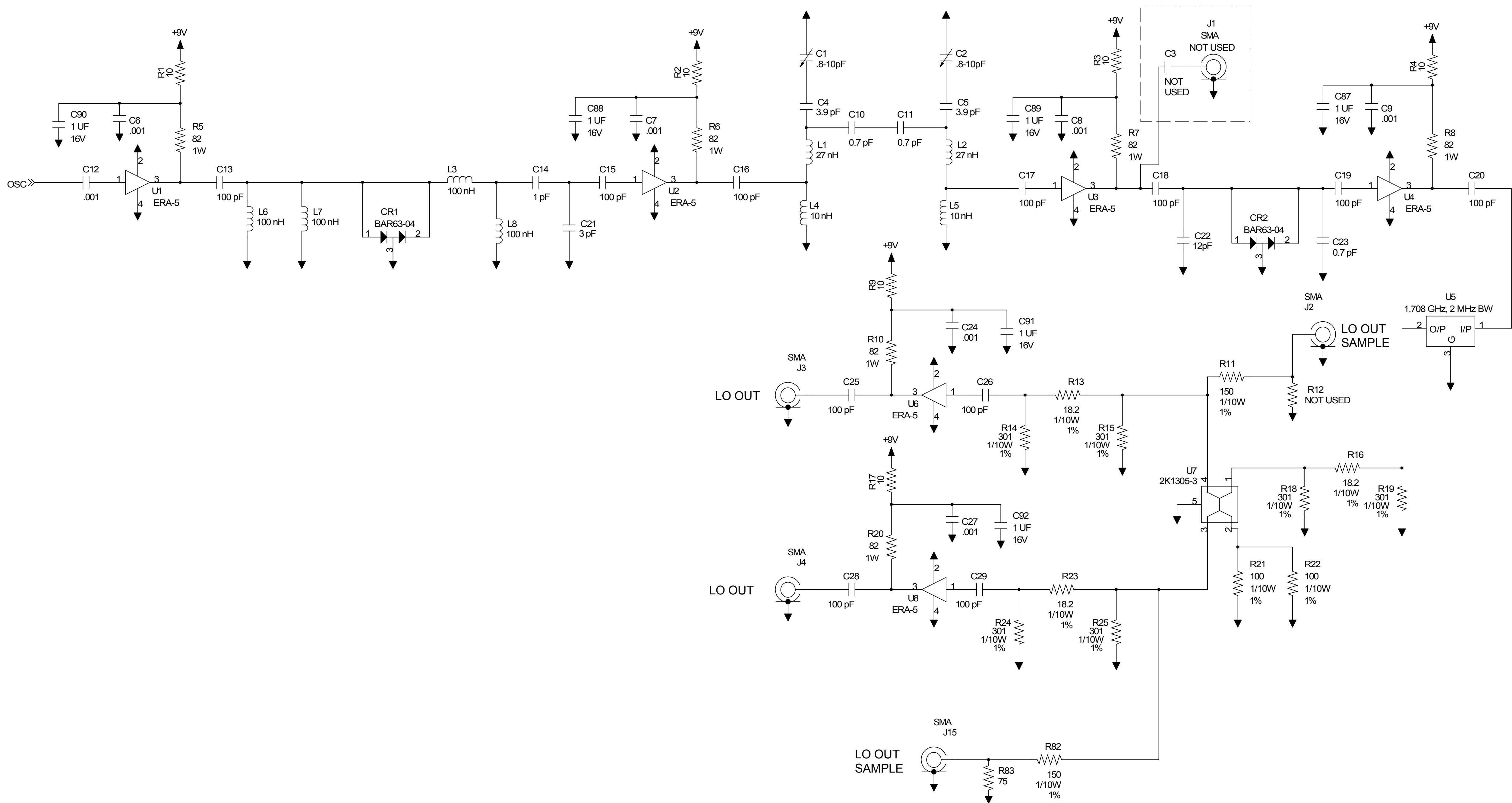
APV		Axcera		REV	ECO	DATE	DB
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION	TITLE SCH., UPCONVERTER BOARD, 1.67 GHz (1304929)				DWN	JKF
ECO	MATERIAL	-----	CHK	DB	10/16/04	DWG. NO.	REV
REV	FINISH	-----	REL	DB	10/20/04	1304930	B0
		SCALE - - -		SHEET 1 OF 1			

NOTE:
1) ALL RESISTORS ARE 1/8W UNLESS OTHERWISE NOTED.
2) ALL CAPACITORS ARE IN uF UNLESS OTHERWISE NOTED.
3) ALL INDUCTORS ARE IN uH UNLESS OTHERWISE NOTED.



LAST USED REF	
R108	
C96	
L12	
DS7	
J15	
U14	
Q18	
TP1	
Y1	
HR1	
CR4	
SH1	
SW2	

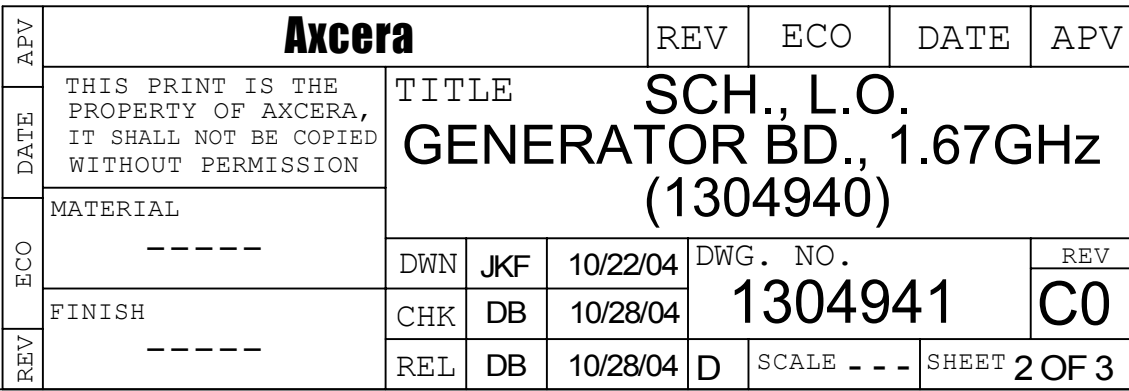
REV	ECO	FINISH	Axcera				DWG. NO.	REV
			REL	DB	SCALE	SHEET		
1						3 OF 3		

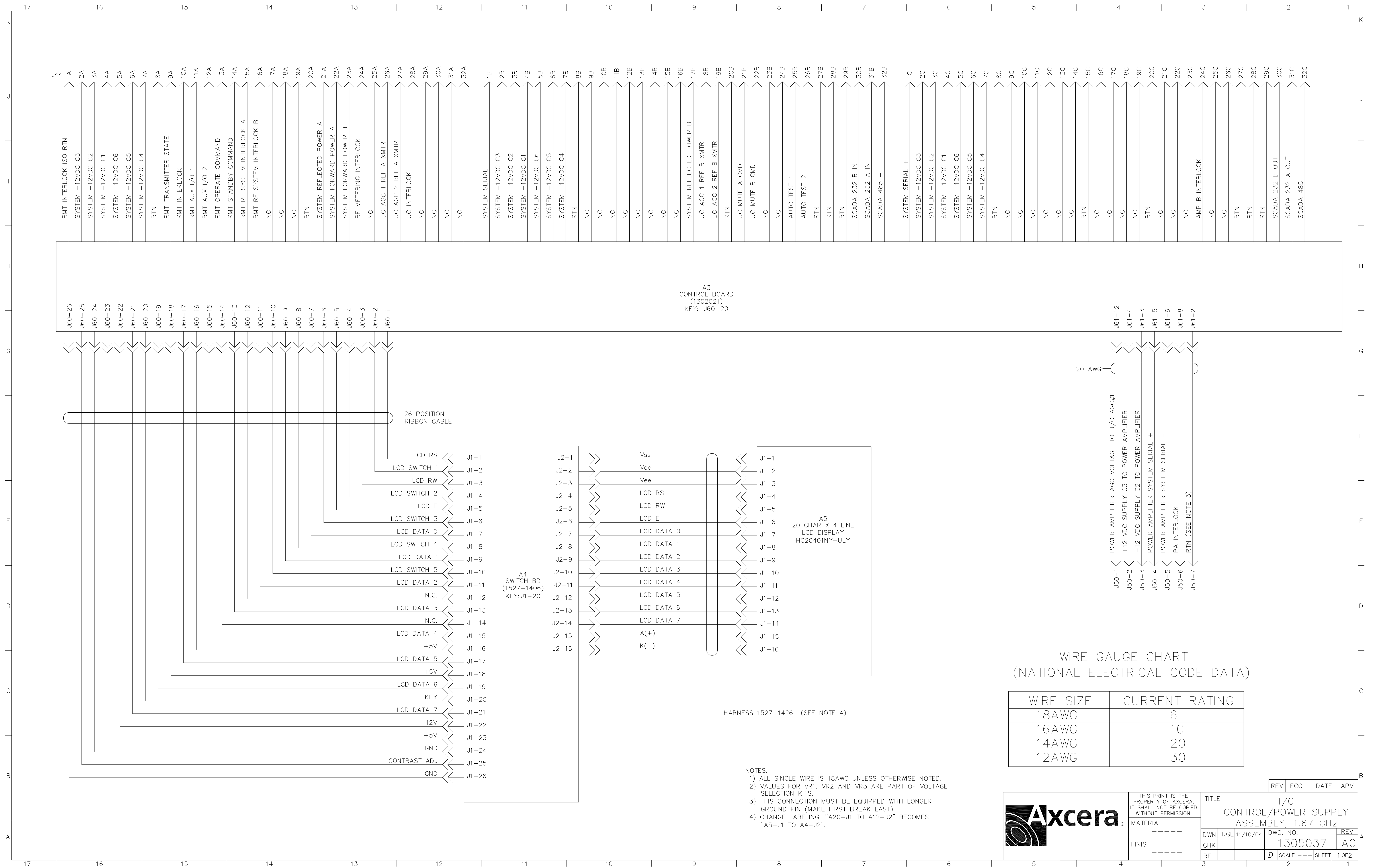


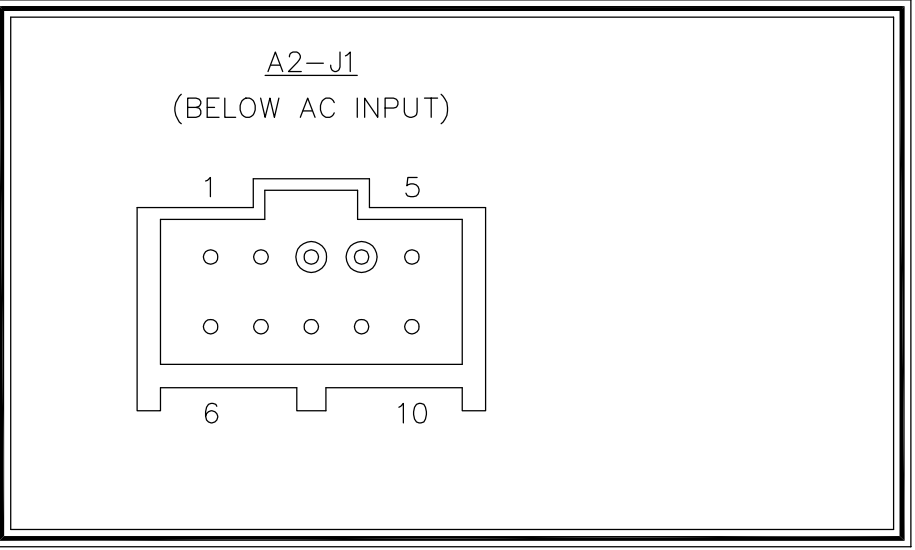
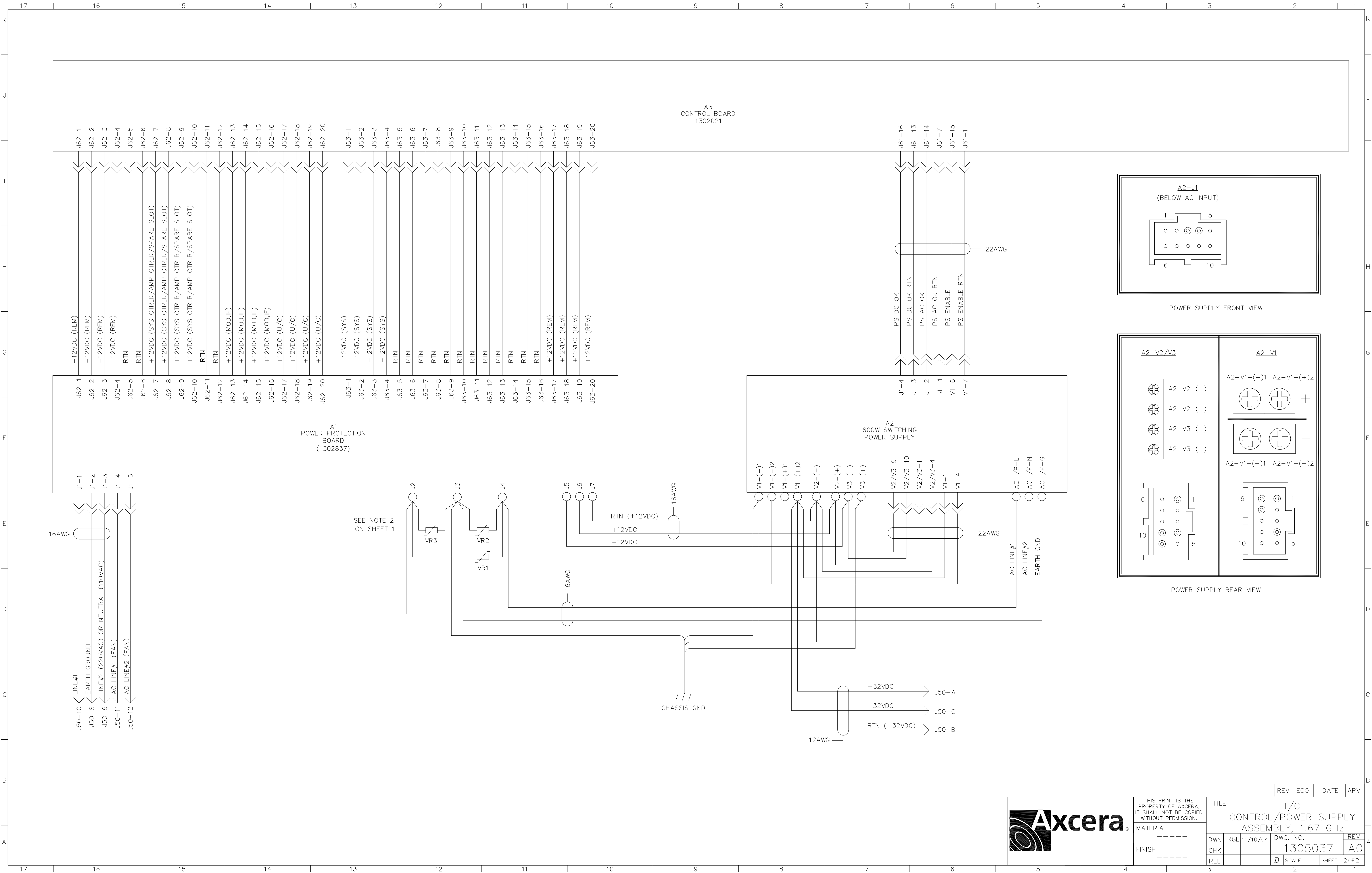
NOTE:
1) ALL RESISTORS ARE 1/8W UNLESS OTHERWISE NOTED.
2) ALL CAPACITORS ARE IN uF UNLESS OTHERWISE NOTED.
3) ALL INDUCTORS ARE IN uH UNLESS OTHERWISE NOTED.

ADDED PAGE 3, C96, Q5 - Q18 & R87 - R108. JKF			
B0	20111664	3/18/05	DB
ADD C87 - C95, J15, R82 - R86. DEL C74 & R12. C53 WAS 5.1 PF. R62 WAS 3.3K. R11 WAS 301. JKF			

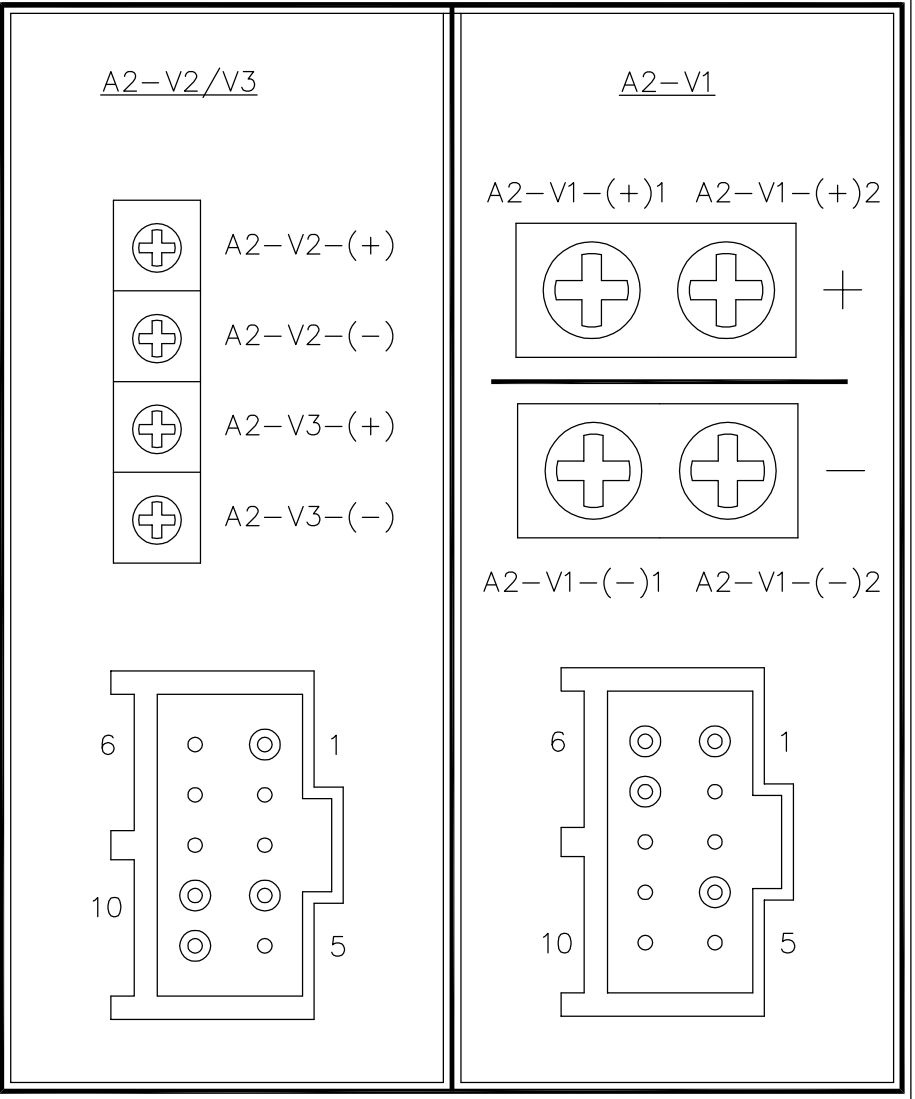
REV	ECO	APV		Axcera		REV		ECO	DATE	APV
	DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION		TITLE		DWN		JKF	10/22/04	DWG. NO.
	MATERIAL	-----		SCH., L.O. GENERATOR BD., 1.67GHz (1304940)		CHK		DB	10/28/04	1304941
	FINISH	-----		REL		DB		DB	10/28/04	C0





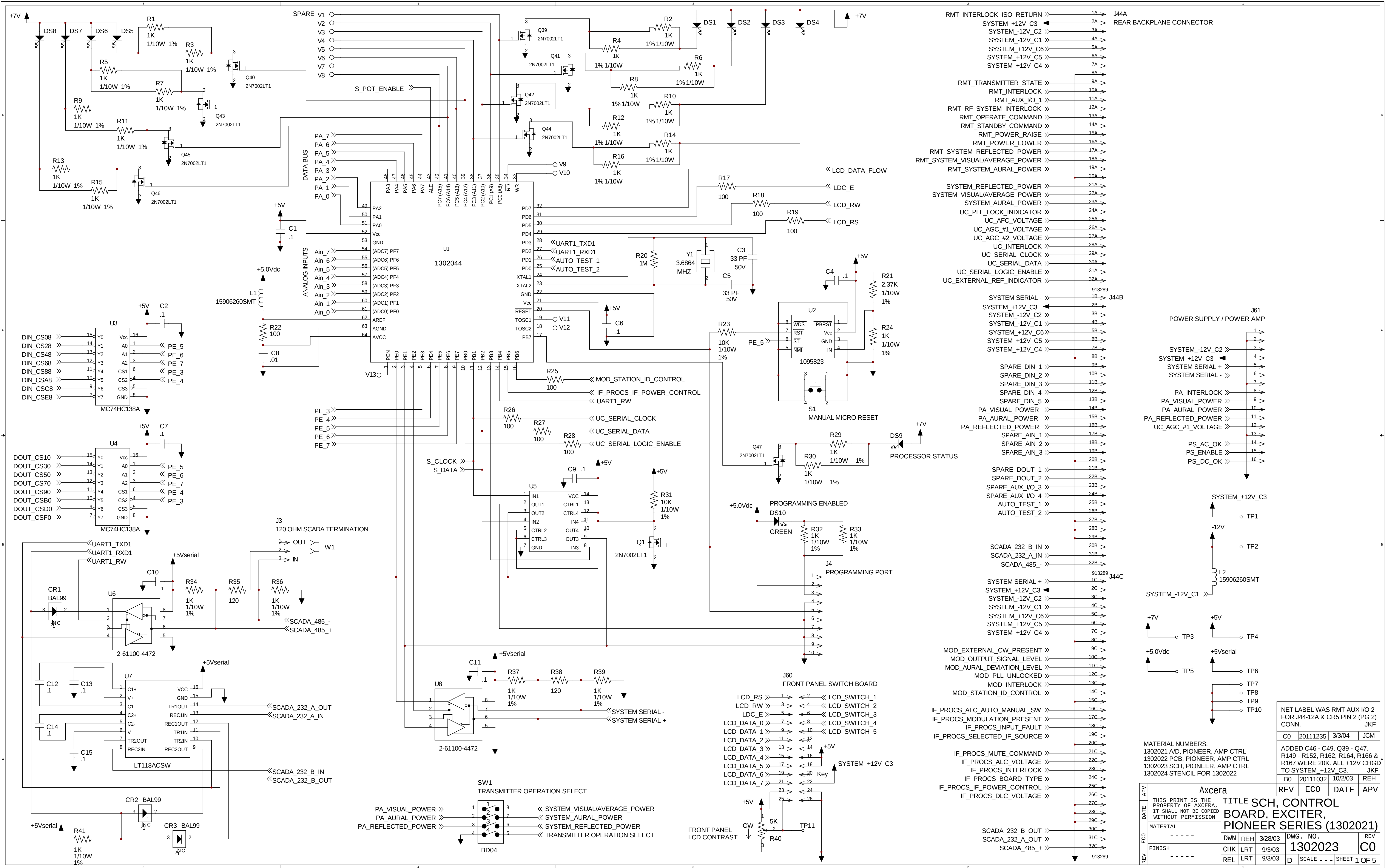


POWER SUPPLY FRONT VIEW



POWER SUPPLY REAR VIEW

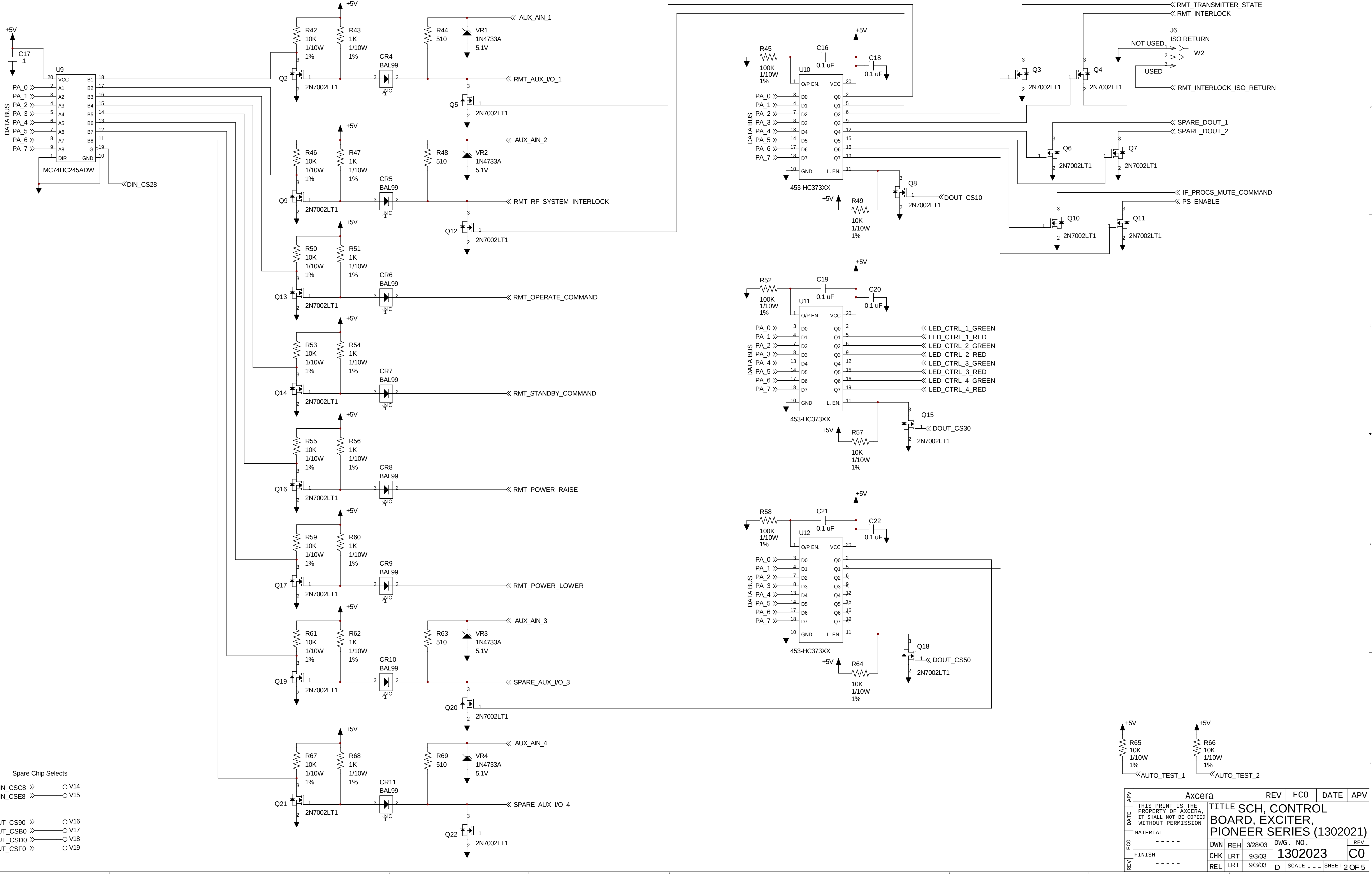
	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION.		TITLE		I/C				
			CONTROL/POWER SUPPLY ASSEMBLY, 1.67 GHz						
	MATERIAL		DWN	RGE	11/10/04	DWG. NO.	REV		
	FINISH		CHK			1305037	A0		
			REL			D	SCALE --- SHEET 2 OF 2		
5		4		3		2		1	



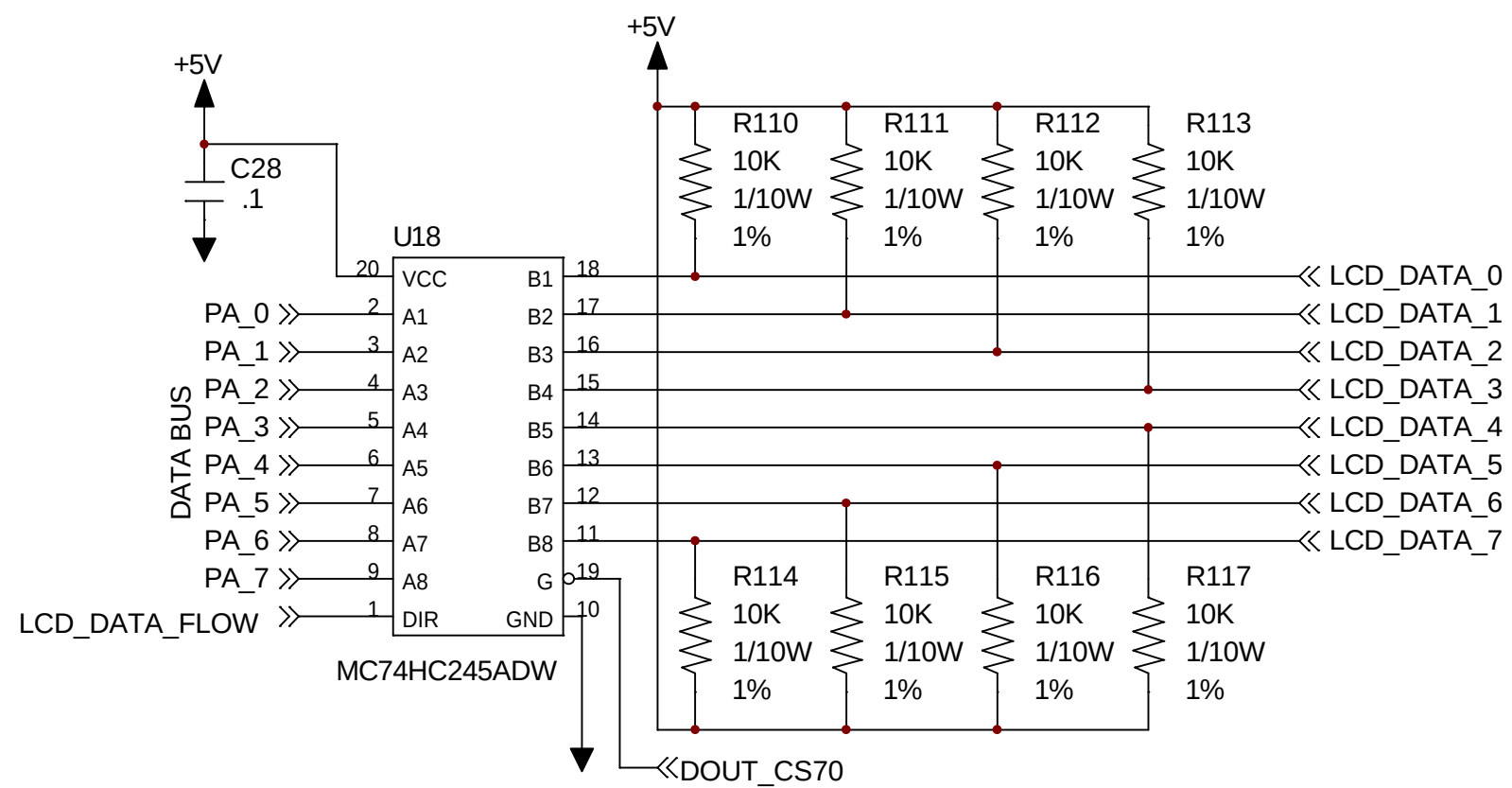
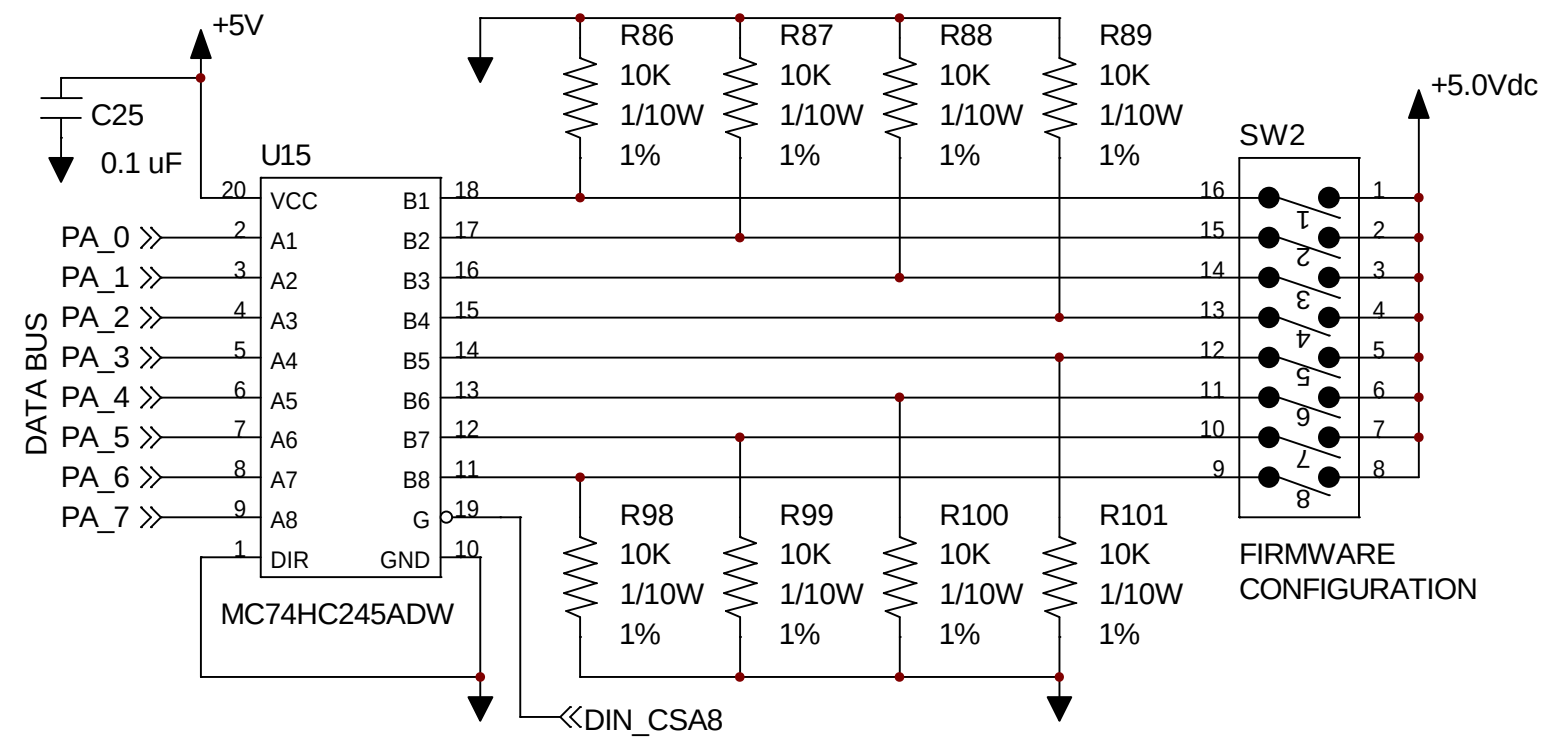
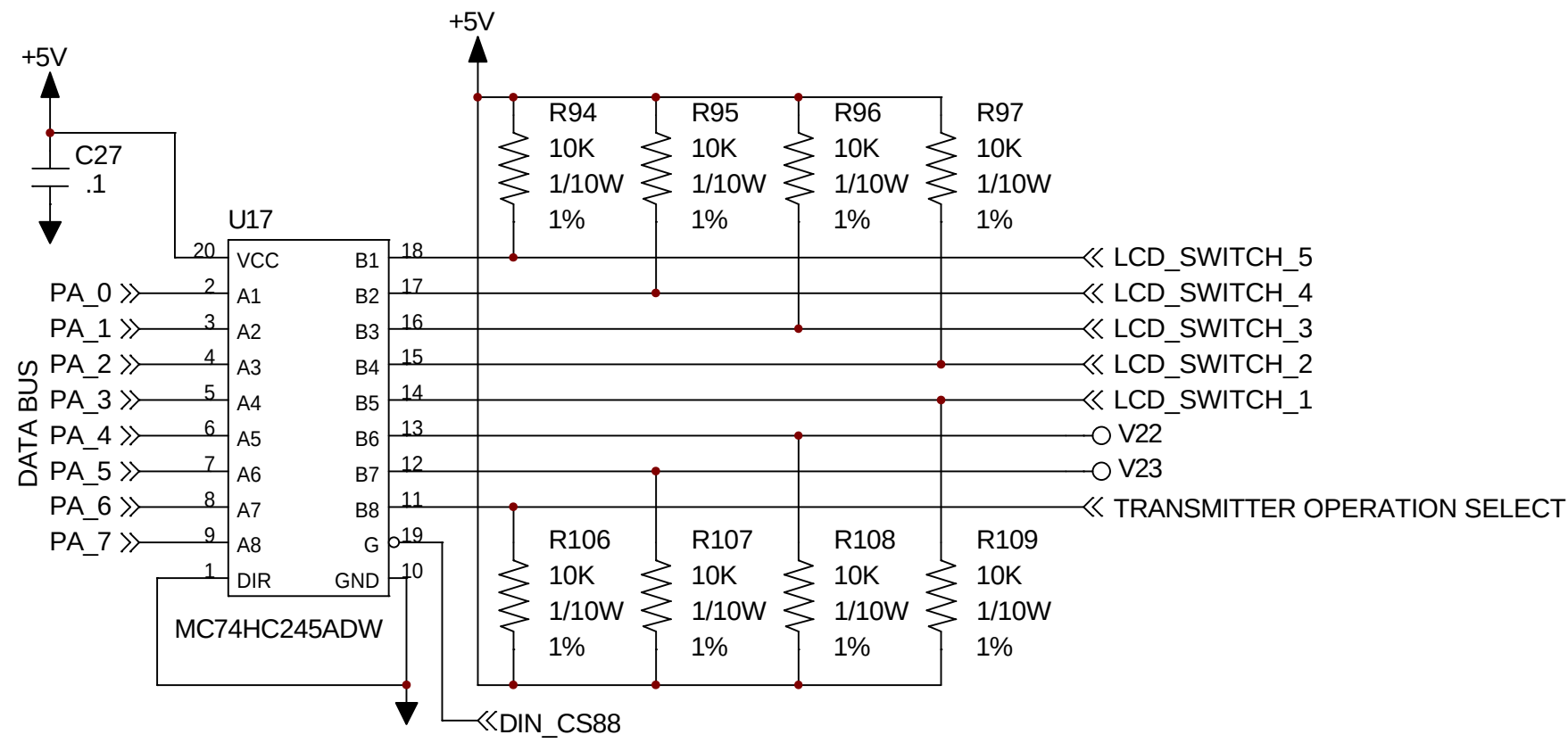
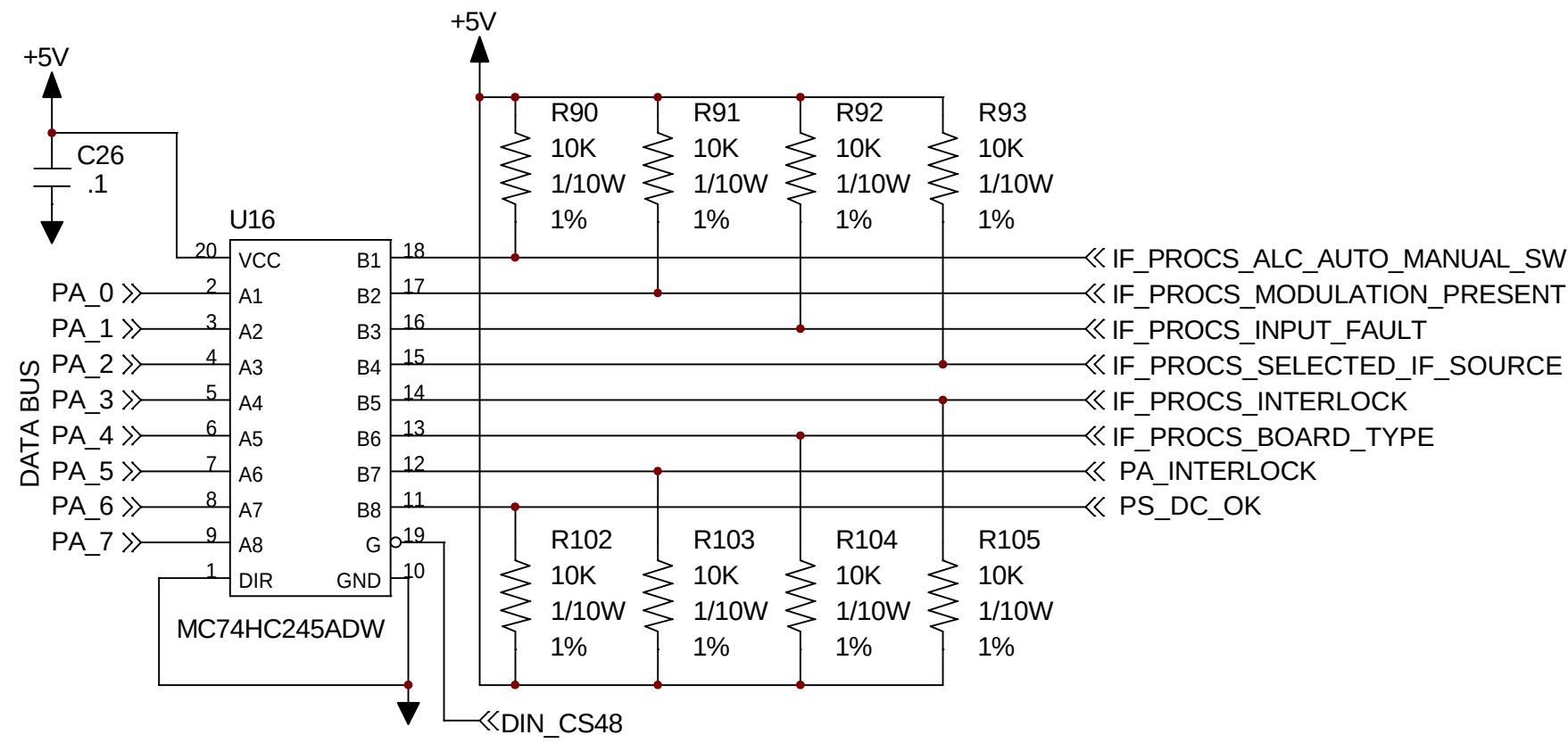
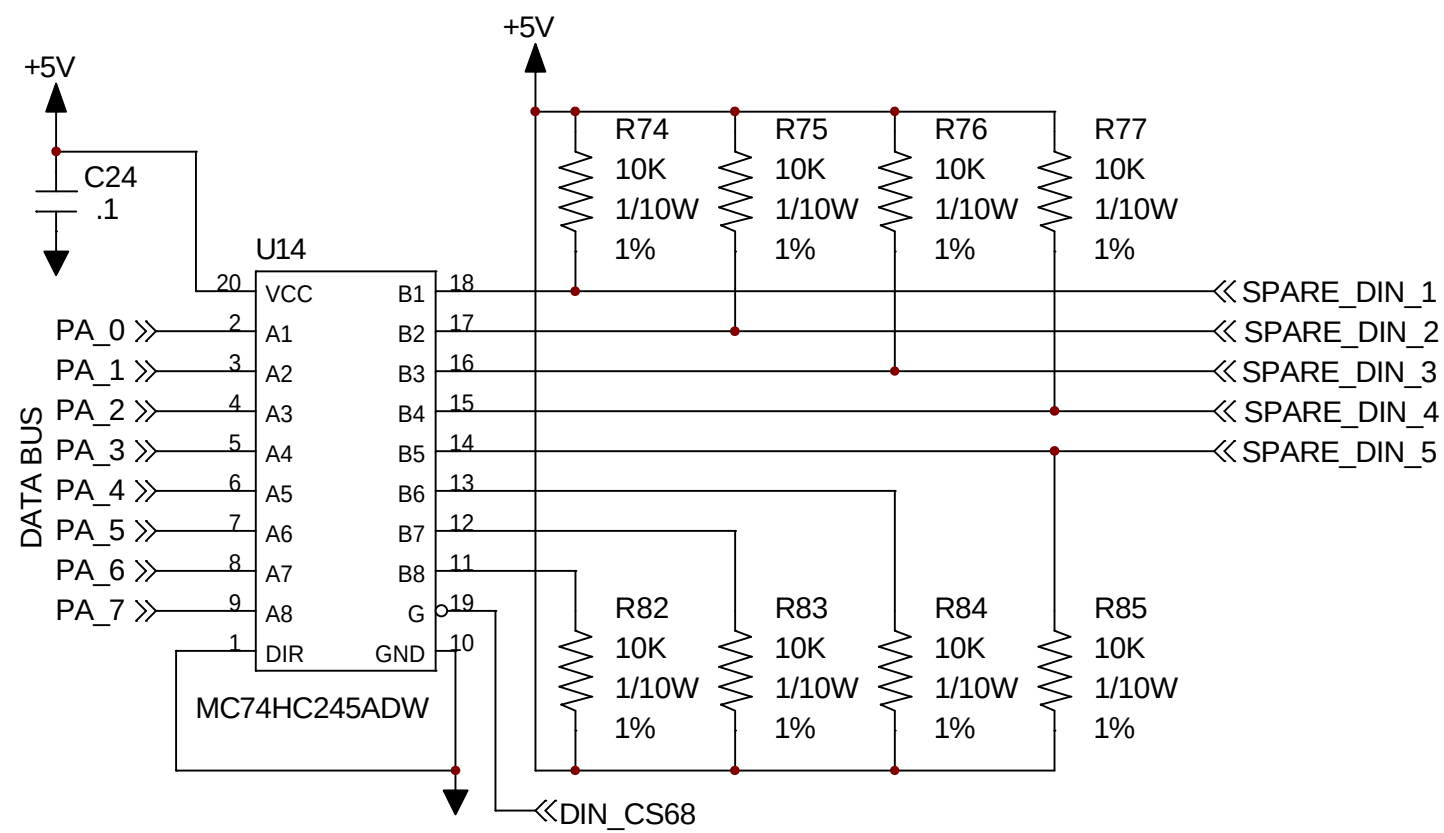
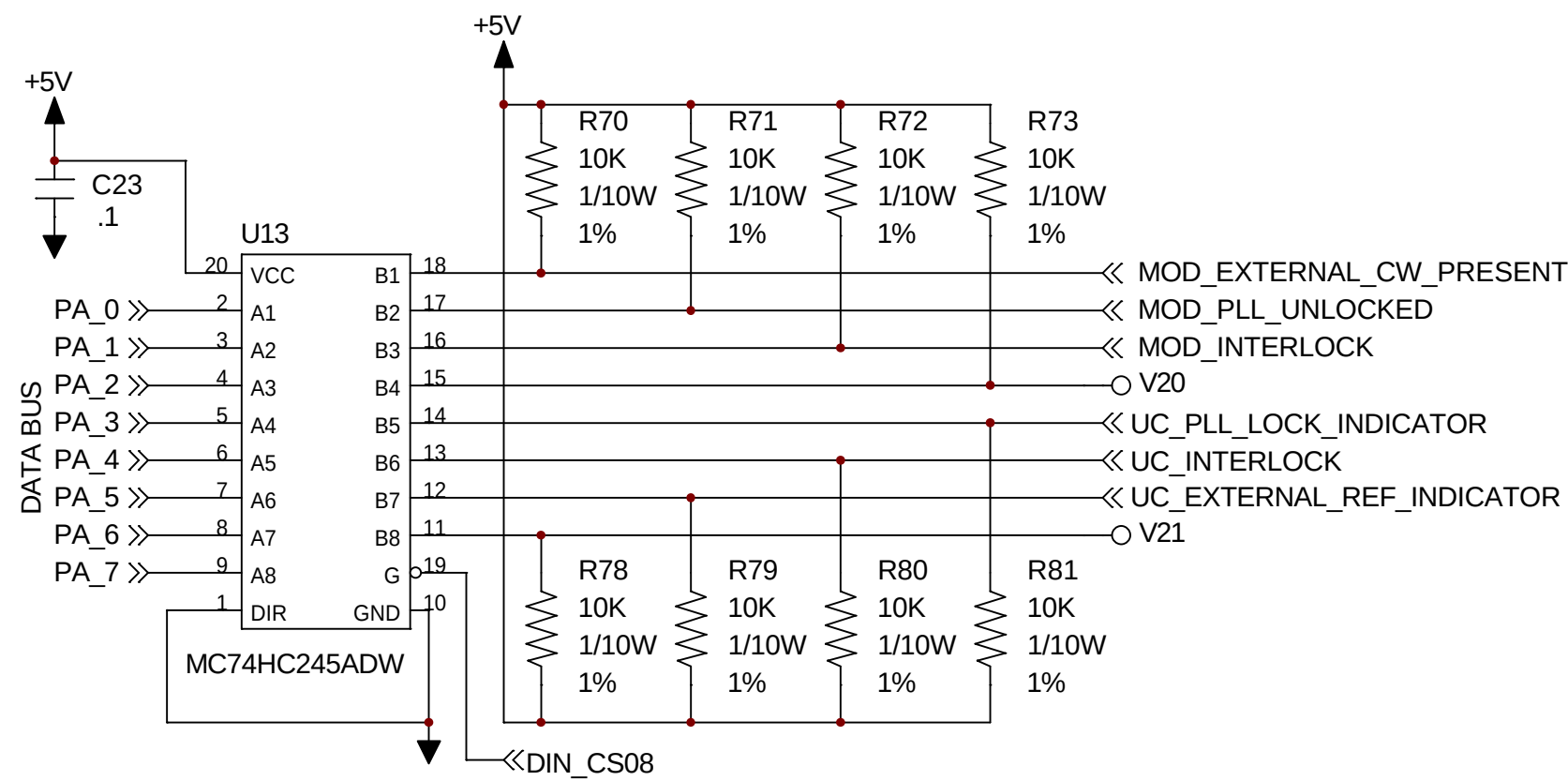
NET LABEL WAS RMT AUX I/O 2
FOR J44-12A & CR5 PIN 2 (PG 2)
CONN. JKF

MATERIAL NUMBERS:
1302021 A/D, PIONEER, AMP CTRL
1302022 PCB, PIONEER, AMP CTRL
1302023 SCH, PIONEER, AMP CTRL
1302024 STENCIL FOR 1302022

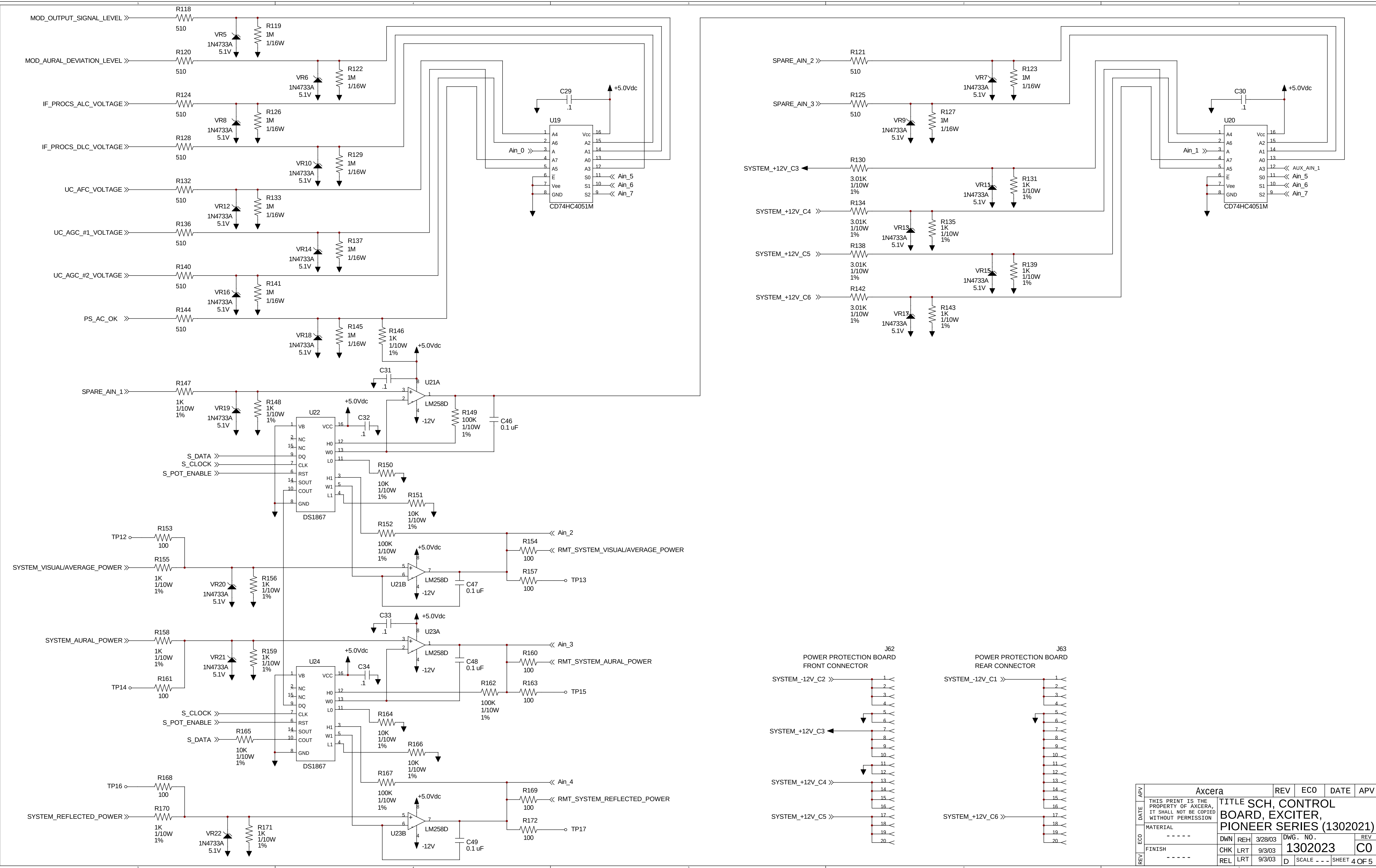
Axcera				REV	ECO	DATE	APV	
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION			TITLE SCH, CONTROL BOARD, EXCITER, PIONEER SERIES (1302021)				
ECO	MATERIAL							
REV	FINISH	-----	DWN	REL	3/28/03	DWG. NO.		REV
	-----	CHK	LRT	9/3/03	1302023		C0	
		REL	LRT	9/3/03	D	SCALE	- - -	SHEET 1 OF 5



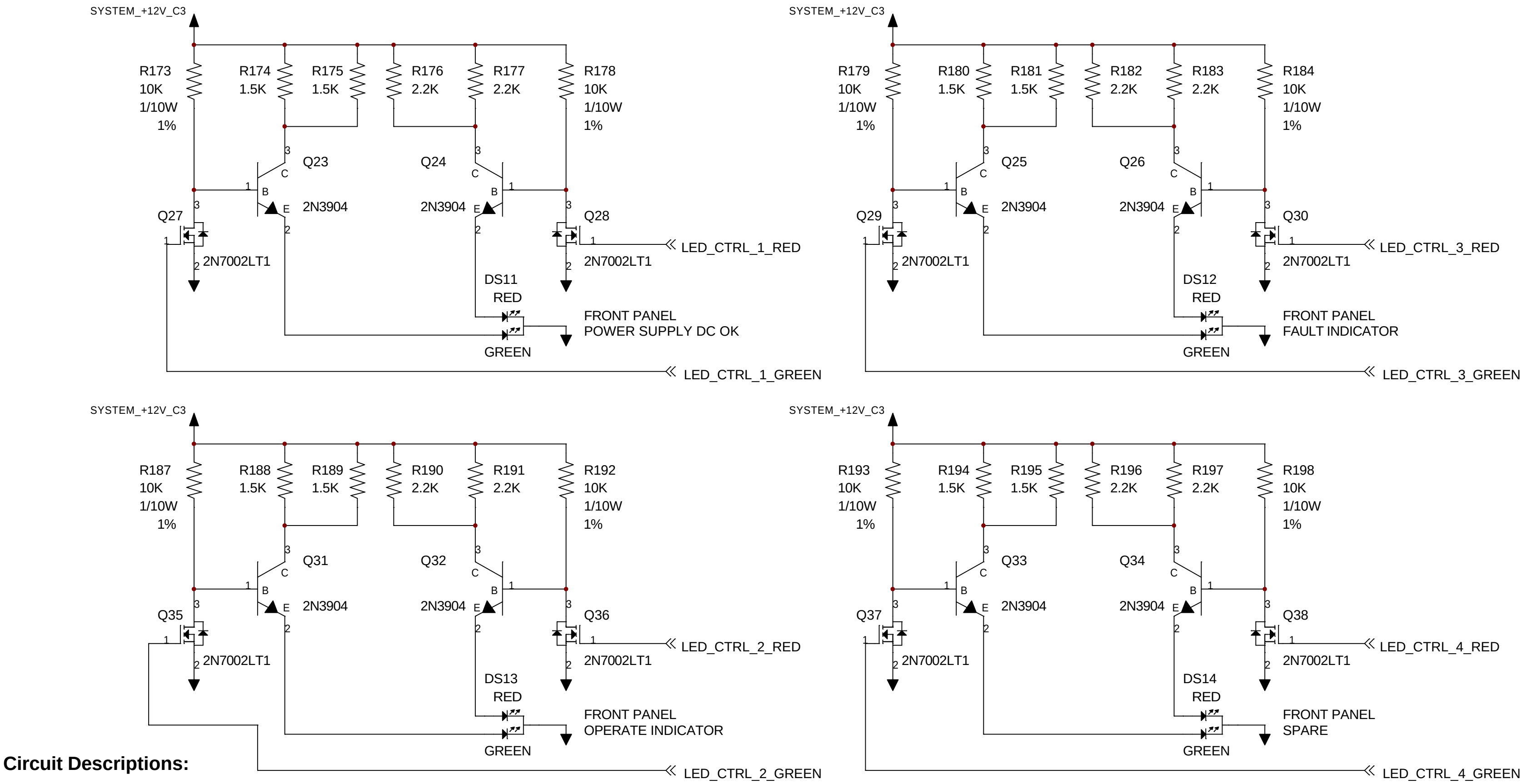
APV	Axcera			REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION			TITLE SCH, CONTROL BOARD, EXCITER, PIONEER SERIES (1302021)			
ECO	MATERIAL			DWN	REH	3/28/03	DWG. NO.
REV	FINISH			CHK	LRT	9/3/03	1302023
				REL	LRT	9/3/03	C0
						SCALE	SHEET 2 OF 5



APV	Axcera			REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION			TITLE SCH, CONTROL BOARD, EXCITER, PIONEER SERIES (1302021)			
ECO				DWN	REH	3/28/03	DWG. NO.
REV	FINISH			CHK	LRT	9/3/03	1302023
				REL	LRT	9/3/03	C0
				SCALE	---		SHEET 3 OF 5



APV	Axcera				REV	ECO	DATE	APV		
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION		TITLE SCH, CONTROL BOARD, EXCITER, PIONEER SERIES (1302021)							
ECO	MATERIAL		DWN		REH		3/28/03		DWG. NO.	REV
REV	FINISH		CHK		LRT		9/3/03		1302023	C0
			REL		LRT		9/3/03		D	SCALE - - - SHEET 4 OF 5



Circuit Descriptions:

PAGE 1:

U1 is an Atmel Atmega128 microcontroller. This part is in-circuit programmed using the serial programming port. When the microcontroller is held in reset by either the programming port or the external watchdog IC, a transistor inverts the reset signal while serial programming lines are connected to U1 through analog switch U5.

U2 is a watchdog IC used to hold the microcontroller in reset if the supply voltage is less than 4.21 Vdc; (1.25 Vdc < Pin 4 (IN) < Pin 2 (Vcc)). The watchdog momentarily resets the microcontroller if Pin 6 (IST) is not clocked every second. A manual reset switch is provided but should not be needed.

Diodes DS1 through DS8 are used for display of auto test results. A test board is used to execute self test routines. When the test board is installed, Auto_Test_1 is held low and Auto_Test_2 is allowed to float at 5 Vdc. This is the signal to start the auto test routines.

U3 and U4 are used to selectively enable various input and output ICs found on page 2 and 3 of the schematic.

U1 has two serial ports available. In this application, one port is used to communicate with transmitter system components where U1 is the master of a RS-485 serial bus. The other serial port is used to provide serial data I/O where U1 is not the master of the data port. A dual RS-232 port driver IC and a RS-485 port driver is also in the second serial data I/O system. The serial ports are wired such that serial data input can come through one of the three serial port channels. Data output is sent out through each of the three serial port channels.

Switch SW1 is used to select either transmitter operation or exciter driver operation. When the switches of SW1 are on, transmitter operation is selected and the power monitoring lines of the transmitter's power amplifier are routed to the system power monitoring lines.

PAGE 2:

Digital output latches are used to control system devices. Remote output circuits are implemented using open drain FETs with greater than 60 Volt drain to source voltage ratings.

Remote digital inputs are diode protected with a 1 Kohm pull-up resistor to 5Vdc. If the remote input voltage is greater than about 2 volts or floating, a FET is turned on and a logic low is applied to the digital input buffer. If the remote input voltage is less than the turn on threshold of the FET (about 2 Vdc), a logic high is applied to the digital input buffer.

Four of the circuits on page two are auxiliary I/O connections wired for future use. They are wired similar to the remote digital inputs but include a FET for digital output operation. To operate these signals as an input, the associated output FET must be turned off. These circuits are also connected to an analog input multiplexer IC.

PAGE 3:

Several ICs are used as input buffers to allow the microcontroller to monitor various digital input values. Most digital inputs use a 10 Kohm pull-up resistor. The buffer IC used for data transfer to the display is wired for read and write control.

PAGE 4:

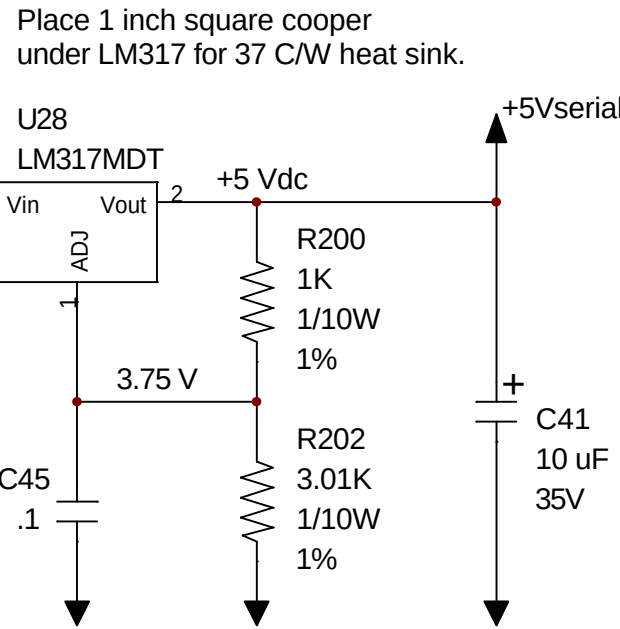
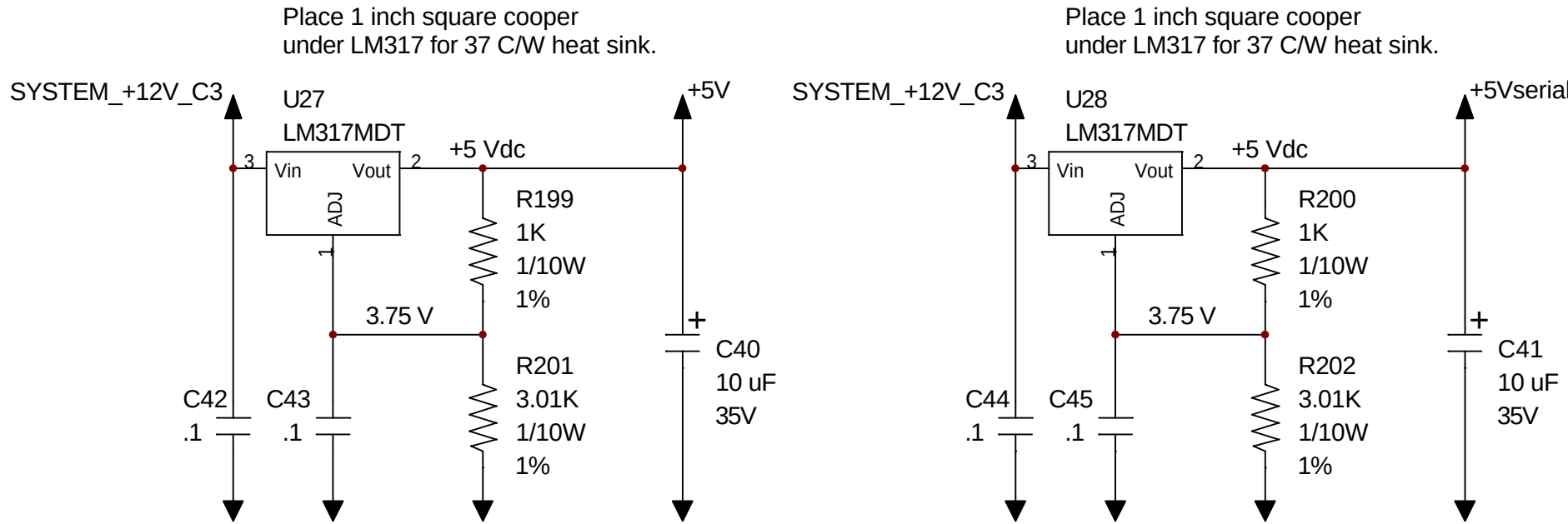
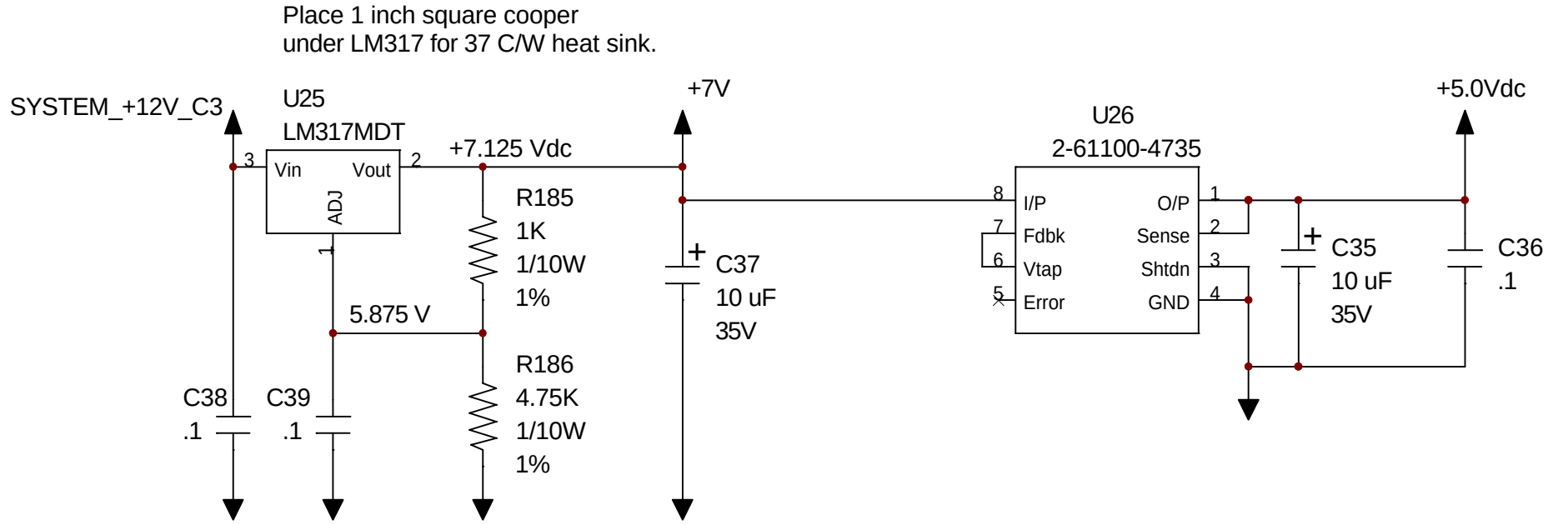
Each analog input is expected to be between 0 and 5 Vdc. If a signal exceeds 5.1 Vdc, a 0.225 Watt zenor diode clamps the signals voltage. Most signals are calibrated at their source however two dual serial potentiometer ICs are used to calibrate four signals. For these four circuits, the input value is divided in half before it is applied to an op-amp. The serial potentiometer is used to adjust the input signal between 80 and 120% of the input signal. Serial data to the second serial potentiometer is transferred through the first IC. The wiper position of the digital potentiometer circuit is used to set the gain of the op-amp. Lower digital values for the wiper setting increases the gain of the op-amp. If N= 0, gain = 6.0. If N = 255, gain = 3.00. If Vin = 1.0 at spare_Ain_1 and N = 128, U21A out = 4.0V with gain = 4.

Two 20 position connectors are used to provide power to the backplane. J62 and J63 get power from the power supply after a nominal 3 amp resettable fuse. The Raychem polyswitch resettable fuses may open on a current as low as 2.43 amps at 50C, 3 amps at 25 C or 3.3 amps at 0C. They definately will open when the current is 4.86 amps at 50C, 6 amps at 20C, or 6.6 amps at 0C. Trace widths of these circuits are 0.140" (designed for maximum current).

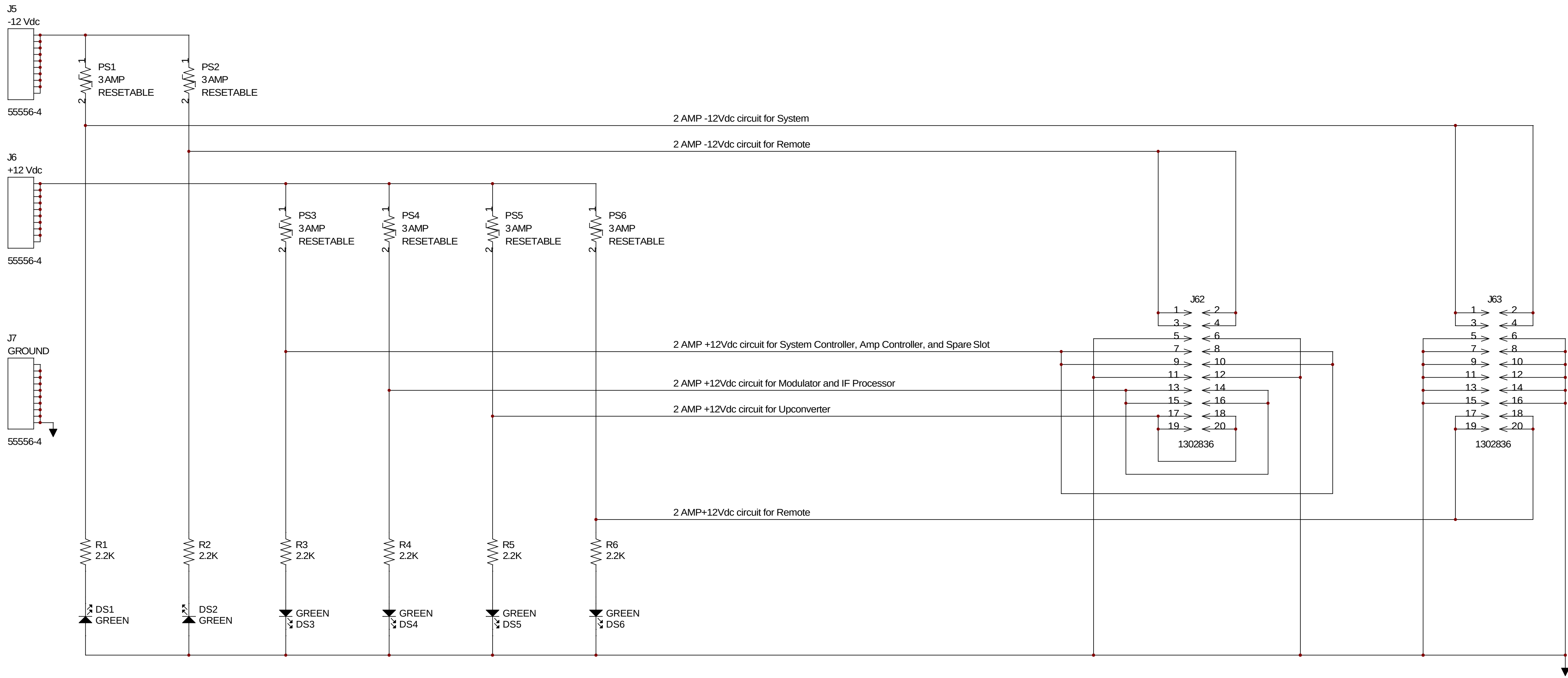
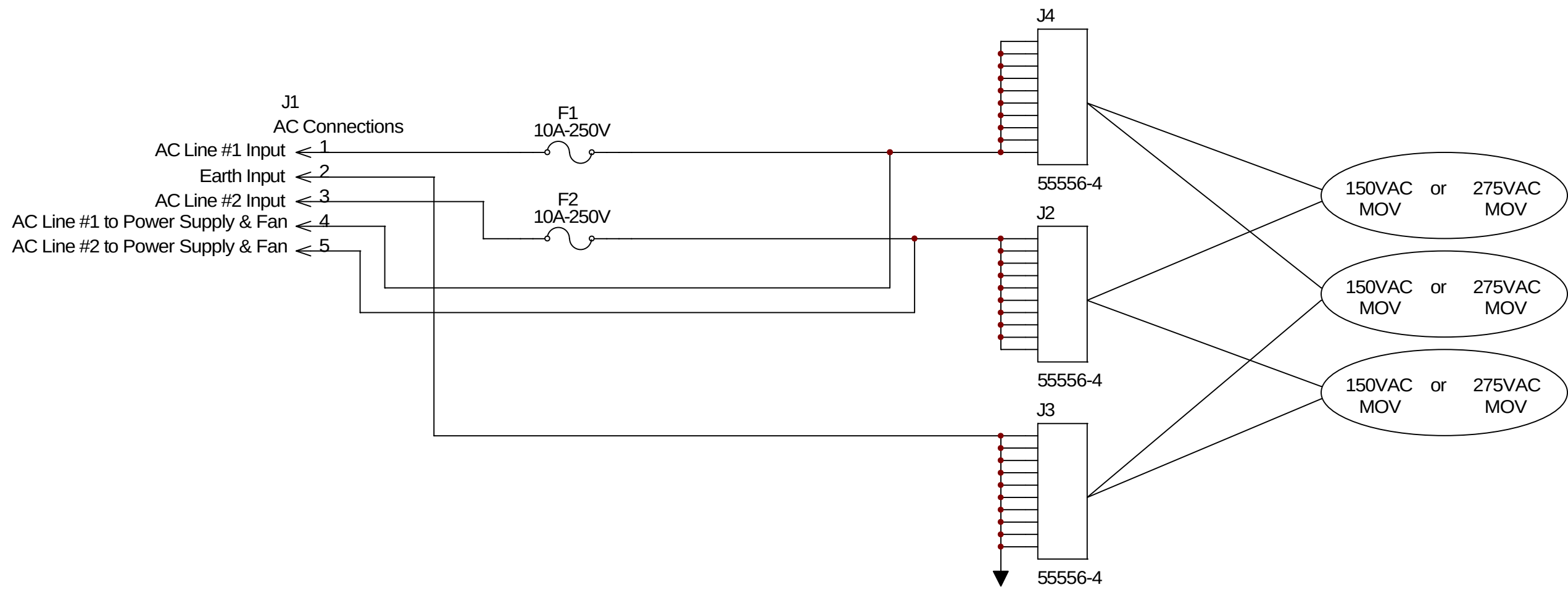
PAGE 5:

Each of the four LED circuits drive a dual element common cathode LED. To make a good amber color, the current applied to the green element is slightly greater than the red element.

Several voltage regulators are used to power the board. Seven volts is typically applied to board LEDs and to the input of a precision 5.0 volt regulator. The 5.0 volt regulator is used for analog circuits and the microcontroller analog reference voltage. Another two 5 volt regulator circuits are used for most other board circuits.



Axcera					REV	ECO	DATE	APV
APV	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION		TITLE SCH, CONTROL BOARD, EXCITER, PIONEER SERIES (1302021)					
DATE								
ECO	MATERIAL		DWN	REH	3/28/03		DWG. NO.	REV
REV	FINISH		CHK	LRT	9/3/03		1302023	C0
	-----		REL	LRT	9/3/03		D	SCALE --- SHEET 5 OF 5



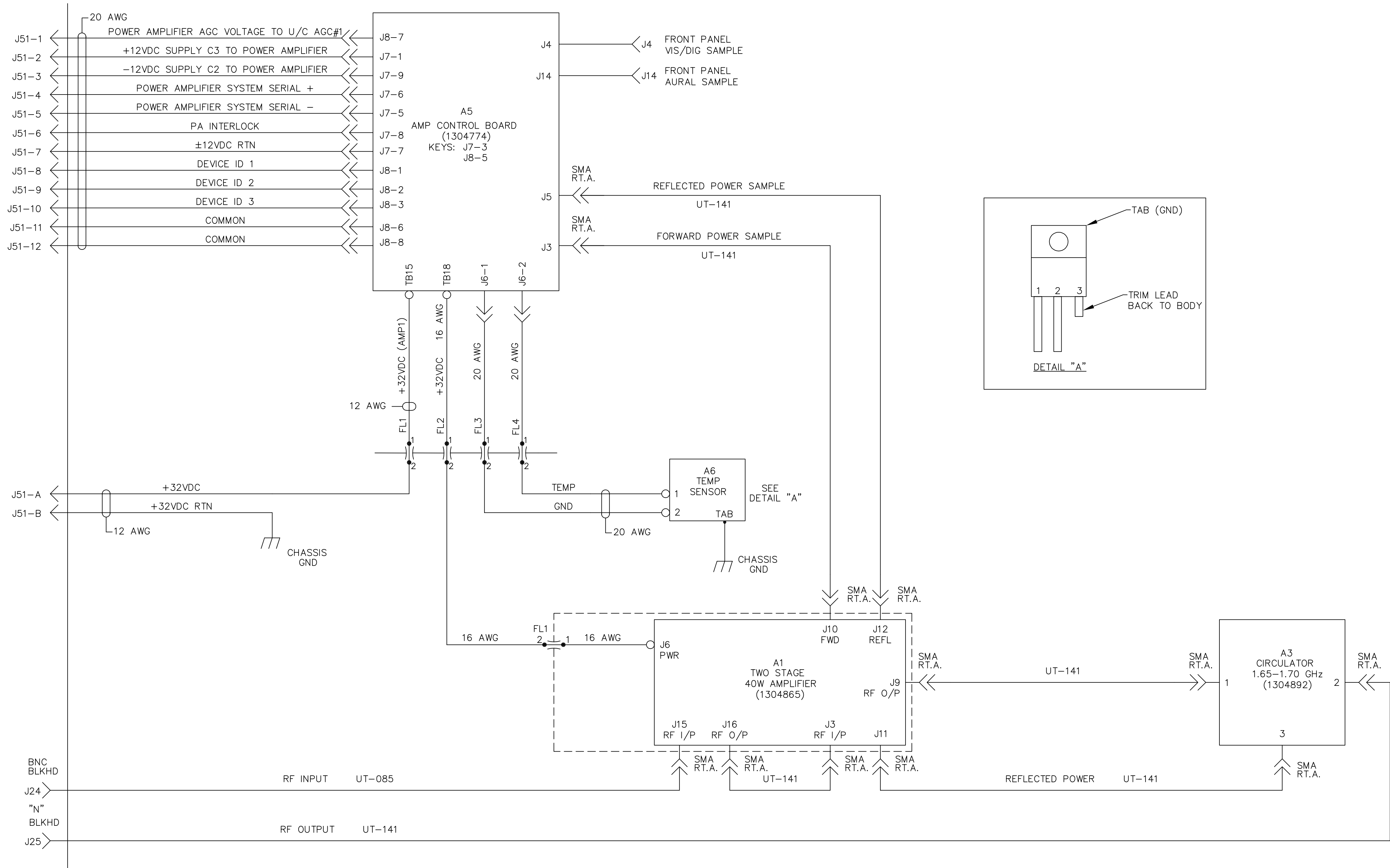
Circuit Design Notes:

- Track widths of 0.140 inch minimum top and bottom of board shall be used to connections to J1, J2, J3, J4.
- Track widths of 0.140 inch minimum top of board shall be used to connections to J5 and J6.
- Track widths of 0.065 inch minimum shall be used to connections to J62 and J63.
- Track widths of 0.020 inch minimum shall be used to connections between LEDs and resistors.
- Board BOM needs the following items added to those exported from Orcad:

Part Number	Qty	Description
102071	4	Fuse Clip, PC BD Mnt., 15 Amp

MATERIAL NUMBERS:
1302837 PIONEER, POWER PROTECTION
1302838 PCB, PIONEER, PWR PROTECT
1302839 SCH, PIONEER, PWR PROTECT
1302840 STENCIL FOR 1302838

REV	DATE	APV	ECO	DATE	APV
THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION			TITLE SCH, PIONEER, PWR PROTECT (1302837)		
MATERIAL			DWN REH 5/29/03		
FINISH			CHK REH 5/29/03		
			REL REH 5/29/03		
			DWG. NO. 1302839		
			REV. AO		
			SCALE - - - SHEET 1 OF 1		



NOTE:
1. ALL WIRE IS 20AWG UNLESS OTHERWISE NOTED.



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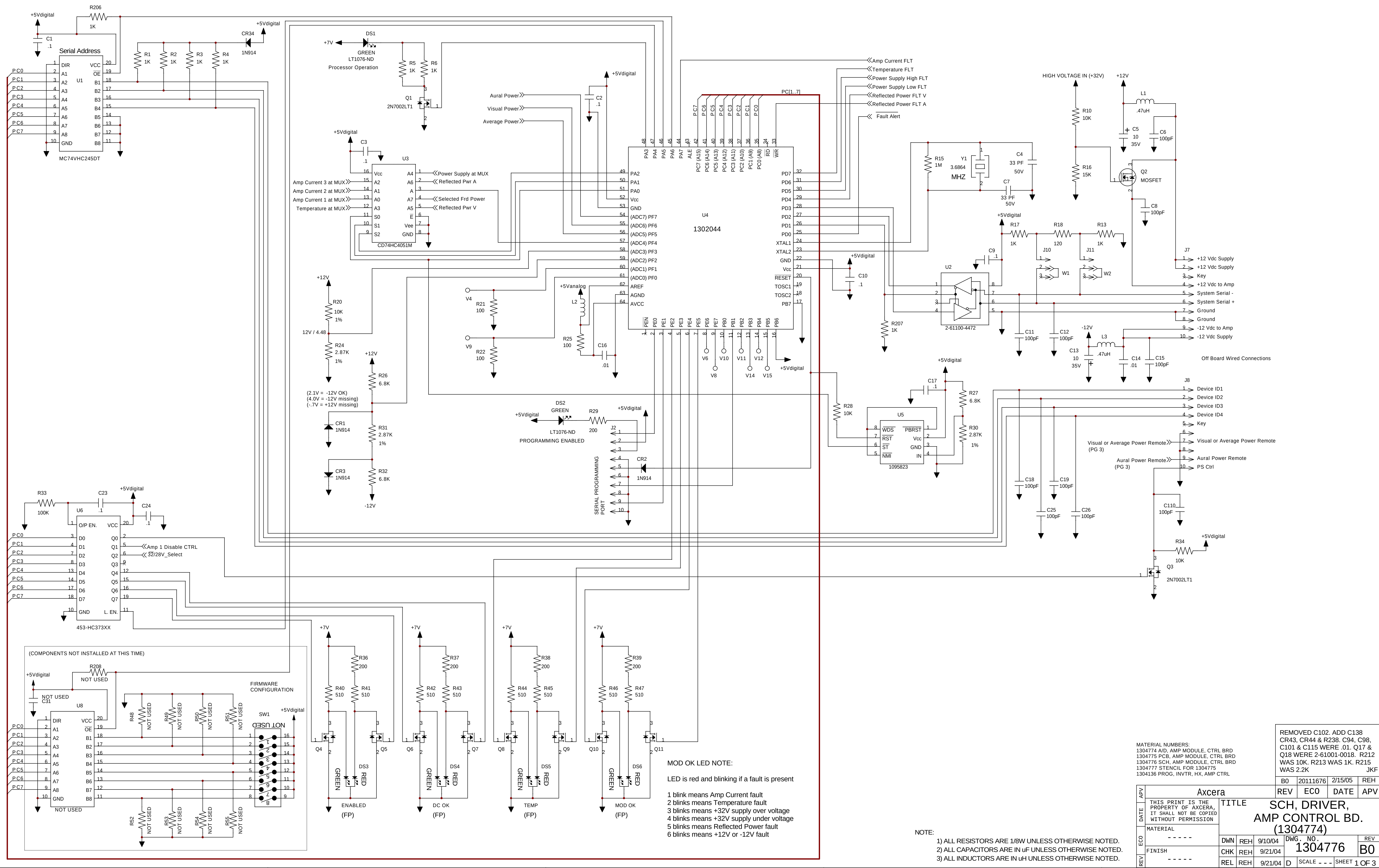
MATERIAL

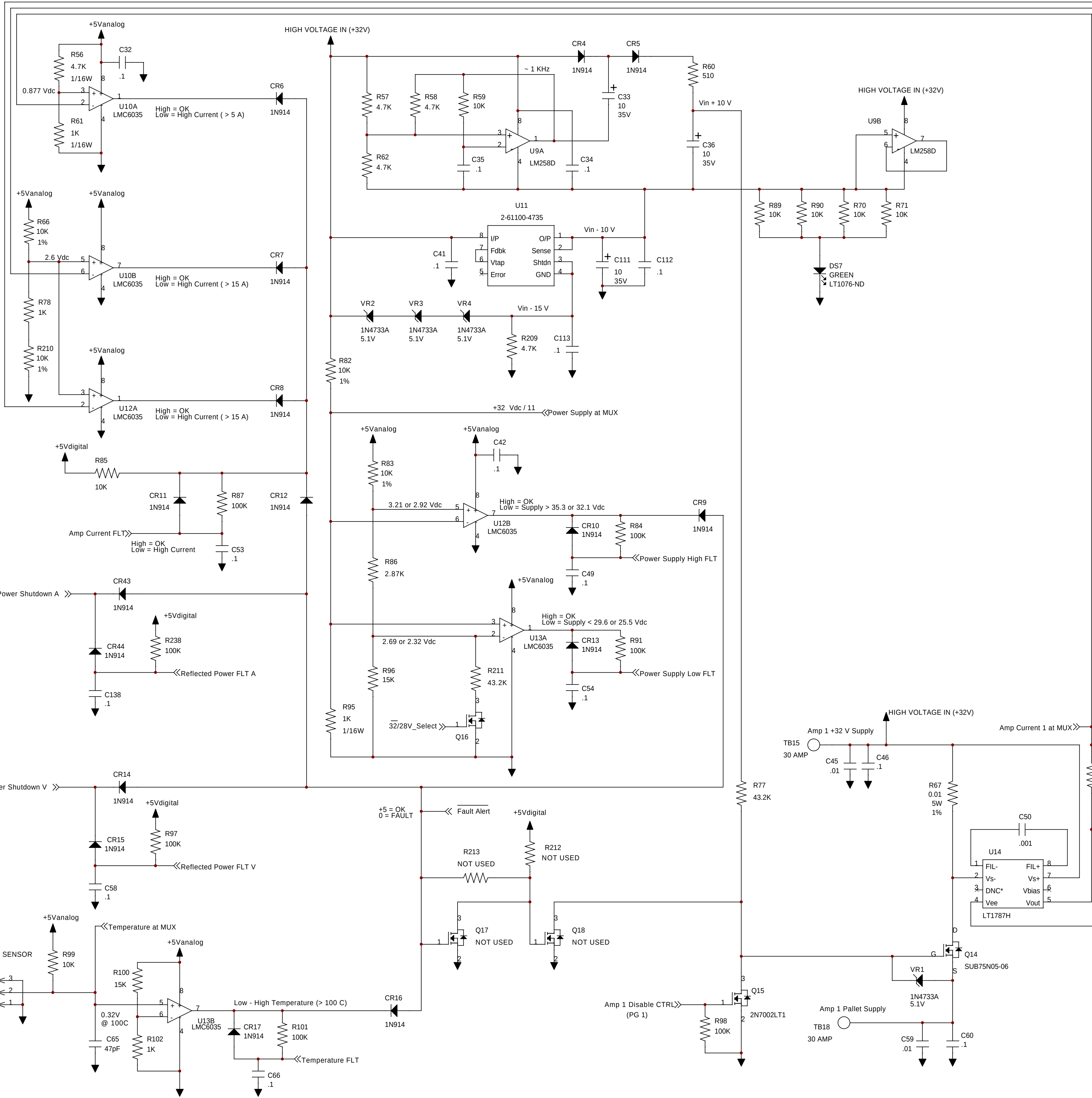
FINISH

TITLE
I/C,
PA ASSEMBLY, 5W
1.67 GHz

DWN	RGE	11/11/04	DWG. NO.	REV
CHK	TB	04/12/05	1305055	A0
REL	TB	04/12/05	SCALE ---	SHEET 1 OF 1

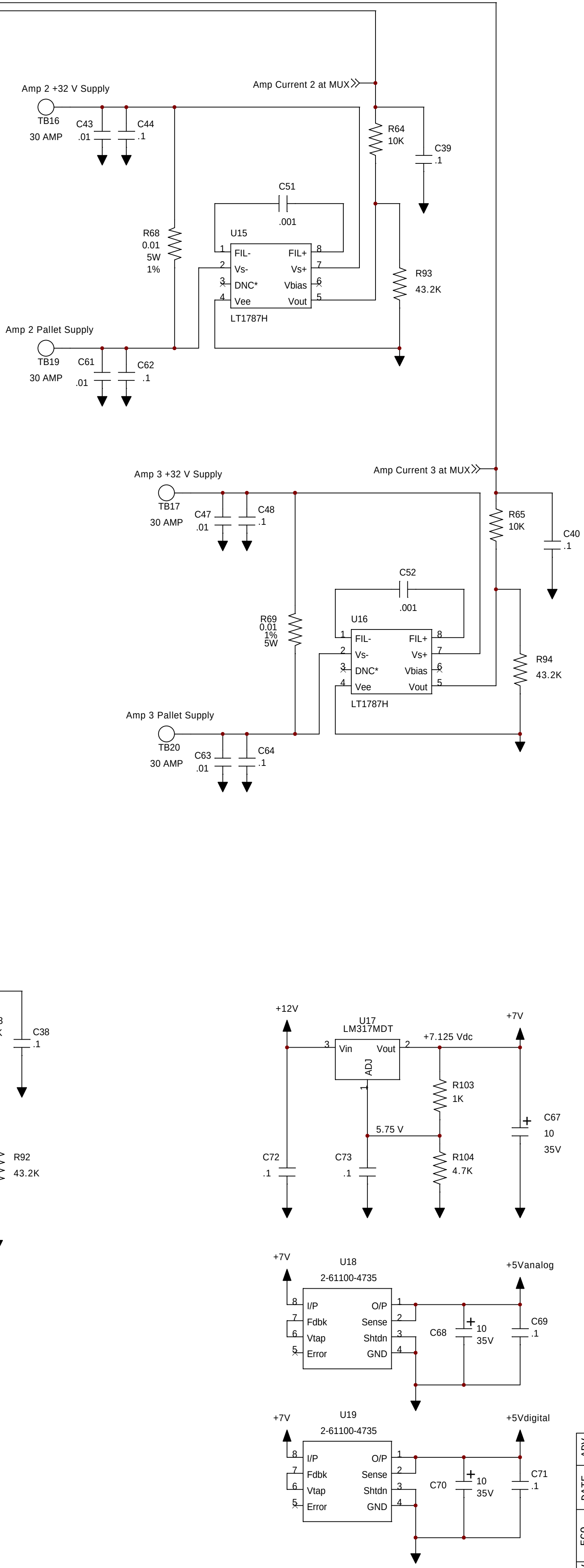
REV	ECO	DATE	APV
-----	-----	------	-----





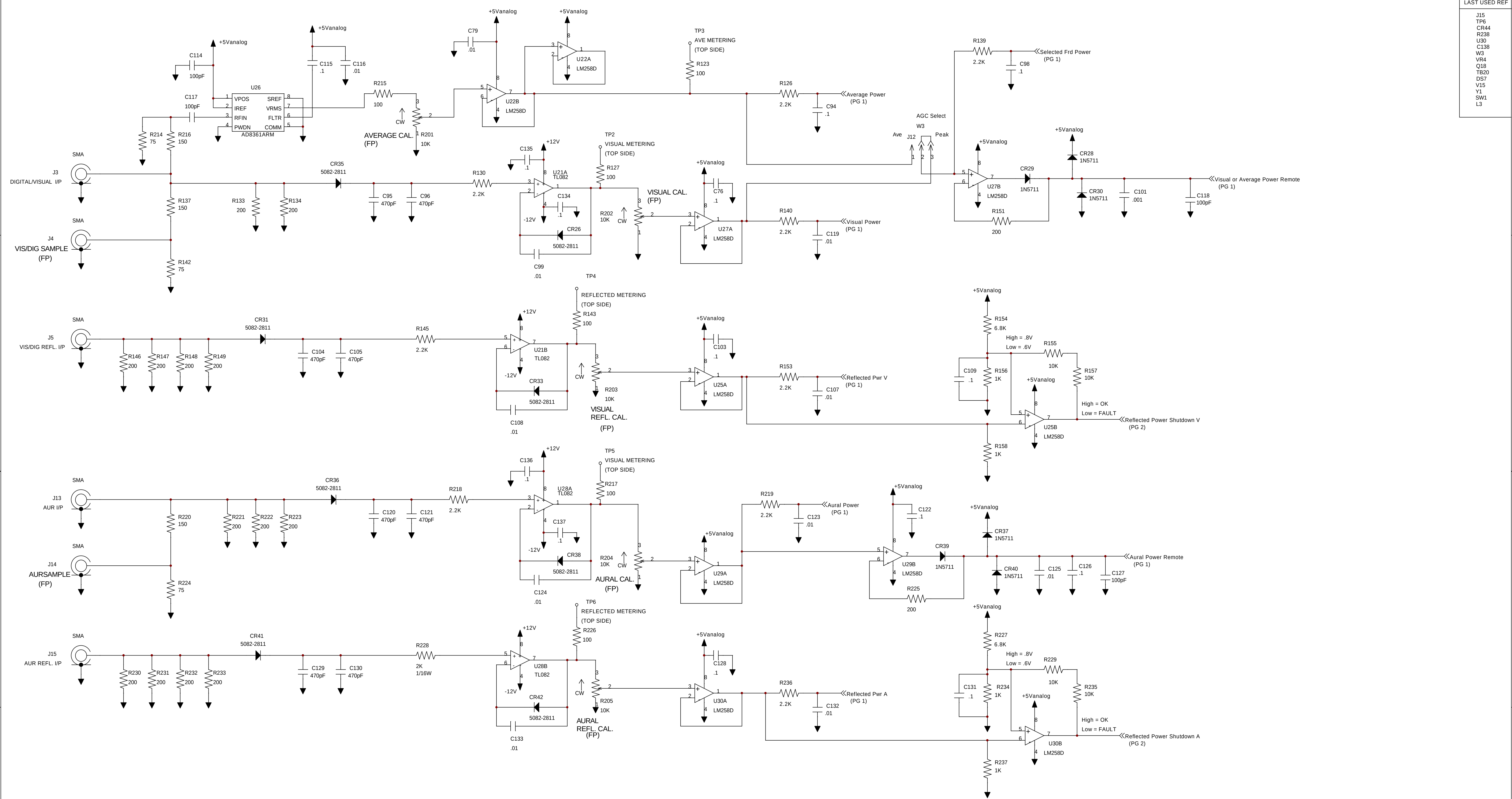
NOTE:

A track width of 0.3 inch is needed between power input vias and sense resistors R55,R56,R57. 0.3 inch track width is also needed between the output of the sense resistors and the output vias that provide power to the amplifier pallets.

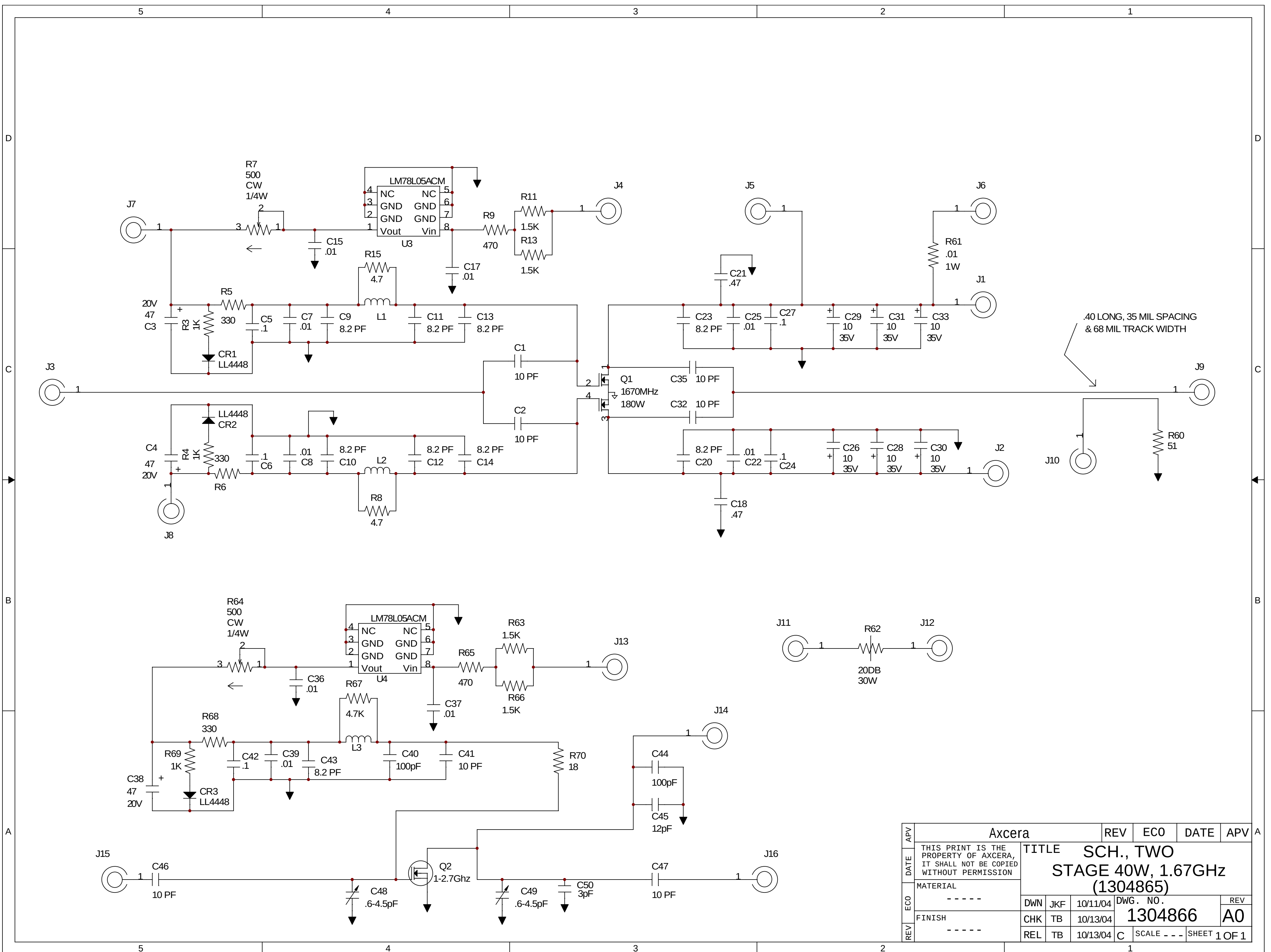


APV		Xxcera		REV	ECO	DATE	APV
DATE		THIS PRINT IS THE PROPERTY OF AXCCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION		TITLE		SCH, DRIVER, AMP CONTROL BD. (1304774)	
ECO		MATERIAL		DWN	REH	9/10/04	REV
REV		FINISH		CHK	REL	9/21/04	B0
				REL	REL	9/21/04	D
				SCALE		SHEET 2 OF 3	

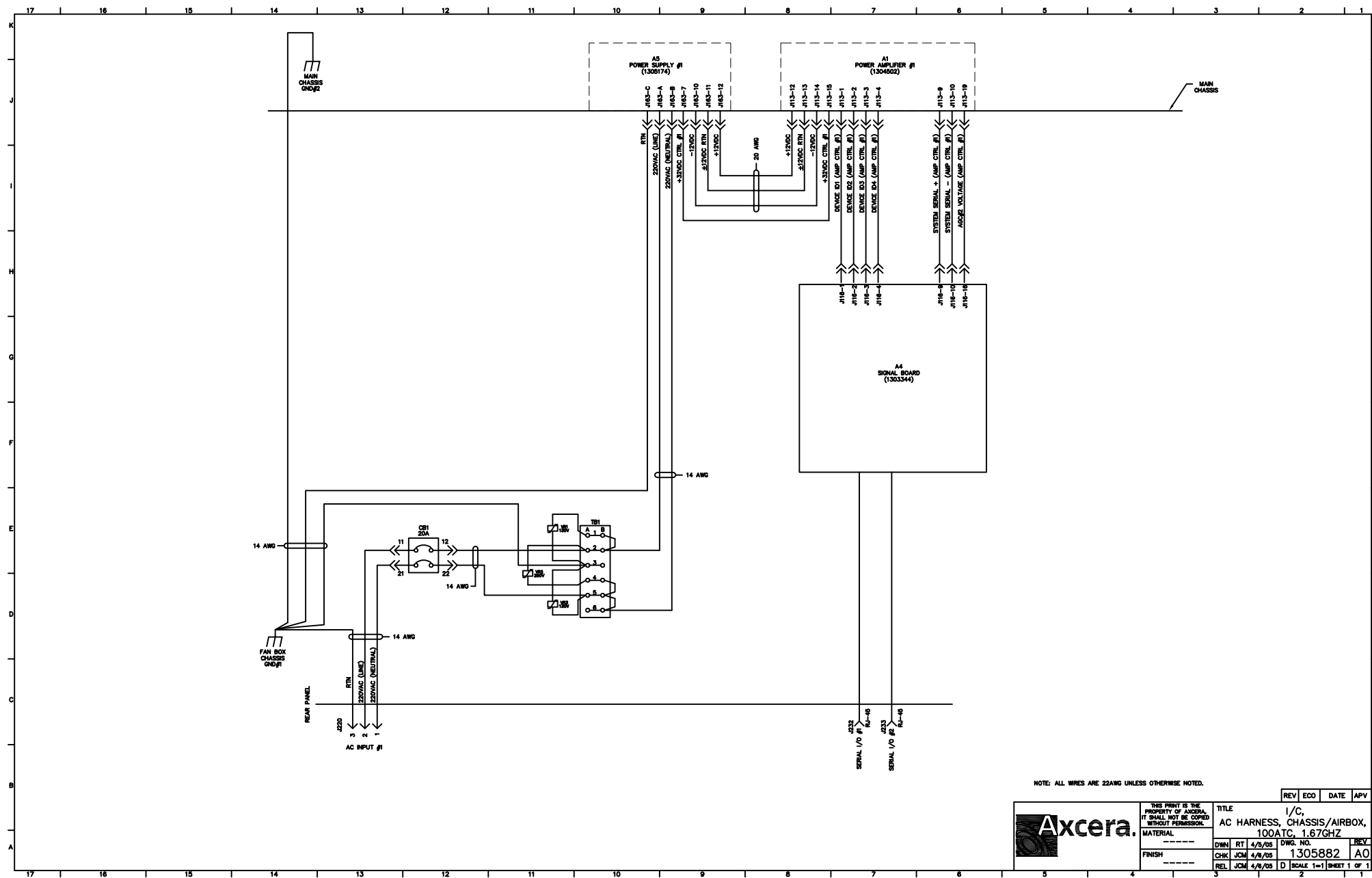
LAST USED REF	
J15	TP6
CR44	CR238
U30	C138
W3	VR4
Q18	T820
DS7	V15
Y1	SW1
L3	



APV	Axcera				REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION				TITLE SCH, AMP. MODULE, CTRL BD (1304774)			
ECO					DWN	REH	9/10/04	DWG. NO. 1304776
REV					CHK	REL	9/21/04	B0
					REL	REH	9/21/04	D SCALE - - - SHEET 3 OF 3

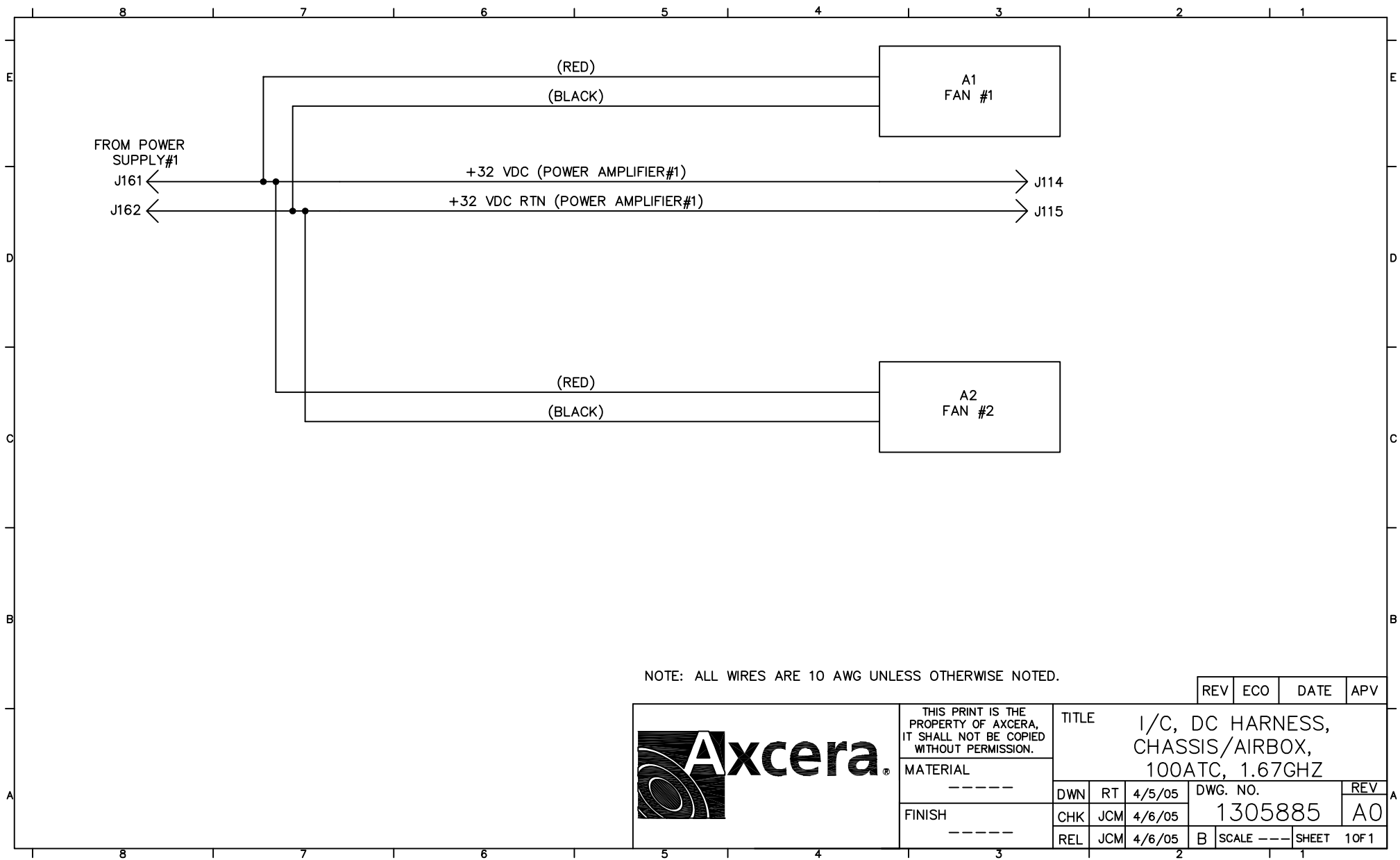


APV	Axcera				REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION				TITLE SCH., TWO STAGE 40W, 1.67GHz (1304865)			
ECO	MATERIAL -----				DWN	JKF	10/11/04	DWG. NO. 1304866
REV	FINISH -----				CHK	TB	10/13/04	REL
					REL	TB	10/13/04	C
					SCALE - - -		SHEET 1 OF 1	



NOTE: ALL WIRES ARE 22AWG UNLESS OTHERWISE NOTED.

REV		ECO	DATE	APV
Axcera.				
THIS PRINT IS THE PROPERTY OF AXCERA. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				
MATERIAL		DWN		REV
FINISH		CHK		NO.
		REL		1305882
		D		A0
		SCALE		1=1
				SHEET 1 OF 1

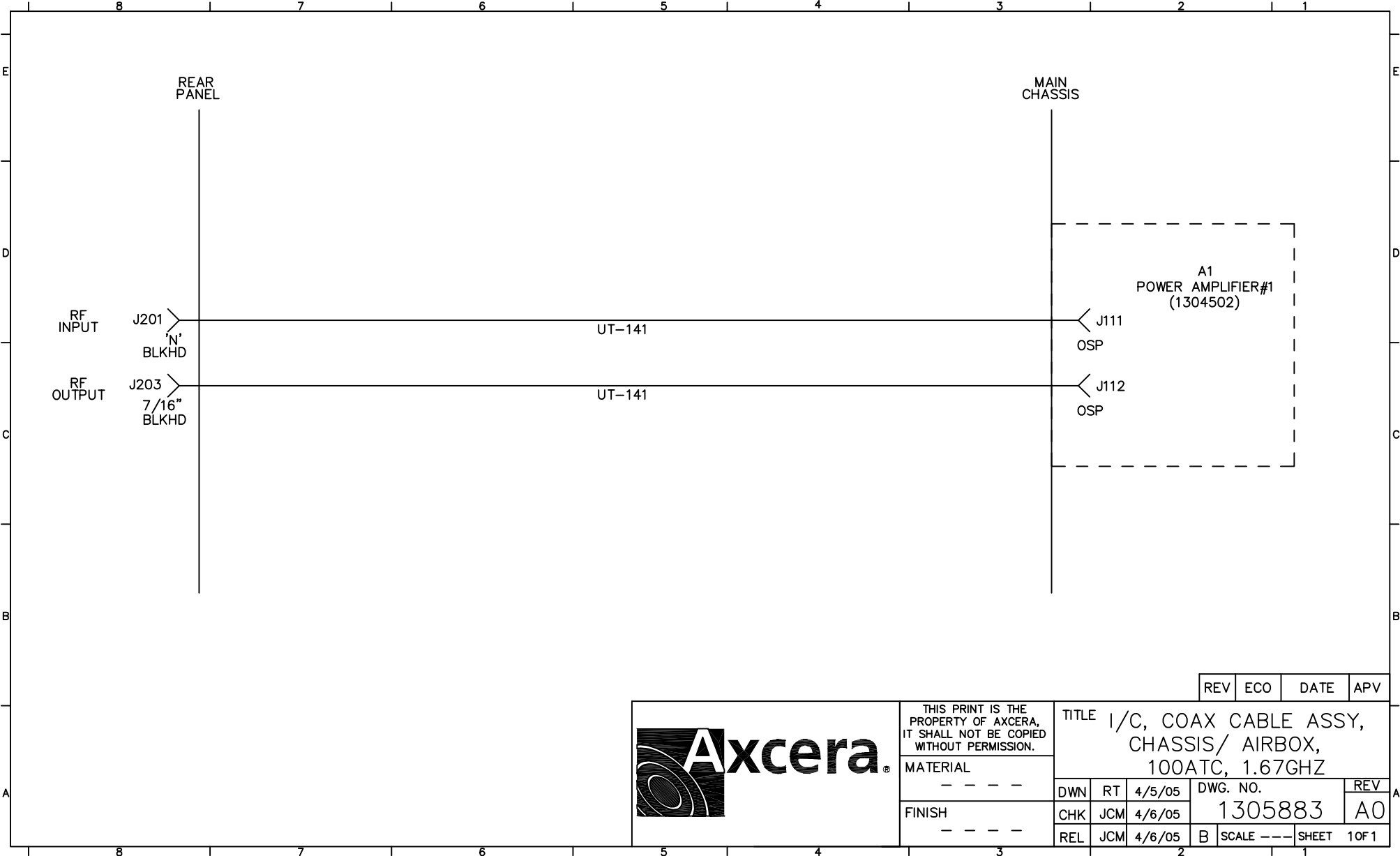


NOTE: ALL WIRES ARE 10 AWG UNLESS OTHERWISE NOTED.

REV	ECO	DATE	APV
-----	-----	------	-----

 Axcera®	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION.			TITLE I/C, DC HARNESS, CHASSIS/AIRBOX, 100ATC, 1.67GHZ					
	MATERIAL -----			DWN	RT	4/5/05	DWG. NO.		REV
	FINISH -----			CHK	JCM	4/6/05	1305885		A0
				REL	JCM	4/6/05	B	SCALE ---	SHEET 10F1

REV
A0

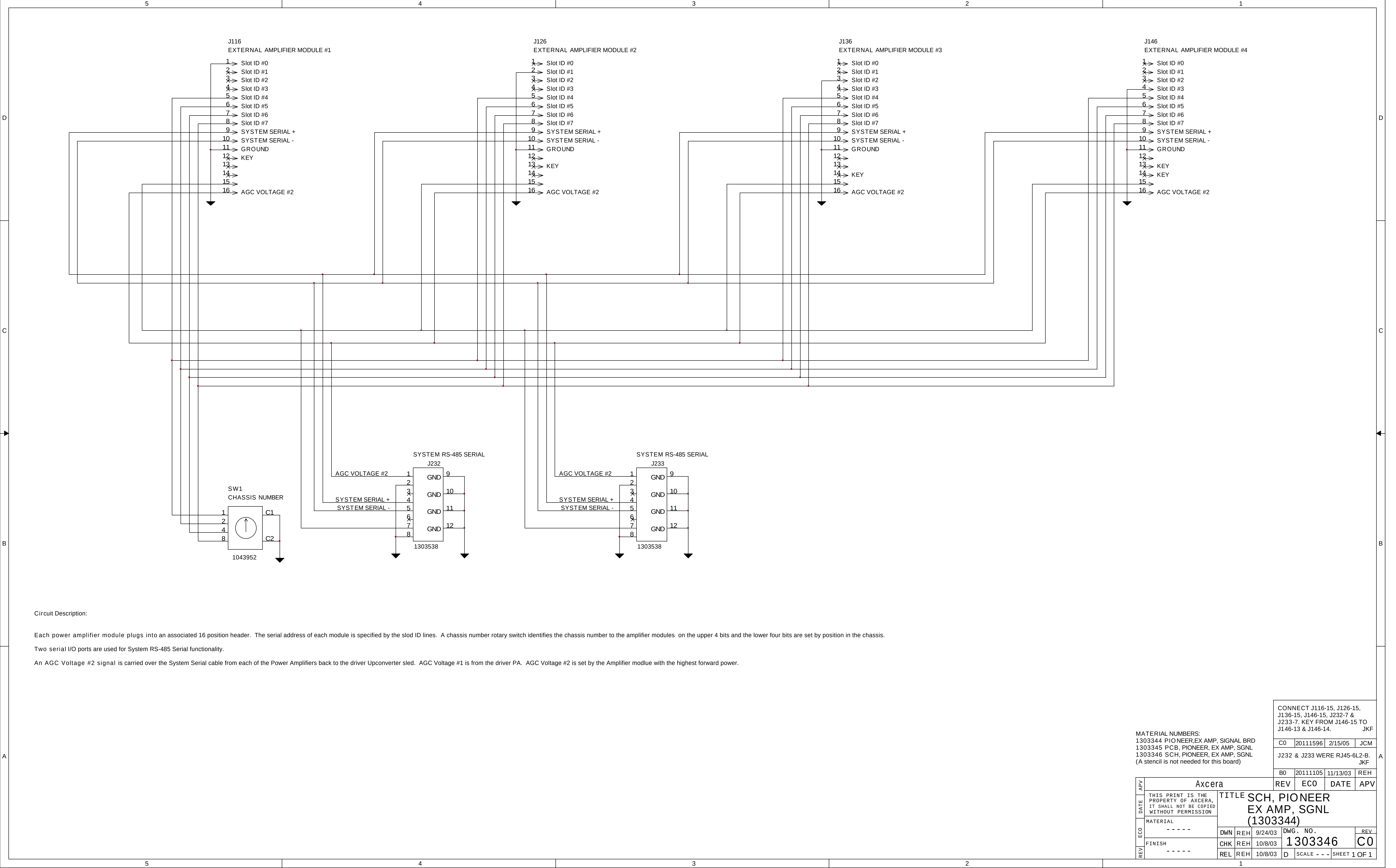


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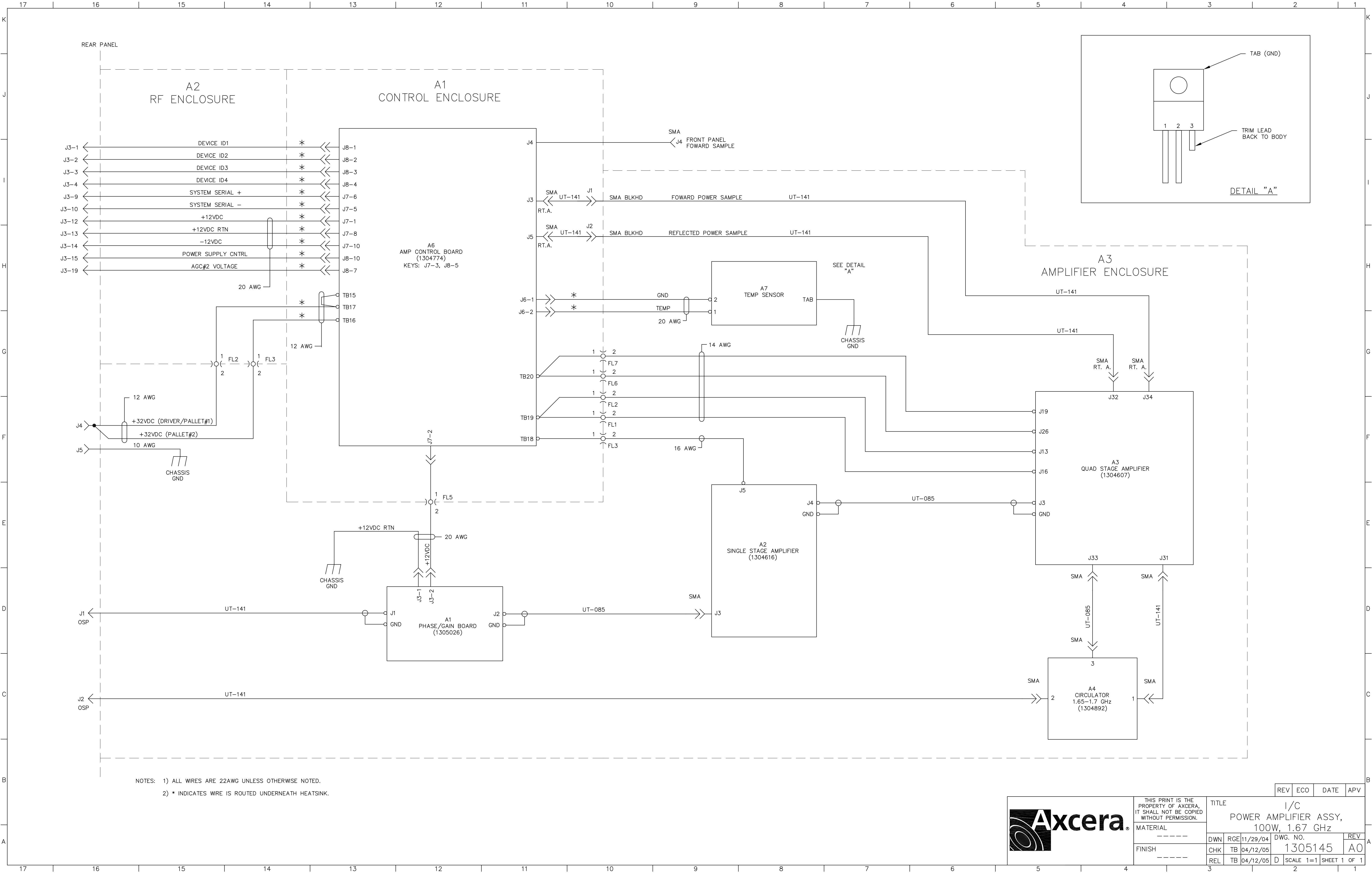
MATERIAL
- - - -

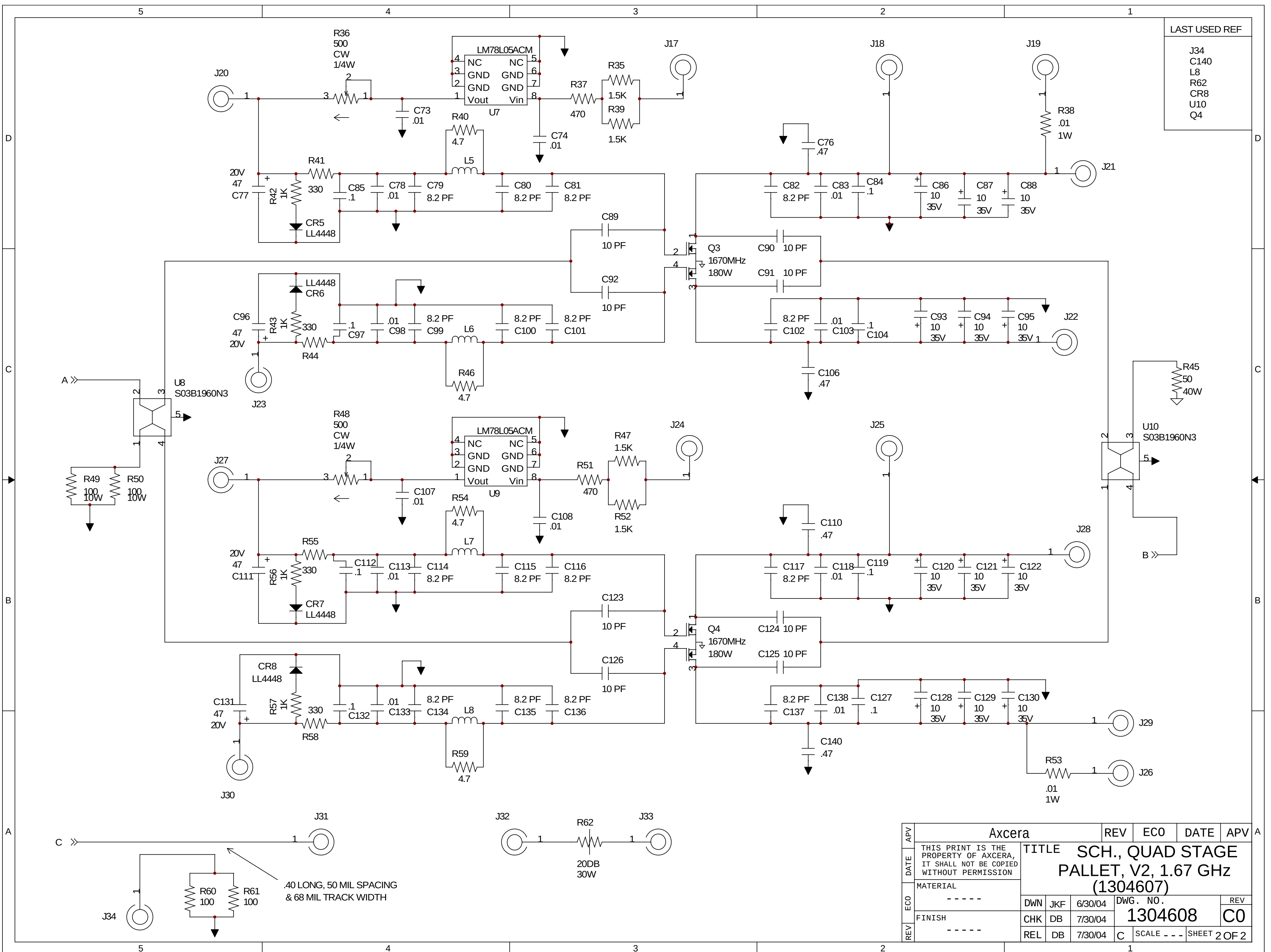
FINISH
- - - -

TITLE I/C, COAX CABLE ASSY, CHASSIS/ AIRBOX, 100ATC, 1.67GHZ				REV	ECO	DATE	APV
DWN	RT	4/5/05	DWG. NO. 1305883	REV A0			
CHK	JCM	4/6/05					
REL	JCM	4/6/05	B	SCALE ---	SHEET	10F1	



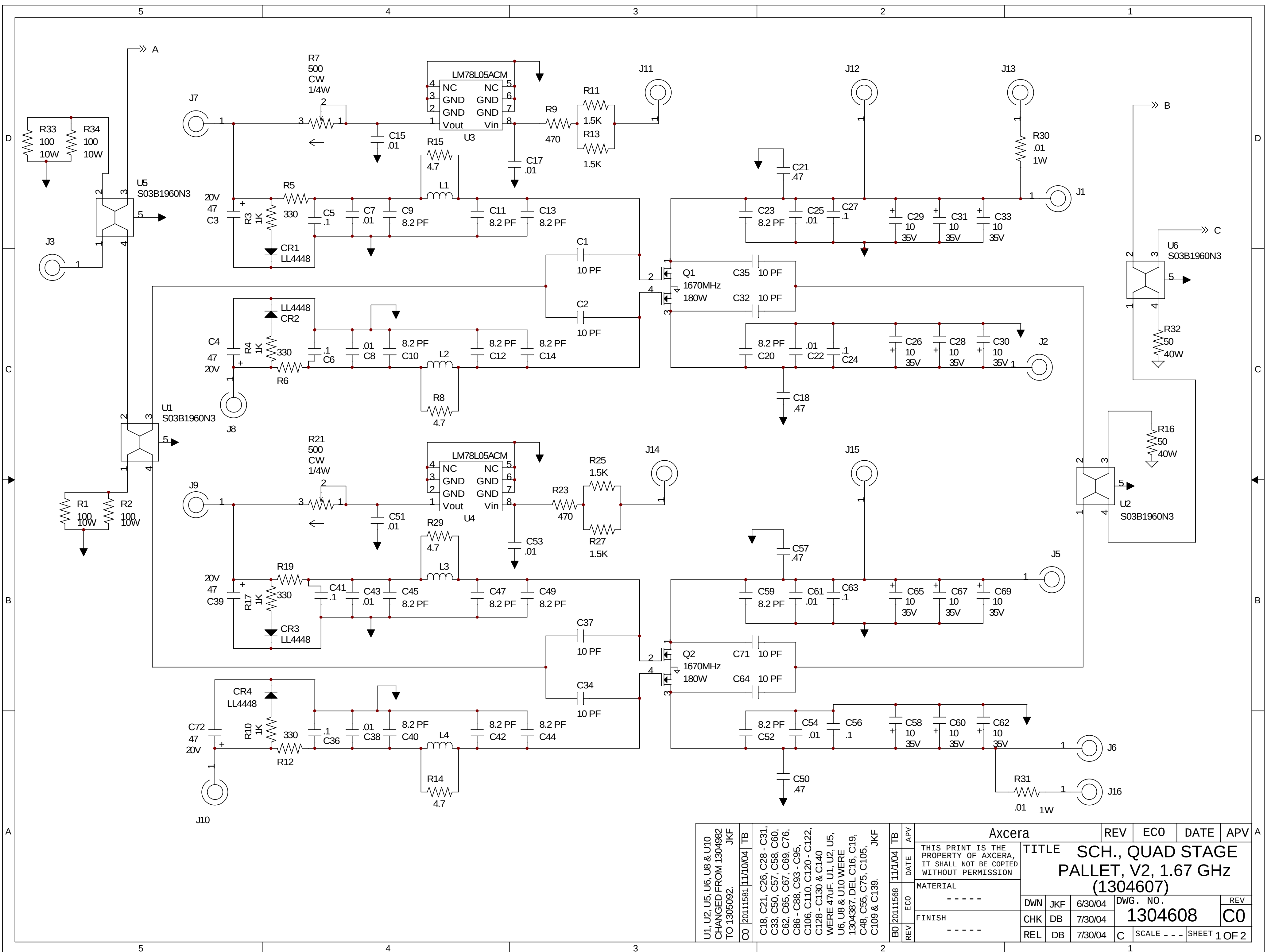
MATERIAL NUMBERS: 1303344 PIONEER,EX AMP, SIGNAL BRD 1303345 PCB, PIONEER, EX AMP, SGNL 1303346 SCH, PIONEER, EX AMP, SGNL (A stencil is not needed for this board)						J146-13 & J146-14. JKF			
						C0	20111596	2/15/05	JCM
						J232 & J233 WERE RJ45-6L2-B. JKF			
						B0	20111105	11/13/03	REH
						REV	ECO	DATE	APV
APV	Axcera								
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION					TITLE SCH, PIONEER EX AMP, SGNL (1303344)			
ECO	MATERIAL								
	-----					DWN	REH	9/24/03	DWG. NO.
FINISH						CHK	REH	10/8/03	1303346
REV	-----					REL	REH	10/8/03	C0
						D	SCALE - - - SHEET 1 OF 1		



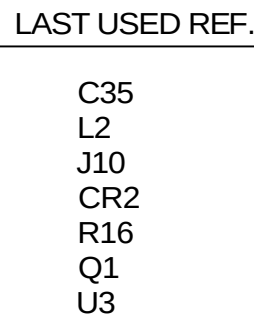


LAST USED REF
J34
C140
L8
R62
CR8
U10
Q4

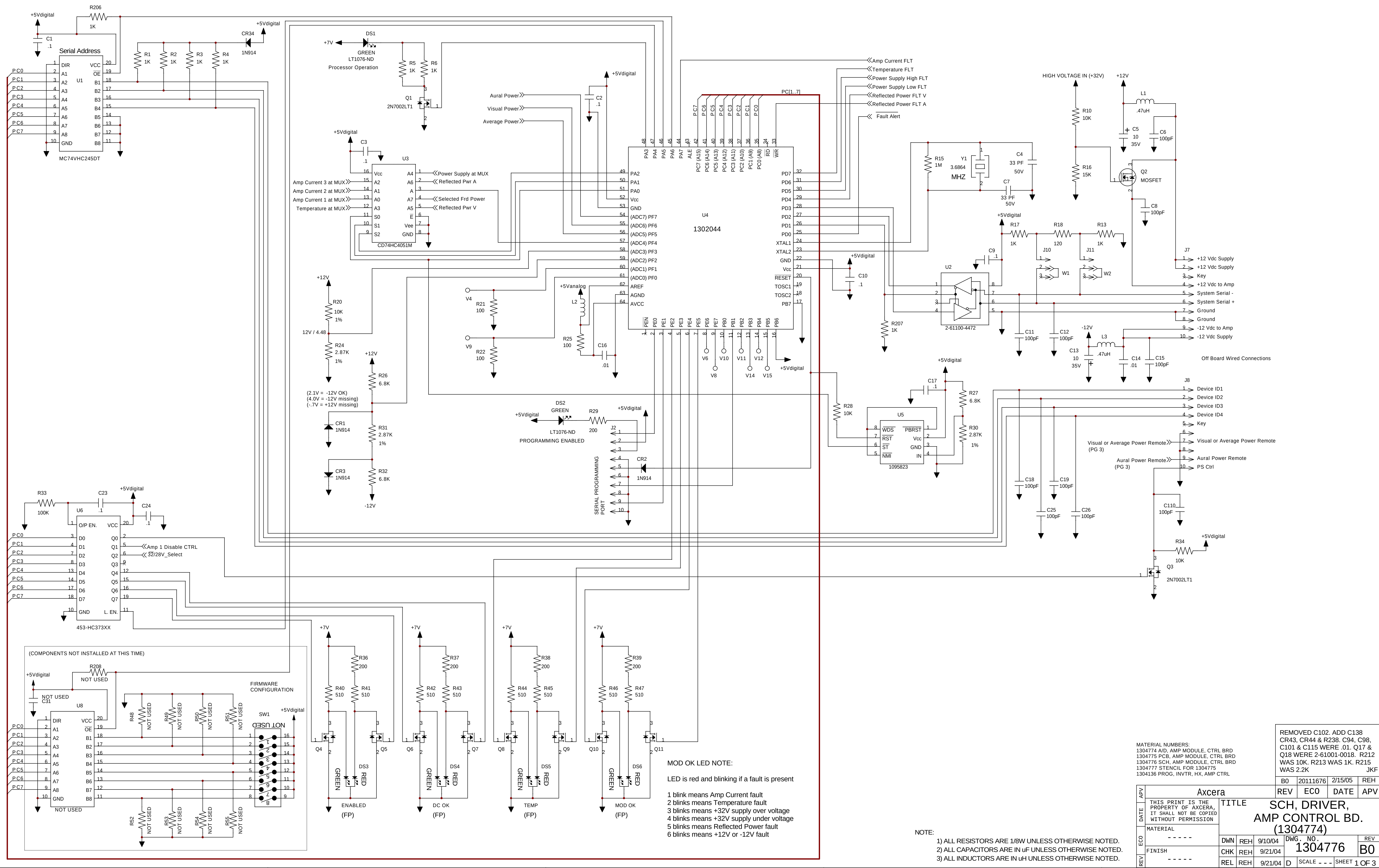
APV	Axcera					REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION					TITLE SCH., QUAD STAGE PALLET, V2, 1.67 GHz (1304607)			
ECO	MATERIAL -----					DWN	JKF	6/30/04	DWG. NO.
REV	FINISH -----					CHK	DB	7/30/04	1304608
						REL	DB	7/30/04	C
								SCALE - - -	SHEET 2 OF 2

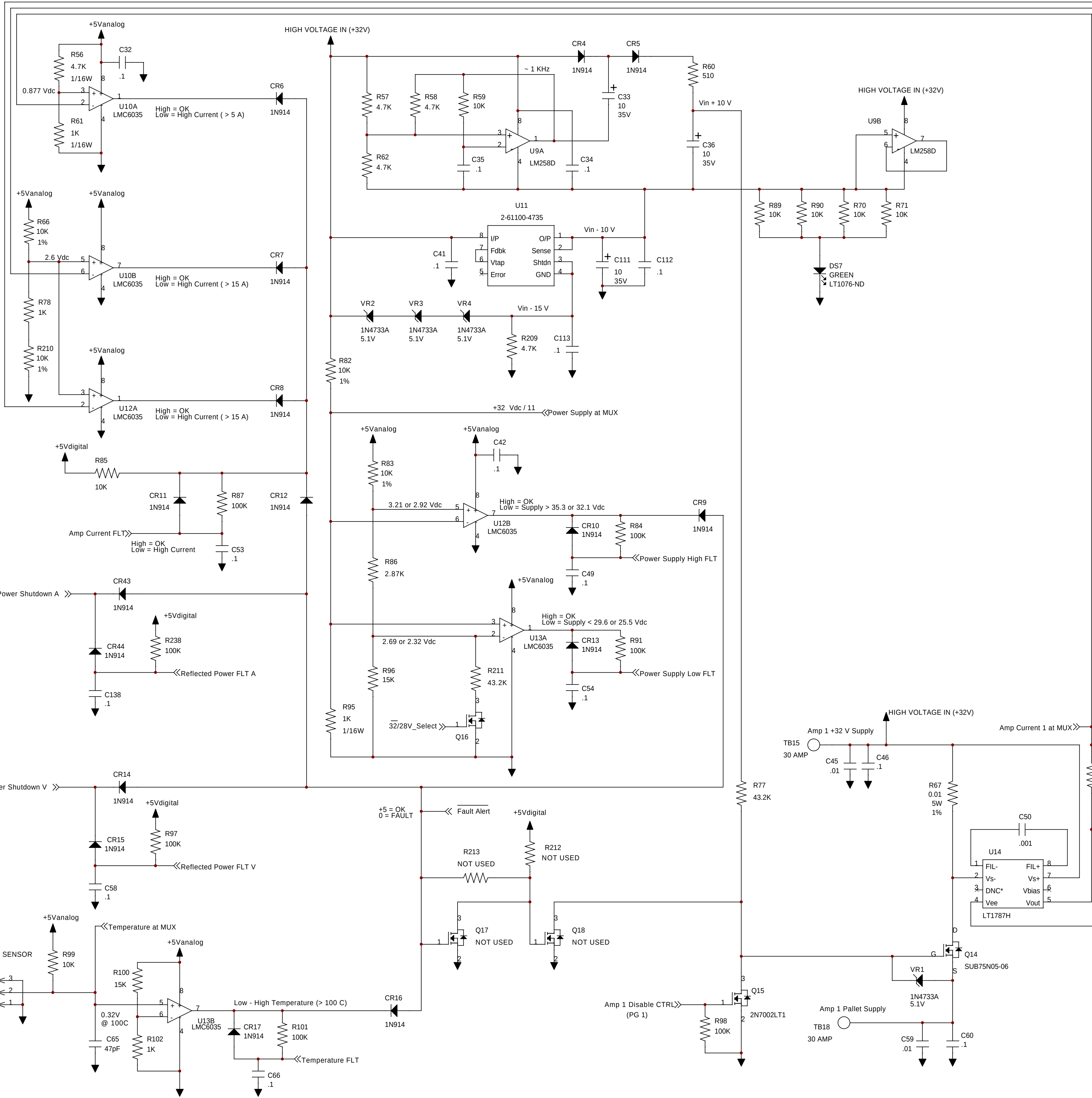


U1, U2, U5, U6, U8 & U10 CHANGED FROM 1304982 TO 1305092. JKF		C0 20111581 11/10/04 TB		C18, C21, C26, C28 - C31, C33, C50, C57, C58, C60, C62, C65, C67, C69, C76, C86 - C88, C93 - C95, C106, C110, C120 - C122, C128 - C130 & C140 WERE 47uF. U1, U2, U5, U6, U8 & U10 WERE 1304387. DEL C16, C19, C48, C55, C75, C105, C109 & C139. JKF		B0 20111568 11/11/04 TB		APV DATE ECO		Axcera		REV	ECO	DATE	APV
												THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION			
												TITLE SCH., QUAD STAGE PALLET, V2, 1.67 GHz (1304607)			
												MATERIAL -----			
FINISH -----		DWN JKF 6/30/04		CHK DB 7/30/04		REL DB 7/30/04		DWG. NO. 1304608		REV C0					
												SCALE - - - SHEET 1 OF 2			



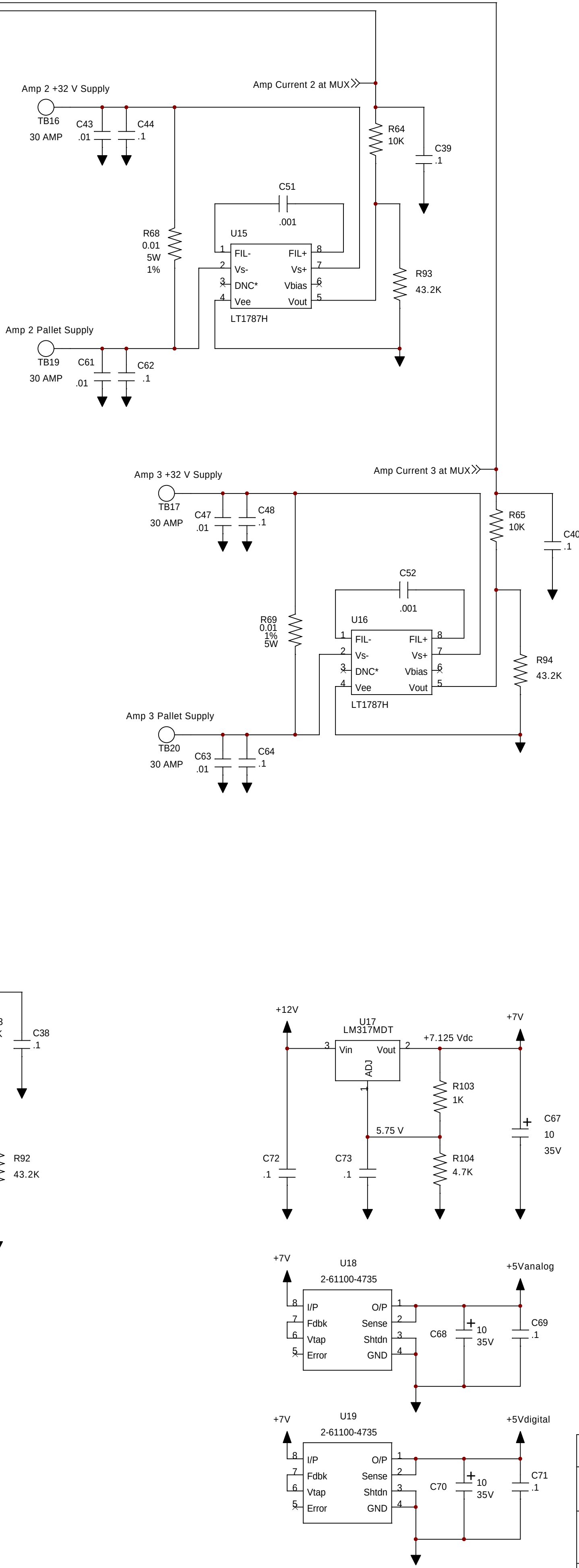
APV	Axcera					REV	ECO	DATE	APV	A
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION		TITLE SCH., SINGLE STAGE PALLET, V2, 1.67 GHz (1304616)							
ECO	MATERIAL - - - - -									
REV	FINISH - - - - -		DWN	JKF	6/28/04	DWG. NO. 1304546			REV A0	
			CHK	DB	7/20/04	C	SCALE - - -	SHEET 1	OF 1	





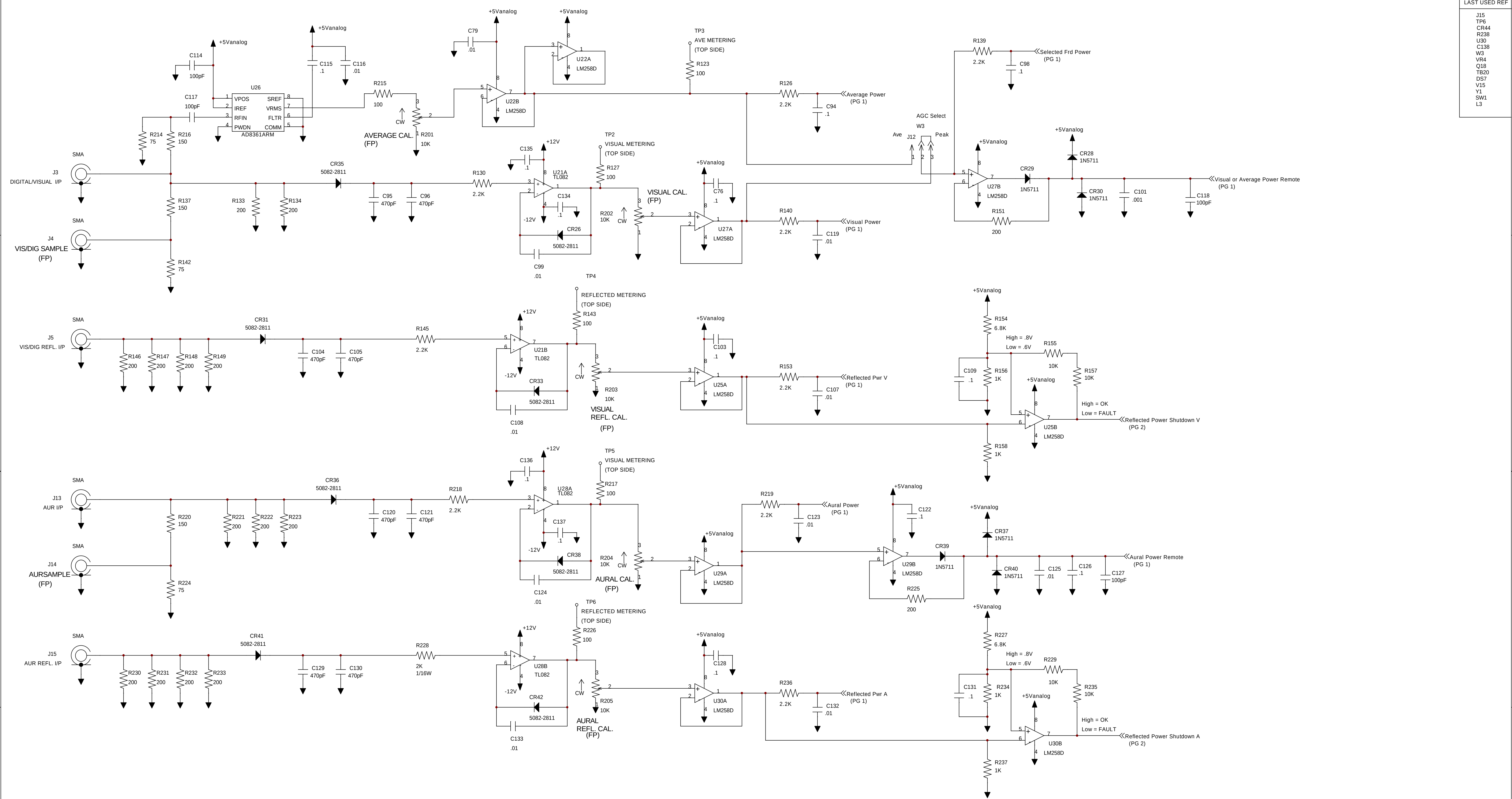
NOTE:

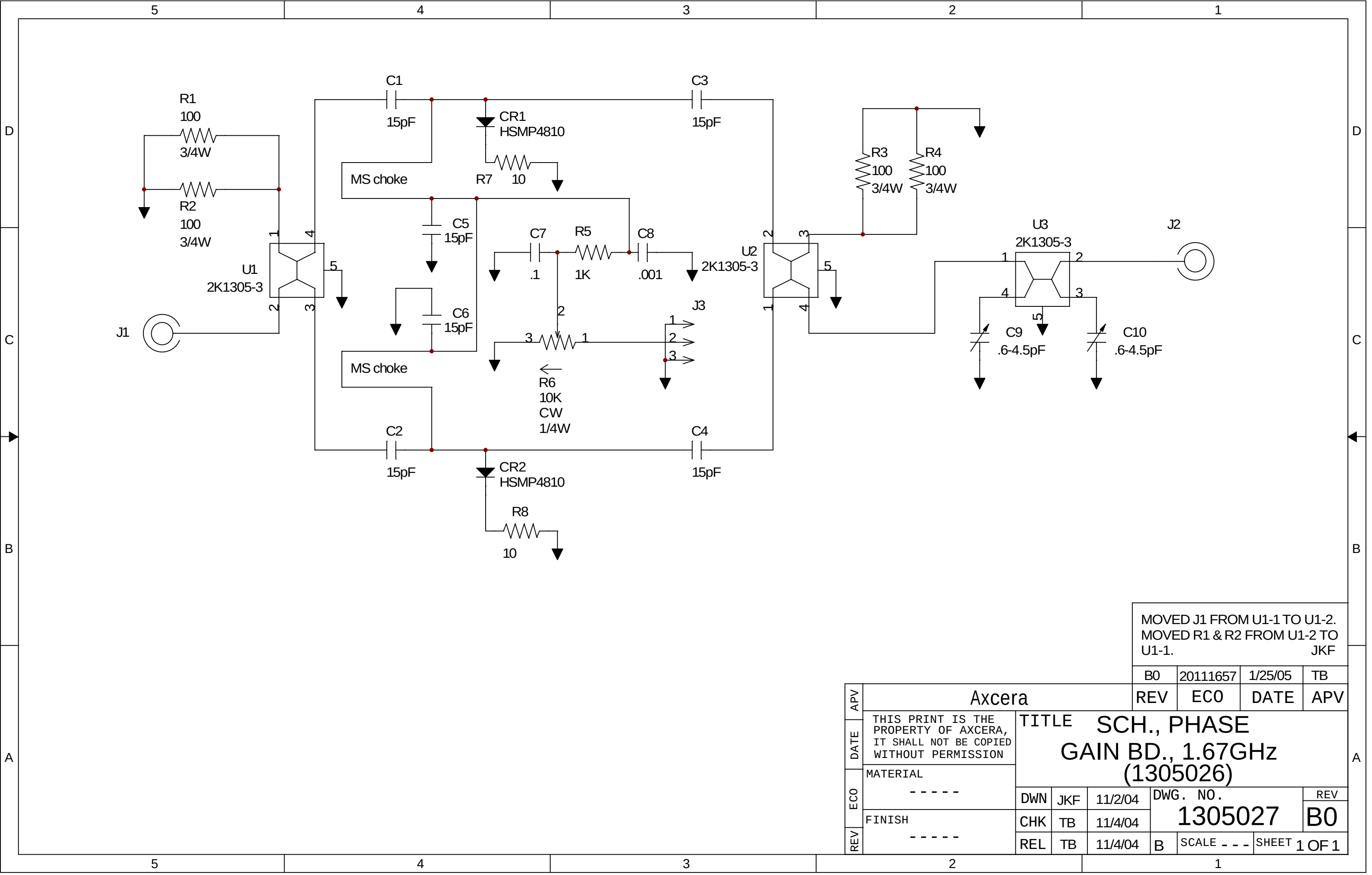
A track width of 0.3 inch is needed between power input vias and sense resistors R55,R56,R57. 0.3 inch track width is also needed between the output of the sense resistors and the output vias that provide power to the amplifier pallets.



APV	Axcera				REV	ECO	DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION				TITLE			
ECO					SCH, DRIVER, AMP CONTROL BD. (1304774)			
REV	FINISH				DWN	REH	9/10/04	REV
					CHK	REH	9/21/04	1304776
					REL	REH	9/21/04	B0
					SCALE - - - SHEET 2 OF 3			

LAST USED REF			
J15	TP6		
CR44			
R238			
C138			
W3			
VR4			
Q18			
T820			
DS7			
V15			
Y1			
SW1			
L3			

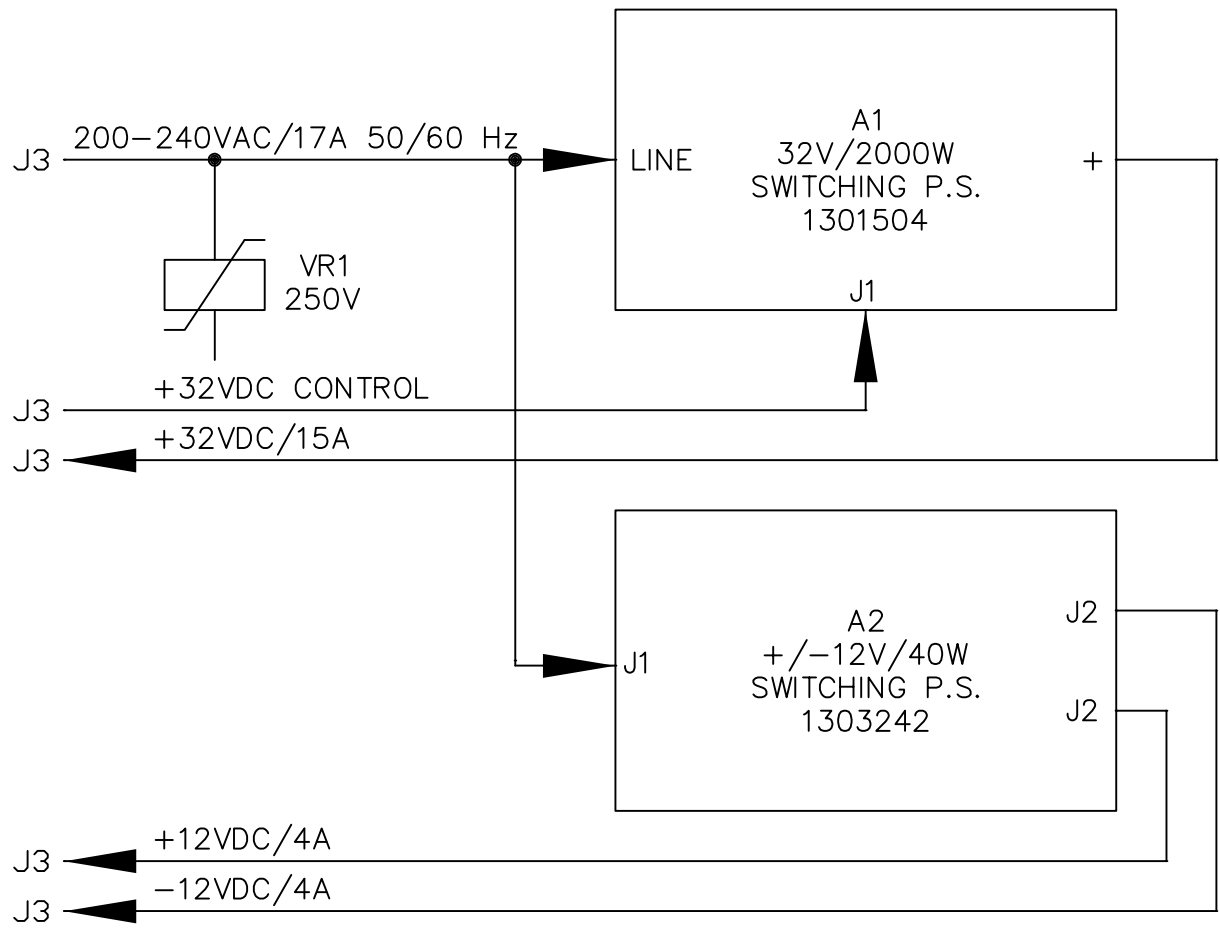




MOVED J1 FROM U1-1 TO U1-2.
MOVED R1 & R2 FROM U1-2 TO
U1-1. JKF

B0	20111657	1/25/05	TB
REV	ECO	DATE	APV

APV	Axcera					REV	ECO		DATE	APV
DATE	THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION		TITLE SCH., PHASE GAIN BD., 1.67GHz (1305026)							
ECO	MATERIAL - - - - -									
REV	FINISH - - - - -		DWN	JKF	11/2/04	DWG. NO. 1305027			REV B0	
			REL	TB	11/4/04	B	SCALE - - -		SHEET 1 OF 1	



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MATERIAL

FINISH

TITLE				
B/D, POWER SUPPLY ASSY, 1KW, INNOVATOR LX				
DWN	RGE	1/27/04	DWG. NO.	
CHK	JCM	1/27/04	1303886	
REL	JCM	1/27/04	A	SCALE ---
			SHEET 1 OF 1	

REV	ECO	DATE	APV
-----	-----	------	-----

