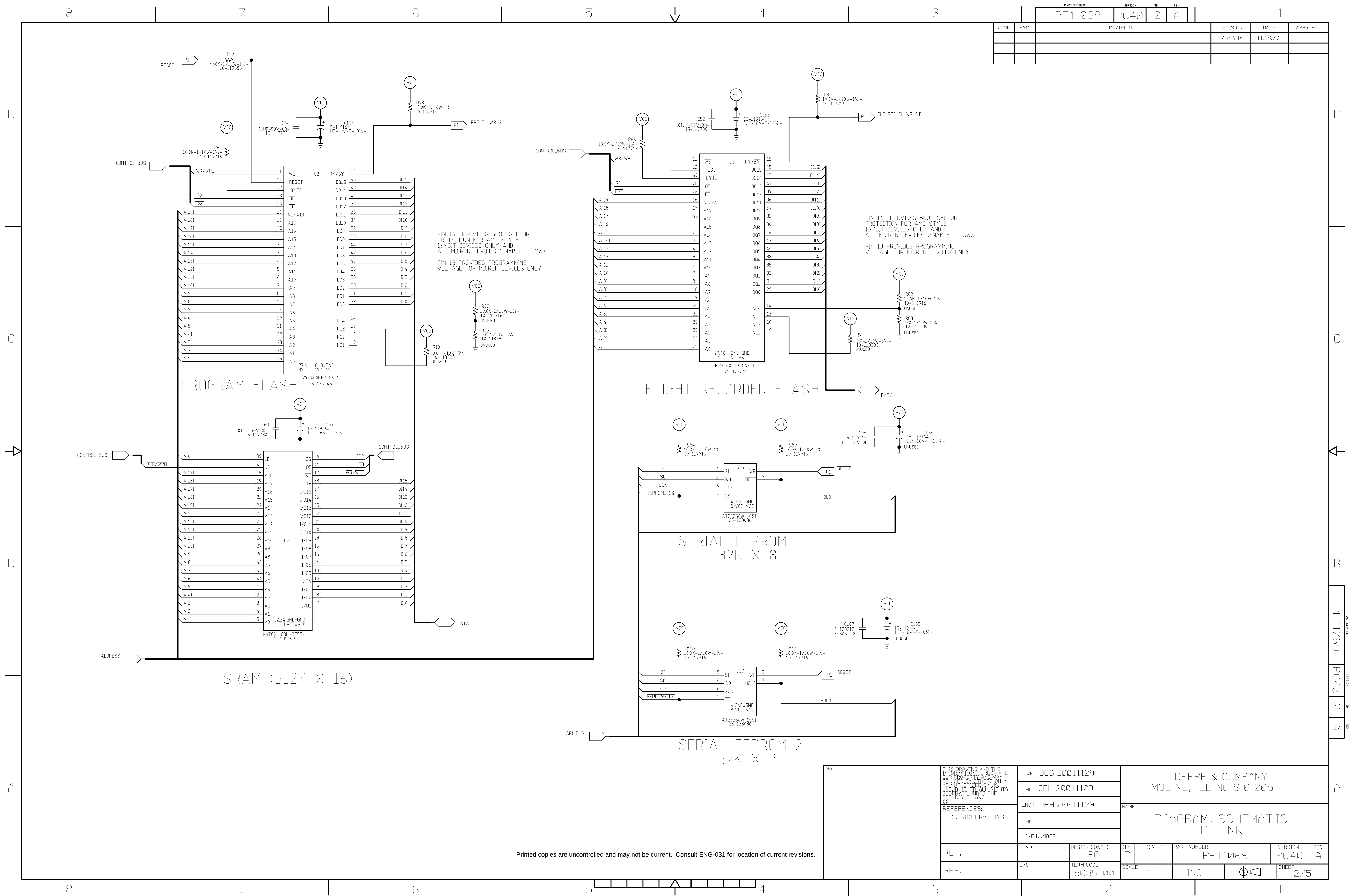
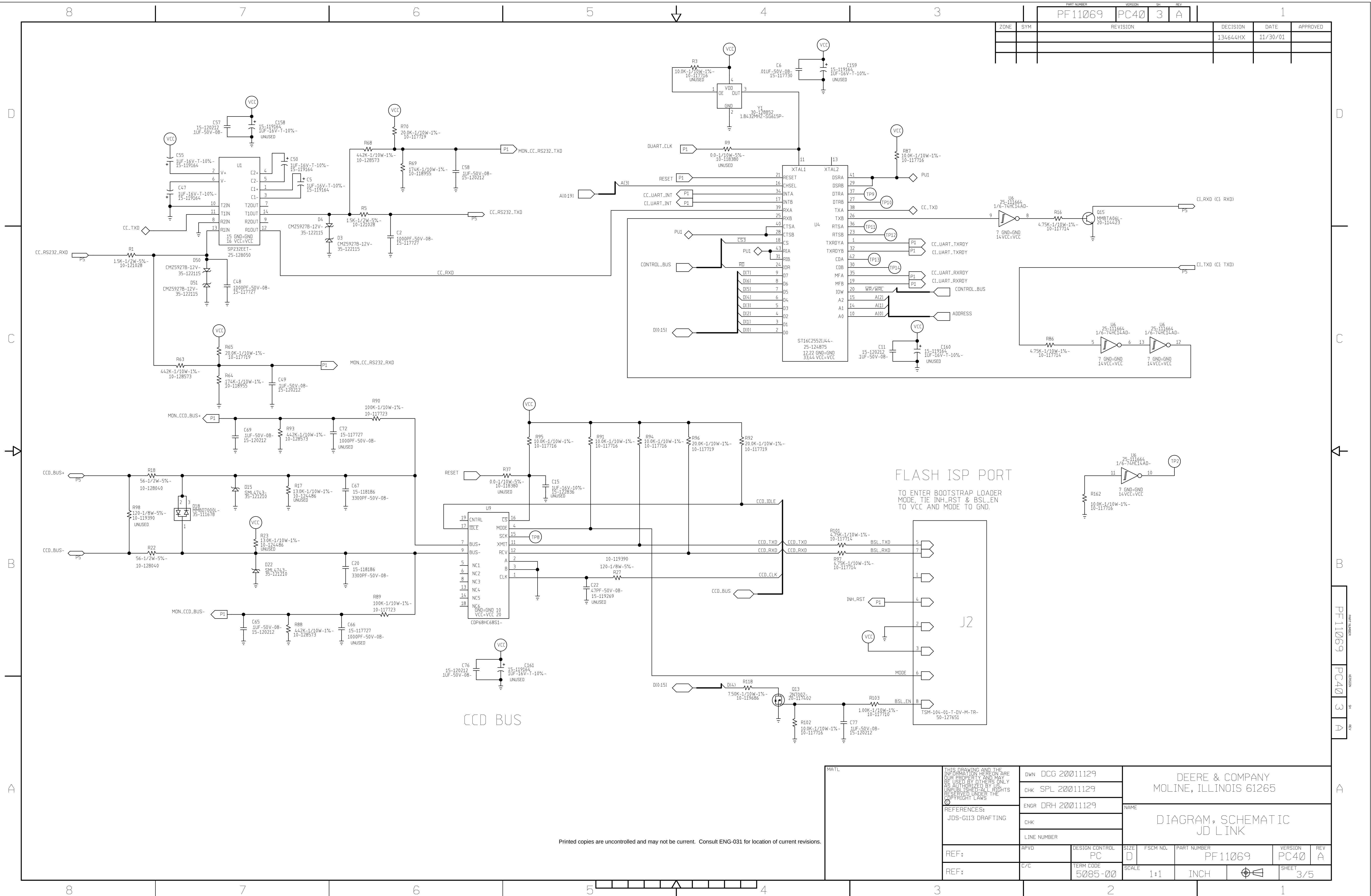
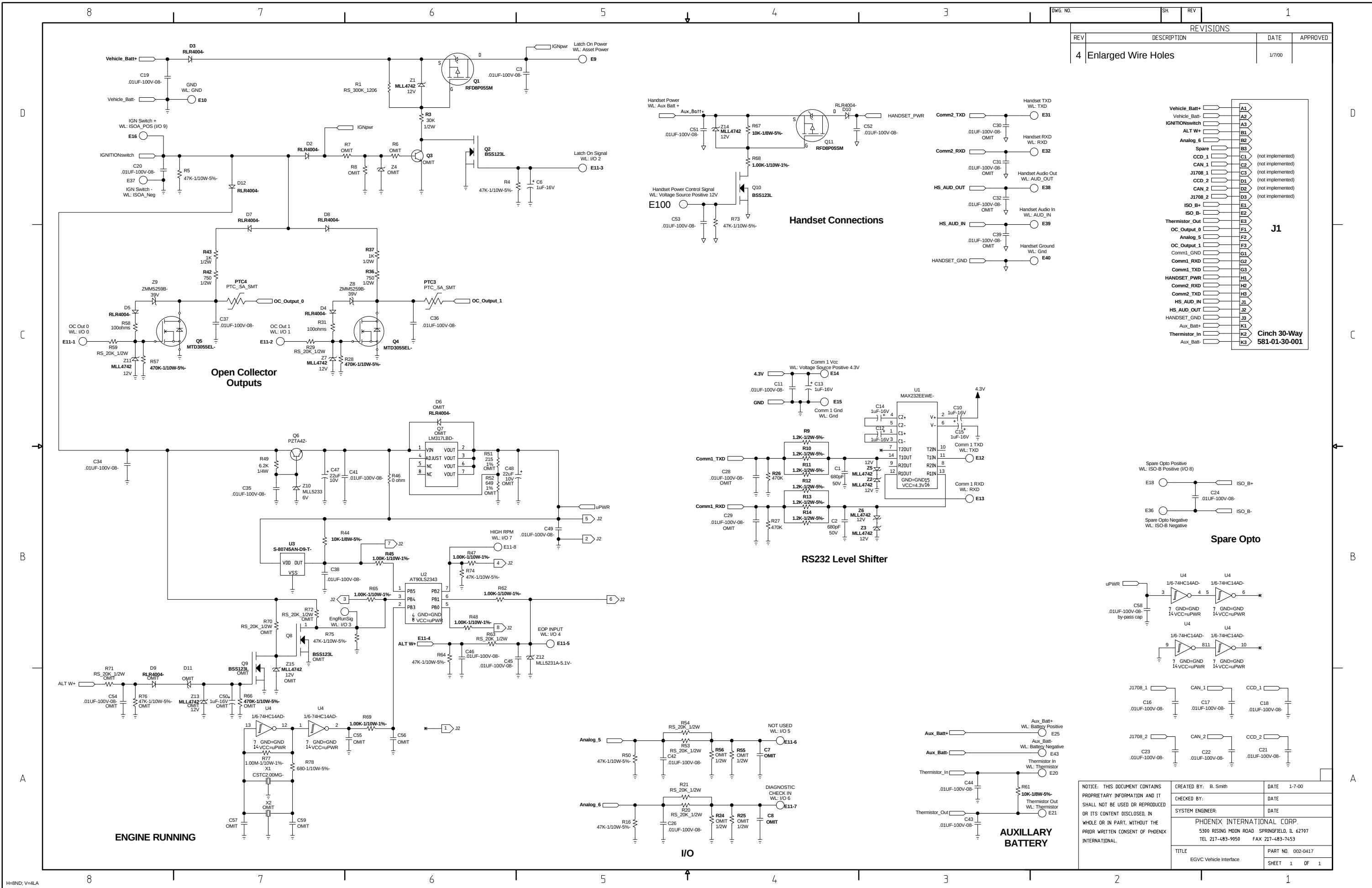








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		REVISIONS		
EV	DESCRIPTION			DATE
4	Enlarged Wire Holes			1/7/00

Pinout diagram for J1 connector (Cinch 30-Way 581-01-30-001):

Pin	Signal	Notes
A1	Vehicle_Batt+	
A2	Vehicle_Batt-	
A3	IGNITIONswitch	
B1	ALT W+	
B2	Analog_6	
B3	Spare	
C1	CCD_1	(not implemented)
C2	CAN_1	(not implemented)
C3	J1708_1	(not implemented)
D1	CCD_2	(not implemented)
D2	CAN_2	(not implemented)
D3	J1708_2	(not implemented)
E1	ISO_B+	
E2	ISO_B-	
E3	Thermistor_Out	
F1	OC_Output_0	
F2	Analog_5	
F3	OC_Output_1	
G1	Comm1_GND	
G2	Comm1_RXD	
G3	Comm1_TXD	
H1	HANDSET_PWR	
H2	Comm2_RXD	
H3	Comm2_TXD	
J1	HS_AUD_IN	
J2	HS_AUD_OUT	
J3	HANDSET_GND	
K1	Aux_Batt+	
K2	Thermistor_In	
K3	Aux_Batt-	

Schematic for Spare Opto:

- Spare Opto Positive:** WL: ISO-B Positive (I/O 8) connected to E18 terminal.
- Spare Opto Negative:** WL: ISO-B Negative connected to E36 terminal.
- A 0.01uF-100V-08 capacitor (C24) is connected between the positive and negative lines.

Spare Opto

Schematic for U4 and U4 components:

- U4 (1/6-74HC14AD):**
 - Pin 3 (U4) connected to uPWR.
 - Pin 7 (U4) connected to GND.
 - Pin 14 (U4) connected to uPWR.
 - Pin 5 (U4) connected to GND.
 - Pin 14 (U4) connected to uPWR.
- U4 (1/6-74HC14AD):**
 - Pin 7 (U4) connected to GND.
 - Pin 14 (U4) connected to uPWR.
 - Pin 9 (U4) connected to GND.
 - Pin 14 (U4) connected to uPWR.

Bypass capacitors (0.01uF-100V-08):

- C58: uPWR to GND.
- C16: J1708_1 to GND.
- C17: CAN_1 to GND.
- C18: CCD_1 to GND.
- C23: J1708_2 to GND.
- C22: CAN_2 to GND.
- C21: CCD_2 to GND.

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	SYSTEM ENGINEER:	DATE
	PHOENIX INTERNATIONAL CORP. 5300 RISING MOON ROAD SPRINGFIELD, IL 62707 TEL 217-483-9050 FAX 217-483-7453	
TITLE EGVC Vehicle Interface		PART NO. 002-0417
		SHEET 1 OF 1