

Exhibit 2

General Information

1. Production Plans

Quantity Production Planned

2. Technical Description - Section 2.1033 (c)

(1) The full name and mailing address of the manufacturer of the device and the applicant

Applicant: Kyocera Wireless Corporation
10300 Campus Point Drive
San Diego, CA 92121

Manufacture: Kyocera Wireless Corporation
10300 Campus Point Drive
San Diego, CA 92121

(2) FCC Identifier

FCC ID: OVFKWC-5135

(3) User's Manual

Sent separate

(4) Types of Emission

40K0F8W
40K0F1D
1M25F9W

F3E voice
F3D supervisory audio tones, signaling tones
F1D wideband data signal

(5) Frequency range

The frequency range of the equipment in Domestic Public Cellular Radio Telecommunications Service bands, 824 - 849 MHz and 869 - 894 MHz for FM and cellular CDMA. The channel spacing is 30 kHz for FM.

The frequency range of the equipment in the Personal Communications Services (PCS) bands, 1850 – 1910 MHz and 1930 – 1990 MHz. The channel spacing is 1.25 MHz for CDMA.

(6) Operating power levels

The transmitter output power is independent of whether the equipment operates in the cellular system FM or CDMA mode, or PCS system CDMA mode. The equipment supports Class 3 Cellular Mobile Station Power Class, and Class 2 PCS Mobile Station Power Class. Its power output capability is reported to the Land Station via Station Class Mark. The equipment will respond to commands from the Land Station to change power levels as defined in the EIA/TIA/IS-98 and ANSI J-STD-018 Specification.

(7) Maximum output power

The equipment supports the maximum output power for Class 3 Cellular Mobile Station which is -2 dBW ERP for a FM mode and in the range of -7 dBW to 0 dBW ERP for a CDMA mode, and meets the 7 W ERP (+8.45 dBW) maximum power limitation of Section 22.913.

The equipment supports the maximum output power for Class 2 PCS Mobile Station which is in the range of -7dBW to 0 dBW EIRP, and is within the limited 2 watts E.I.R.P. peak power of CFR 47 Part 24.232 (b). The equipment is able to limit the output power to the minimum necessary for successful communications.

(8) Final RF amplifying device power consumption

The equipment is powered by lithium ion rechargeable batteries which have a voltage range of 3.4 to 4.2 Vdc.

In the Cellular band, the power consumption of the high power amplifier is about 31dBm watts. In the PCS band, the power consumption of the high power amplifier is about 27.9dBm.

(9) Tune-up procedure over the power range

All frequency and power adjustments are set at the factory and there are no field adjustments for this product. Under digital mode, frequency is locked to the base station and controlled by VCTCXO adjustments to offset any possible errors.

(10) Circuit description

(a) Circuit diagram and list of semiconductor device

See parts list that was sent separate

(b) Circuit description for frequency determining and stabilizing

The circuit provided for determining and stabilizing frequency is shown in the schematics.

A voltage controlled, temperature compensated, crystal oscillator (VCTCXO) is employed as a frequency reference for all of the transceiver local oscillators. This crystal oscillator is specified to remain within +/- 2.5 ppm over temperature and voltage variations. The lock status indicator of all synthesizers is monitored by the microprocessor and an out of lock condition will inhibit transmission. In

all modes, the mobile receiver monitors the received signal and adjusts the frequency of the VCTCXO, this corrects any errors between the mobile frequency and the base station transmitter. The mobile is locked to the base station.

(c) Circuit description for spurious radiation suppression

The circuit provided for suppression of spurious radiation is in the schematics.

The transmitter front end provides filtering of the RF signal in order to meet FCC specifications. For radiated spurious suppression, proper design techniques and the use of proper shielding techniques reduced the emission levels well below the permissible FCC limit.

(d) Circuit description for limiting modulation

The circuit provided for limiting modulation is in the schematics.

AMPS Mode

The F3E audio modulation is accomplished through the use of Digital Signal Processor (DSP). The audio signal is converted to digital samples at 8 kHz sample rate. The samples are filtered, integrated, interpolated, and phase modulated at a 40 kHz rate. The resulting signal is then decomposed into I and Q signals, oversampled again at 160 kHz rate, and then sent to the digital-to-analog converter after proper filtering. The transmit audio modulation limiting function is performed digitally in the DSP. The pre-emphasis is performed through an IIR filter and the filtering of audio frequencies is performed through a FIR filter in DSP. The combined performance of these filters is shown in Exhibit 6 along with the actual audio frequency response of the modulated carrier signal. The DSP clocks are locked to the reference VCTCXO output signal, and maintained within ± 2.5 ppm tolerance.

CDMA Mode

The CDMA mode is described in the following pages from the TIA/EIA /IS-95B Standard. The justification for the CDMA bandwidth of 1.25 MHz is that the chip rate is 1.2288 MHz (see page 6-35 of IS-95B). The 1.25MHz is measured at the 3dB down bandwidth. Channel spacing is normally set at this 1.25 MHz. In addition the reference baseband filtering requirements are shown on page 6-60 of IS95B. The Z-transform filter coefficient for the recommended baseband filter are shown on page 6-61, and also yield a "necessary bandwidth" of 1.25 MHz based on optimal detection and channel capacity theory.

6.1.3 Modulation Characteristics

6.1.3.1 Reverse CDMA Channel Signals

The Reverse CDMA Channel is composed of Access Channels and Reverse Traffic Channels. These channels shall share the same CDMA frequency assignment using direct-sequence CDMA techniques. Figure 6.1.3.1-1 shows an example of all of the signals received by a base station on the Reverse CDMA Channel. Each Traffic Channel is identified by a distinct user long code sequence; each Access Channel is identified by a distinct Access Channel long code sequence. Multiple Reverse CDMA Channels may be used by a base station in a frequency division multiplexed manner.

The Reverse CDMA Channel has the overall structure shown in Figure 6.1.3.1-2. Data transmitted on the Reverse CDMA Channel is grouped into 20 ms frames. All data transmitted on the Reverse CDMA Channel is **convolutionally** encoded, block interleaved, modulated by the **64-ary** orthogonal modulation, and direct-sequence spread prior to transmission.

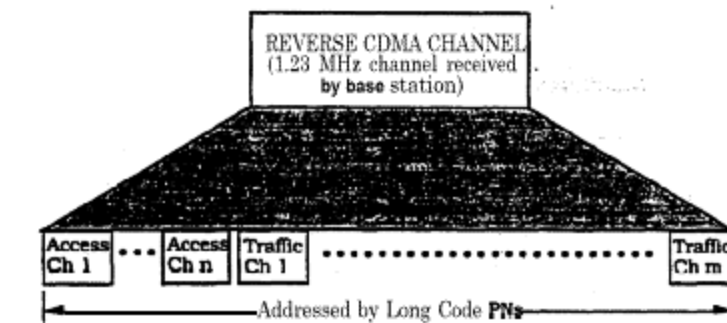


Figure 6.1.3.1-1. Example of Logical Reverse CDMA Channels Received at a Base Station

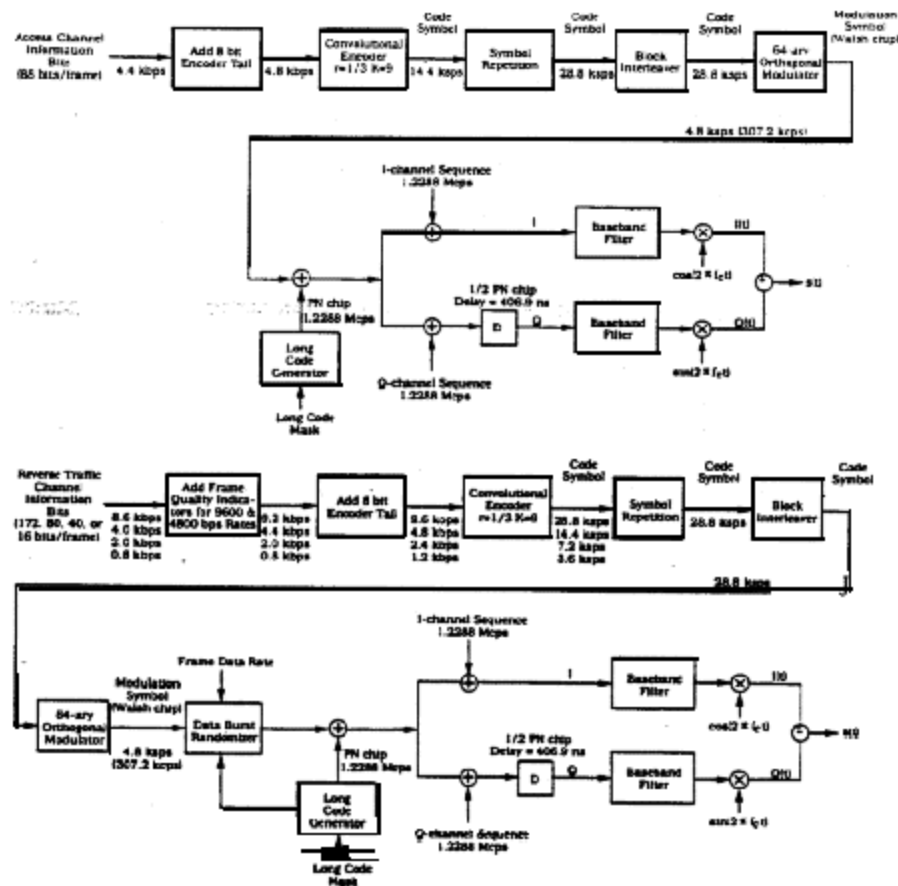


Figure 6.1.3.1-2. Reverse CDMA Channel Structure

After adding frame quality indicators for both the 9600 bps and 4800 bps rates (see 6.1.3.3.2.1) and adding eight Encoder Tail Bits (see 6.1.3.3.2.2), data frames may be transmitted on the Reverse **Traffic** Channel at data rates of 9600, **4800**, **2400**, and 1200 bps. The Reverse **Traffic** Channel may use any of these **data** rates for transmission. The transmission duty cycle on the Reverse Traffic Channel varies with the transmission data rate. Specifically, the transmission duty cycle for 9600 bps frames is 100 percent, the transmission duty cycle for 4800 bps frames is 50 percent, the transmission duty cycle for 2400 bps frames is 25 percent, and the **transmission** duty cycle for 1200 bps frames is 12.5 percent as shown in Table 6.1.3.1.1-1. As the duty cycle for transmission varies **proportionately** with the data rate, the actual burst transmission rate is fixed at 28.800

code symbols per second. Since ~~six~~ code symbols are modulated as one of 64 modulation symbols for transmission, the modulation ~~symbol~~ transmission rate is fixed at 4800 modulation symbols per second. This results in a ~~fixed~~ Walsh chip rate of 307.2 kcps. The rate of the spreading PN sequence is fixed at 1.2288 Mcps, so that each Walsh chip is spread by four PN chips. Table 6.1.3.1.1- 1 defines the signal rates and their relationship for the various transmission rates on the Reverse Traffic Channel.

The numerology is identical for the Access Channel except that the transmission rate is ~~fixed~~ at 4800 bps after adding eight Encoder Tail Bits (see 6.1.3.2.2). Each code symbol is repeated once, and the transmission duty cycle is 100 percent. Table 6.1.3.1.1-2 defines the signal rates and their relationship on the Access Channel.

6.1.3.1.1 Modulation Parameters

The modulation parameters for the Reverse Traffic Channel and the Access Channel are shown in Table 6.1.3.1.1- 1 and Table 6.1.3.1.1-2, respectively.

Table 6.1.3.1.1-1. Reverse Traffic Channel Modulation Parameters

Parameter	Data Rate (bps)				Units
	9600	4800	2400	1200	
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/3	1/3	1/3	1/3	bits/code sym
Transmit Duty Cycle	100.0	50.0	25.0	12.5	%
Code Symbol Rate	28,800	28,800	28,800	28,800	sps
Modulation	6	6	6	6	code sym/mod symbol
Modulation Symbol Rate	4800	4800	4800	4800	sps
Walsh Chip Rate	307.20	307.20	307.20	307.20	kcps
Mod Symbol Duration	208.33	208.33	208.33	208.33	μs
PN Chips/Code Symbol	42.67	42.67	42.67	42.67	PN chip/code symbol
PN Chips/Mod symbol	256	256	256	256	PN chip/mod symbol
PN Chips/Walsh Chip	4	4	4	4	PN chips/Walsh chip

(e) Circuit description for limiting power

Transmitted power is monitored by a RF detector diode which is coupled from the Power Amplifier (PA) output. The detected DC voltage is fed into a microprocessor which uses a calibration table along with an offset correction and temperature correction table to control power limits. When the RF power exceeds a predetermined limit the gain of the stage preceding the PA is reduced.

(11) Photograph of the identification label

Sent separate

(12) Photograph to reveal equipment construction and layout

Sent separate