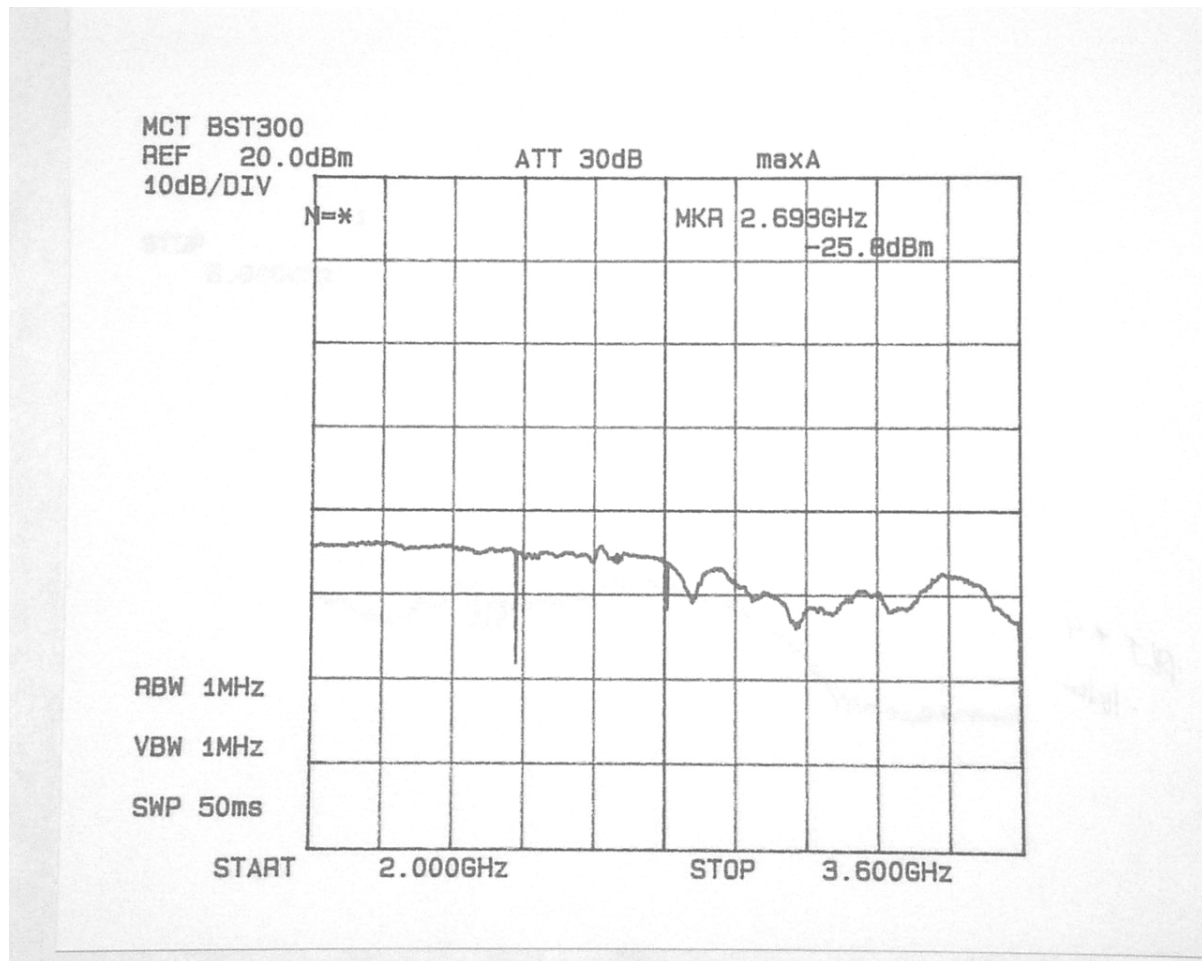
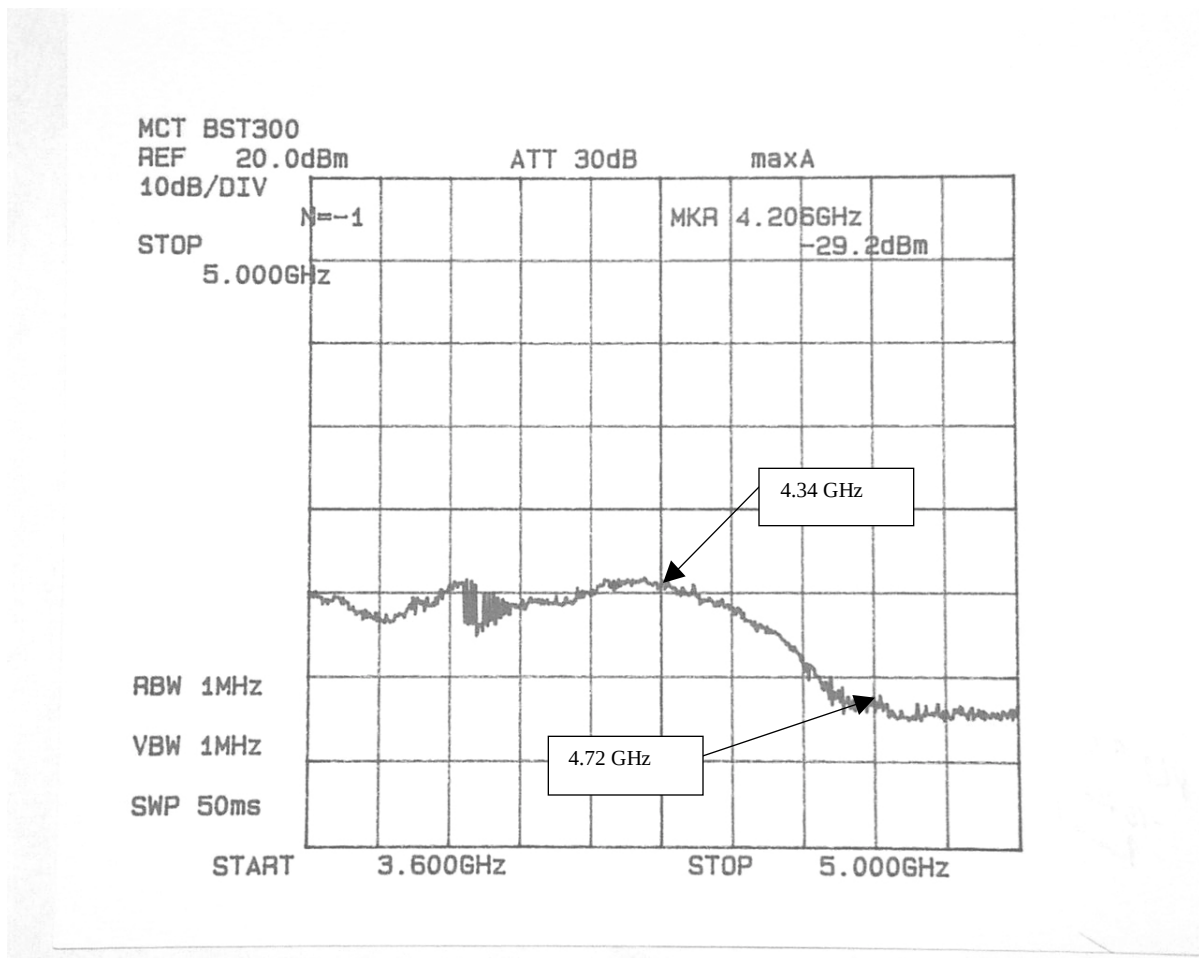
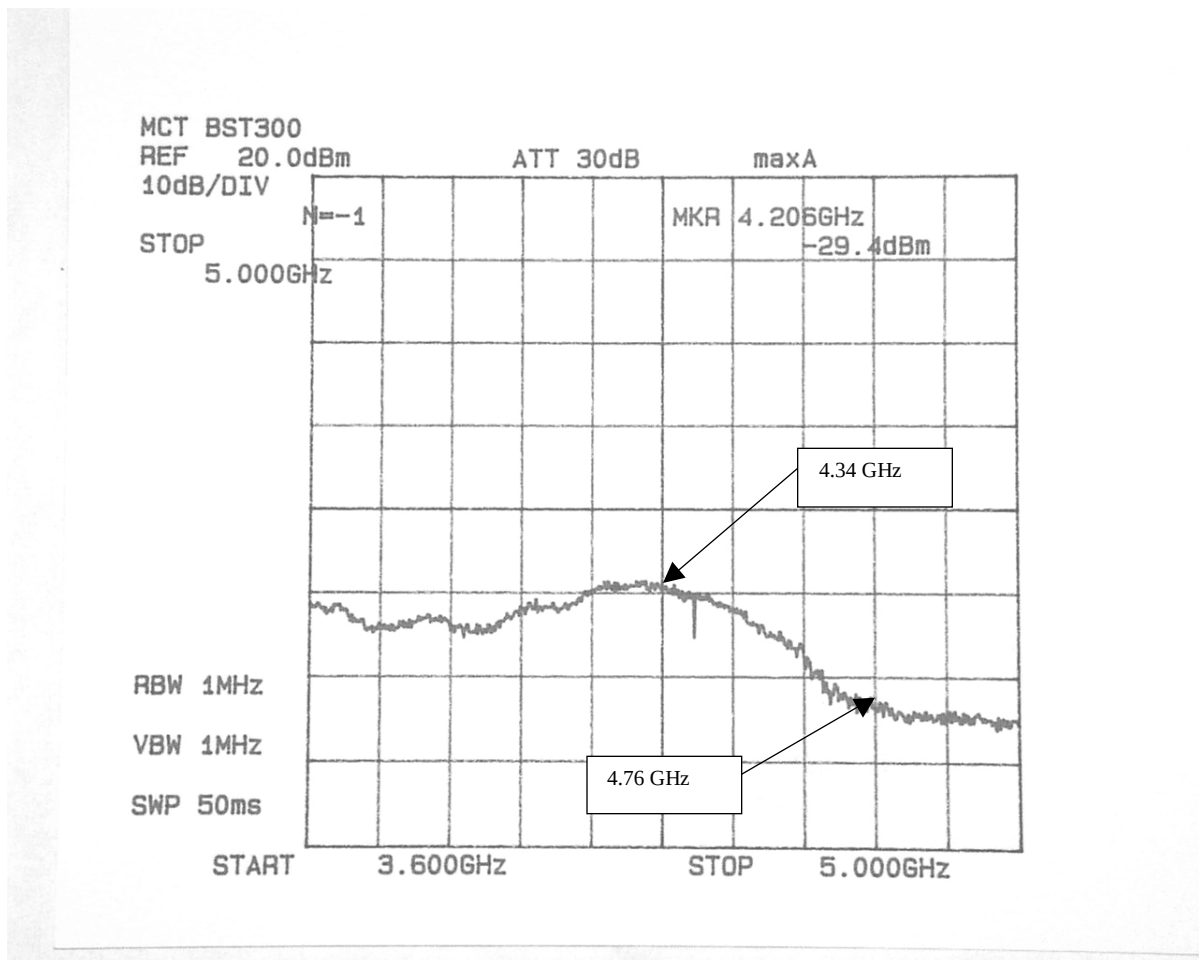




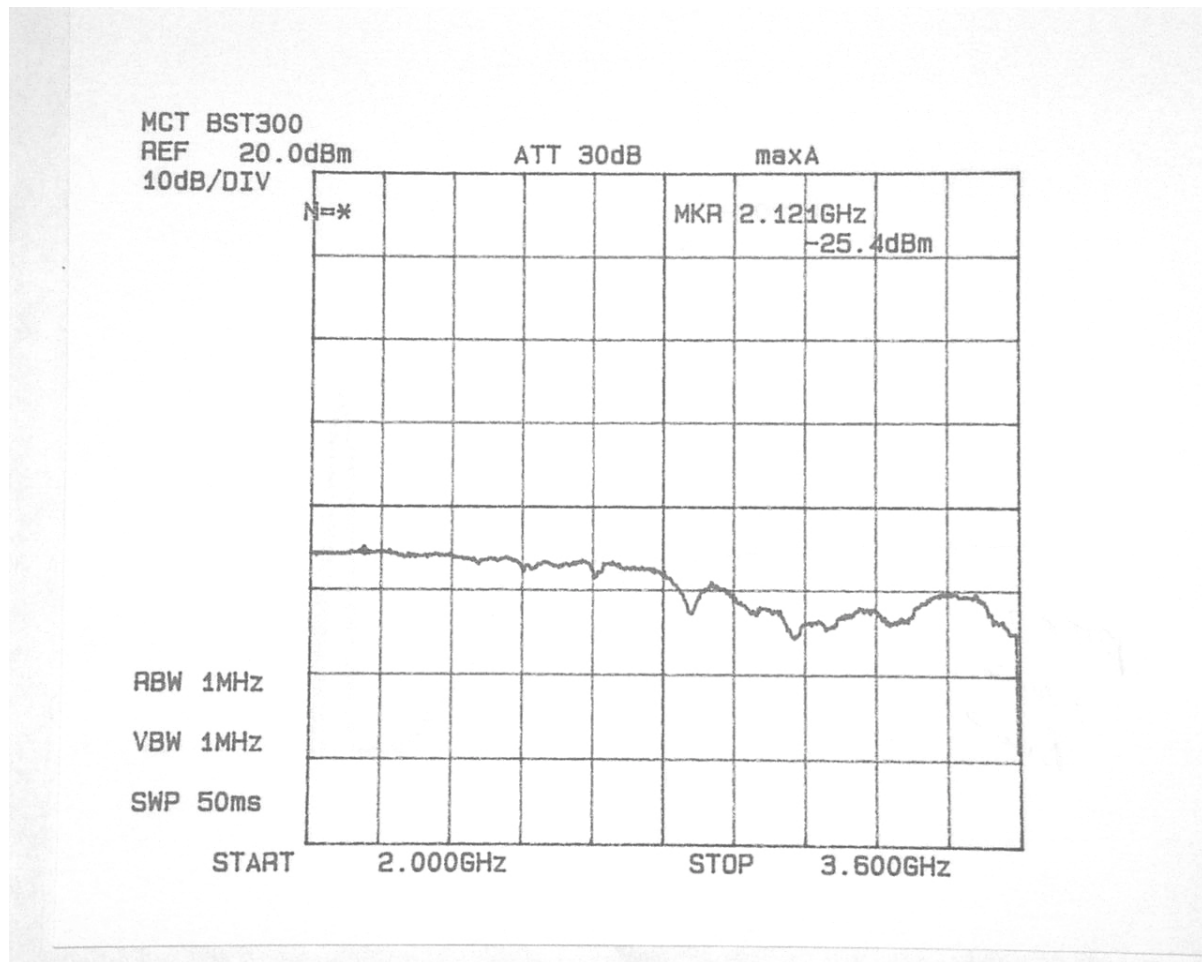
Plot 3
1.9 GHz Bypass Shape
Signal Input Up link
BST300
FCC PART 2.1043 PERMISSIVE CHANGE



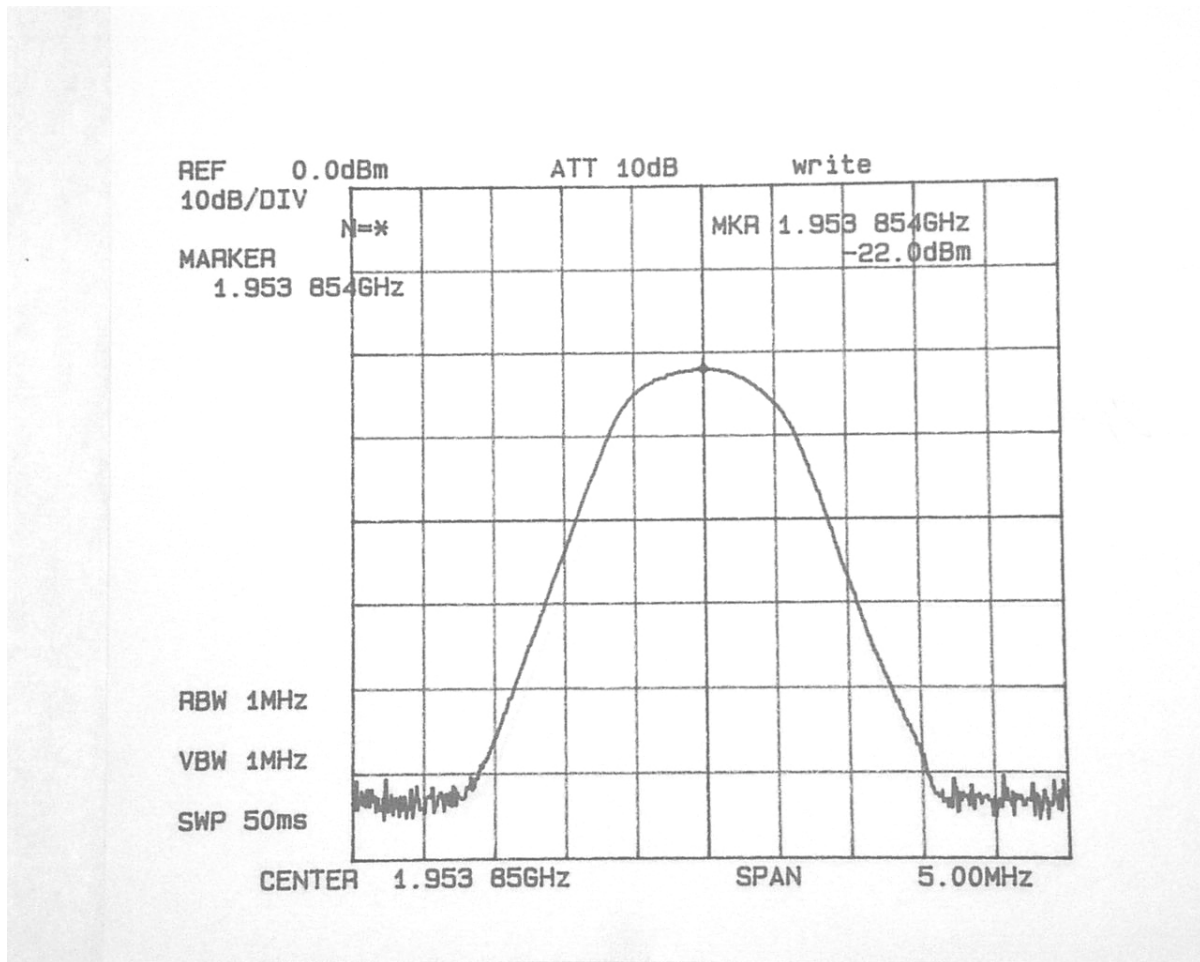
Plot 4
1.9 GHz Bypass Shape
Signal Input Up link
BST300
FCC PART 2.1043 PERMISSIVE CHANGE



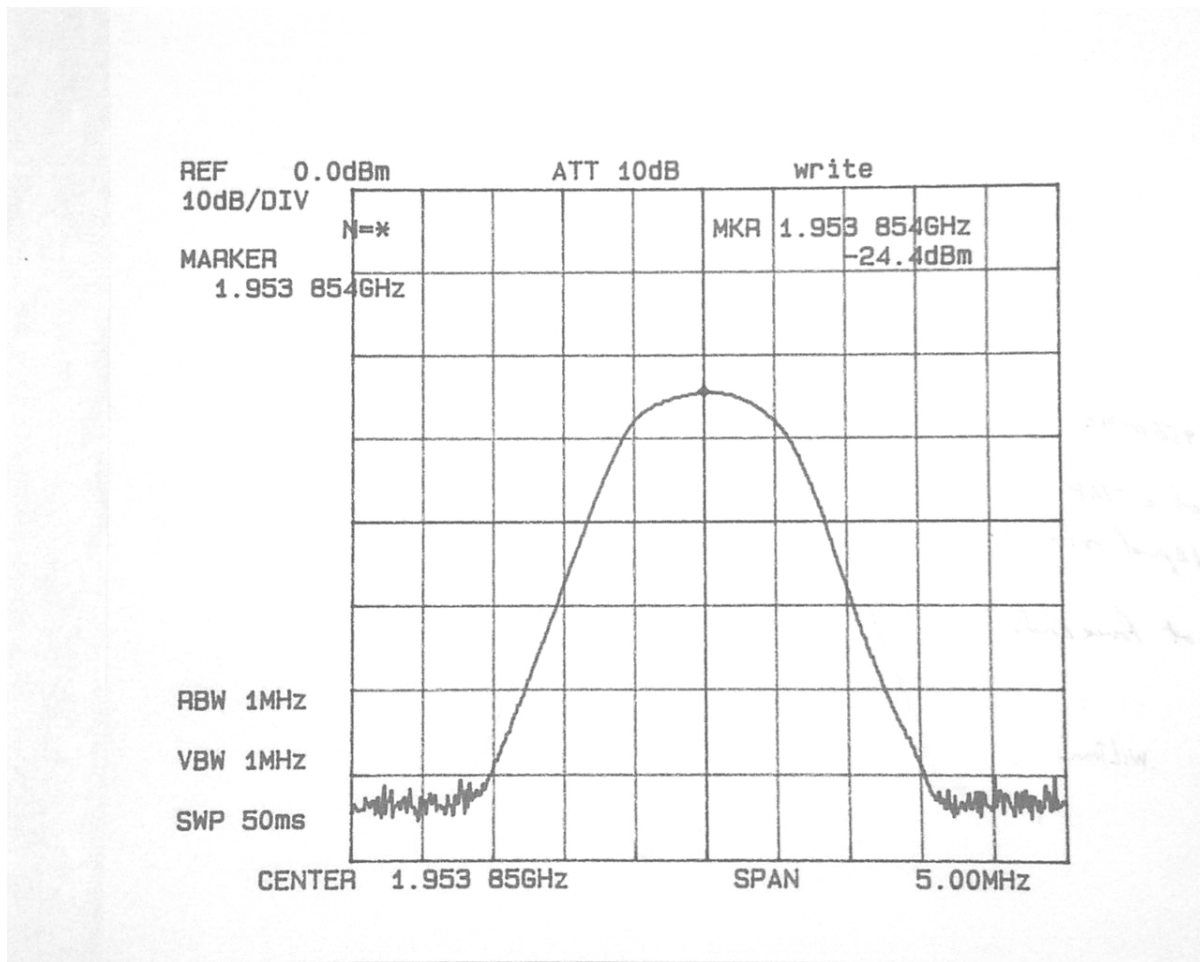
Plot 5
1.9 GHz Bypass Shape
Signal Input Down link
BST300
FCC PART 2.1043 PERMISSIVE CHANGE



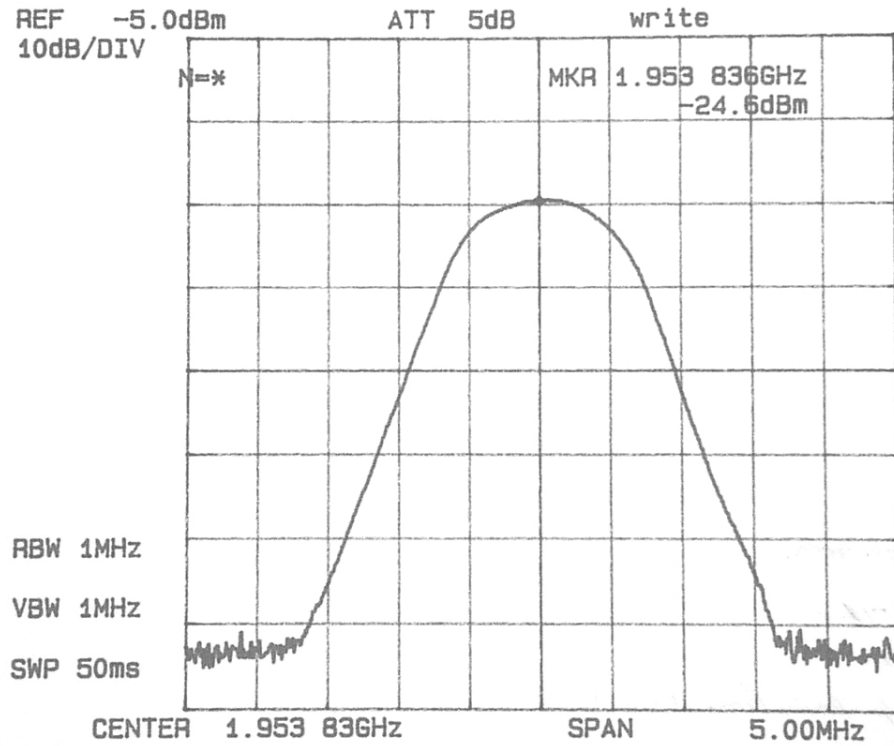
Plot 6
1.9 GHz Bypass Shape
Signal Input Down link
BST300
FCC PART 2.1043 PERMISSIVE CHANGE



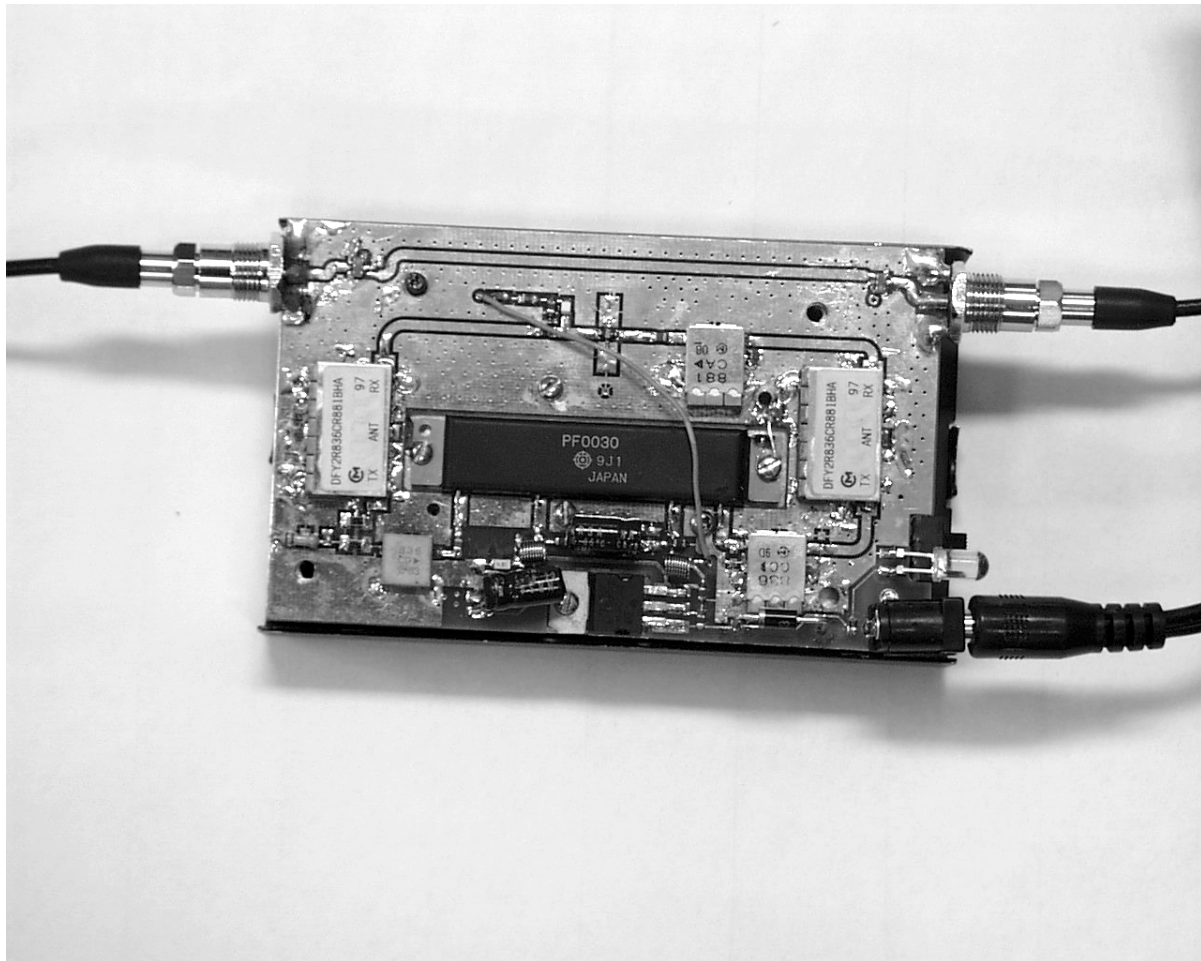
Plot 7
1.9 GHz Bypass
Test Signal Input Level
(Signal Generator direct to Spectrum Analyzer)
BST300
FCC PART 2.1043 PERMISSIVE CHANGE



Plot 8
1.9 GHz Bypass
Test Signal Through Uplink
BST300
FCC PART 2.1043 PERMISSIVE CHANGE



Plot 9
1.9 GHz Bypass
Test Signal Through Down link
BST300
FCC PART 2.1043 PERMISSIVE CHANGE



Photograph 1.

Sample with 1.9 GHz Passive Bypass Circuitry

BST300

FCC PART 2.1043 PERMISSIVE CHANGE