REVISIONS	SYM	DATE	POSITION	DESCRIPTION	REVISED	CHECKED	APPROVED
	1 X 33	'01.07.09		Rev.Up	H.Wakaizumi	Y.Ikeda	H.Kawaguchi
	2 X 4	'01.10.01		Modification by DSP modify	M.Sato	K.Sugimoto	N.Kimura
	3 X 4	'01.10.15		Part addition. Capacitor exchange	M.Sato	K.Sugimoto	N.Kimura
	4 X 0	'01.12.22		Rev.up	M.Sato	K.Sugimoto	N.Kimura
	5 X						

U700
Hillary

MAIN CLOCK 19.2MHz — DSPCLK1 — 76 DSPCLK 19.2MHz — R611 47 — C600 22p NL *1

TIMING CONTROL GEN — TG.SYNC — 4
RXDSP.INT — 70 DSP.INT — R602 220 — C601 22p

POWER CONTROL MODULE — RES.TXDSP — 75 DSPRST — R601 220 — C602 22p

RX SAMPLE INTERFACE
- RXSIF.DSPCLK — 73 — R601 220 — C603 22p
- RXSIF.DSPDATA — 69 — R602 220 — C604 22p
- RXSIF.DSPSYNC — 74 — R601 220 — C605 22p

Tx SAMPLE INTERFACE
- TXSIF.DSPSYNC — 80 — R600 220 — C606 22p
- TXSIF.DSPCLK — 79 — R600 220 — C607 22p
- TXSIF.DSPDATA — 77 — R600 220 — C608 22p
- R600 220

PCM/DSP HOST INTERFACE
- DSPC.TXSYNC — 71 — R602 220 — C609 22p
- PCM.DSPMIC — 68 — R602 220 — C610 22p
- PCM.DSPCLK — 72 — R601 220 — C611 22p
- PCM.DSPEAR — 64 — R603 220 — C612 22p
- DSPC.RXSYNC — 66 — R604 220 — C613 22p

VDDO.3V — 67 — +3V — C634 *1 NL — C614 47p
VSSO.3V — 78

J3 *1 NL AXK620345P
PCM.DSPEAR 3
DSPC.RXSYNC 2
+3.3V 1
PCM.DSPMIC 20
RXSIF.DSPDATA 19
DSP.INT 18
DSPC.TXSYNC 17
PCM.DSPCLK 16
RXSIF.DSPCLK 15
RXSIF.DSPSYNC 14
DSPRST 13
DSPCLK 12
TXSIF.DSPDATA 11
VSSO.3V 4
VSSO.3V 9
TXSIF.DSPCLK 10
TXSIF.DSPSYNC 5
VSSO.3V 6
VSSO.3V 7
VSSO.3V 8
VSSO.3V

TP601

U600
DSP
TMS320VC5416

TOUTO	82		
HINT	51		
CLKOUT	94		
X1	96		
X2/CLKIN	97		
CLKMD1	77		
CLKMD2	78		
CLKMD3	79		
IAQ	29		
MSC	26		
HOLDA	28		
HOLD	30		
READY	19		
IACK	61		
INTO	64		
INT1	65		
INT2	66		
INT3	67		
NMI	63		
BIO	31		
XF	27		
RS	98		
BCLKR2	42		
BDR2	47		
BFSR2	44		
BDX2	60		
BCLKX2	49		
BFSX2	54		
BCLKR1	38	R625 10k TP602	Added pin
BDR1	35	R626 10k TP603	
BFSR1	36	R627 10k TP604	
BCLKX1	71	R628 10k TP605	
BDX1	74	R629 10k TP606	
BFSX1	73	TP607	
BCLKR0	41		
BDR0	45		
BFSR0	43		
BCLKX0	48		
BDX0	59		
BFSX0	53		
HPI16	80	R630 10k	Added pin
HCNTL0	39		
HCNTL1	46		
HRDY	55		
HBIL	62		
HPIENA	92	R613 10k	
HDS1	127		
HDS2	128		
HAS	13		
HCS	17		
HR/W	18		
HD0	58	R617 47kNL*1	
HD1	69	R618 47kNL*1	
HD2	81	R619 47kNL*1	
HD3	95	R620 47kNL*1	
HD4	120	R621 47kNL*1	
HD5	124	R622 47kNL*1	
HD6	135	R623 47kNL*1	
HD7	6	R624 47kNL*1	

+3.3V — R614 10k — R615 10k — R616 10k — TP608
+3.3V — R612 10k
+3.3V — R617 47kNL*1 — R618 47kNL*1 — R619 47kNL*1 — R620 47kNL*1 — R621 47kNL*1 — R622 47kNL*1 — R623 47kNL*1 — R624 47kNL*1

DSP — SCAN

TDI	86	TP620	PD
TMS	89	TP621	VSS
TRST	87	TP622	TDI
TCK	88	TP623	TMS
TDO	85	TP624	TRST
EMU0	83	TP625	TCK
OFF/EMU1	84	TP626	TDO
		TP627	EMU0
		TP628	EMU1

JTAG I/F

+3.3V — R609 10k — R610 10k
+5VD

D0	99	R605 4	5 47k
D1	100		6 47k
D2	101		7 47k
D3	102		8 47k
D4	103	R606 4	5 47k
D5	104		6 47k
D6	113		7 47k
D7	114		8 47k
D8	115	R607 4	5 47k
D9	116		6 47k
D10	117		7 47k
D11	118		8 47k
D12	119	R608 4	5 47k
D13	121		6 47k
D14	122		7 47k
D15	123		8 47k

+3.3V

PS	20		
DS	21		
IS	22		
ISTRB	25		
MSTRB	24		
R/W	23		
A0	131		
A1	132		
A2	133		
A3	134		
A4	136		
A5	137		
A6	138		
A7	139		
A8	140		
A9	141		
A10	5		
A11	7		
A12	8		
A13	9		
A14	10		
A15	11		
A16	105		
A17	107		
A18	108		
A19	109		
A20	110		
A21	143		Added pin
A22	2		
MP/MC	32		

+1.5V
CVDD 12.16.52.68.91.125.142 — C622 0.1u — C623 470p — C624 0.1u — C625 47p — C626 0.1u — C627 47p — C628 4.7u
+3.3V
CVSS 1.3.15.34.37.50.70.90.111.126
DVDD 4.33.56.75.112.130 — C615 0.1u — C616 47p — C617 0.1u — C618 47p — C619 0.1u — C620 47p — C621 4.7u
DVSS 14.40.57.72.76.93.106.128.144

* _ added pin CVDD 12
CVSS 1.15.37.90.126
DVSS 72.144

*1 J3. R617 - R624. C600. C634 Not Used

DRAWN H.Wakaizumi	DATE '01.05.30	CHECKED O.Baba	DATE '01.05.30	DATE '01.05.30		UNIT mm	MODEL H/H	EQPT.NAME J700P(C5416) 800MHz
DSGN OR ENGR Y.Ikeda	DATE '01.05.30	APROVED H.Kawaguchi	DATE '01.05.30	STORED	SCALE		TITLE MAIN LOGIC SCHEMATIC	3/6
KOKUSAIDENKI ENGINEERING CO., LTD					PARTS CODE		DWG. No. G3UD06903	Rev. 4