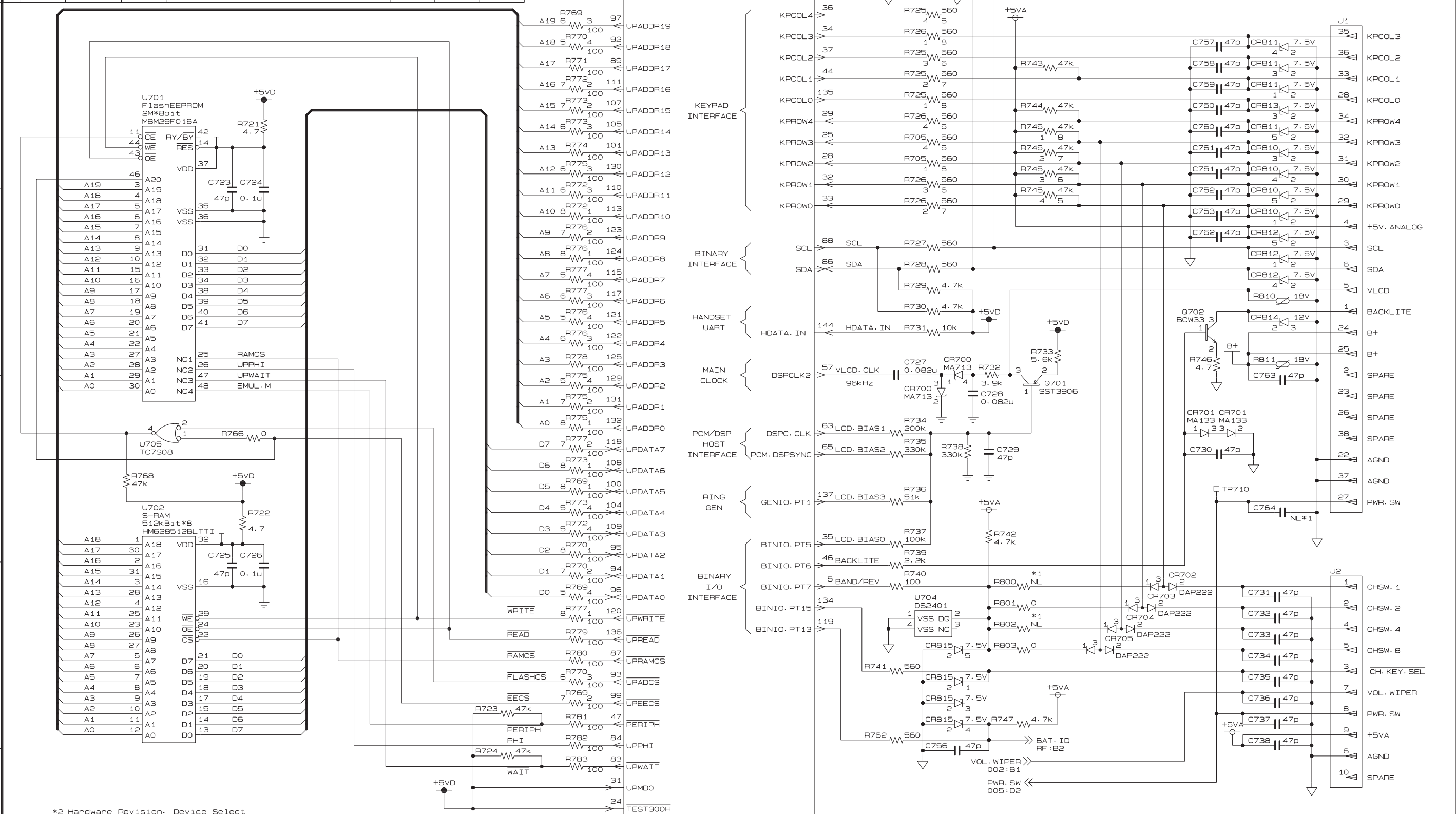


| H       |     |          | G        |                 |              | F           |              |  |
|---------|-----|----------|----------|-----------------|--------------|-------------|--------------|--|
| REVISED | SYM | DATE     | POSITION | DESCRIPTION     | REVISED      | CHECKED     | APPROVED     |  |
|         | 1X3 | 01.07.09 |          | Rev. Up         | H. Wakaizumi | Y. Ikeda    | H. Kawaguchi |  |
|         | 2X7 | 01.10.01 |          | Constant change | M. Sato      | K. Sugimoto | N. Kimura    |  |
|         | 3X0 | 01.10.15 |          | Rev. up         | M. Sato      | K. Sugimoto | N. Kimura    |  |
|         | 4X0 | 01.12.22 |          | Rev. up         | M. Sato      | K. Sugimoto | N. Kimura    |  |
|         | 5X  |          |          |                 |              |             |              |  |
|         |     |          |          |                 |              |             |              |  |



| *2 Hardware Revision |       | Device | Select |        |      |        |         |
|----------------------|-------|--------|--------|--------|------|--------|---------|
|                      | R803  | R802   |        | Device | Open | Short  |         |
| RA0                  | Short | Short  |        | R800   | DSP  | VC5416 | LC56    |
| RA1                  | Short | Open   |        | R801   | PLL  | SA7025 | CX72301 |
| RA2                  | Open  | Short  |        |        |      |        |         |
| RA3                  | Open  | Open   |        |        |      |        |         |

|                                   |                  |                          |                  |                  |            |                                                                                       |                               |                                   |           |
|-----------------------------------|------------------|--------------------------|------------------|------------------|------------|---------------------------------------------------------------------------------------|-------------------------------|-----------------------------------|-----------|
| DRAWN<br>H. Wakaizumi             | DATE<br>01.05.30 | CHECKED<br>O. Baba       | DATE<br>01.05.30 | DATE<br>01.05.30 |            | UNIT<br>mm                                                                            | MODEL<br>H/H                  | EQPT. NAME<br>J700P(C5416) 800MHZ |           |
| DSGN OR ENGR<br>Y. Ikeda          | DATE<br>01.05.30 | APPROVED<br>H. Kawaguchi | DATE<br>01.05.30 | STORED           | SCALE      |  | TITLE<br>MAIN LOGIC SCHEMATIC |                                   | 4/6       |
| KOKUSAIDENKI ENGINEERING CO., LTD |                  |                          |                  |                  | PARTS CODE | DWG. No.<br>G3UD06903                                                                 |                               |                                   | Rev.<br>4 |