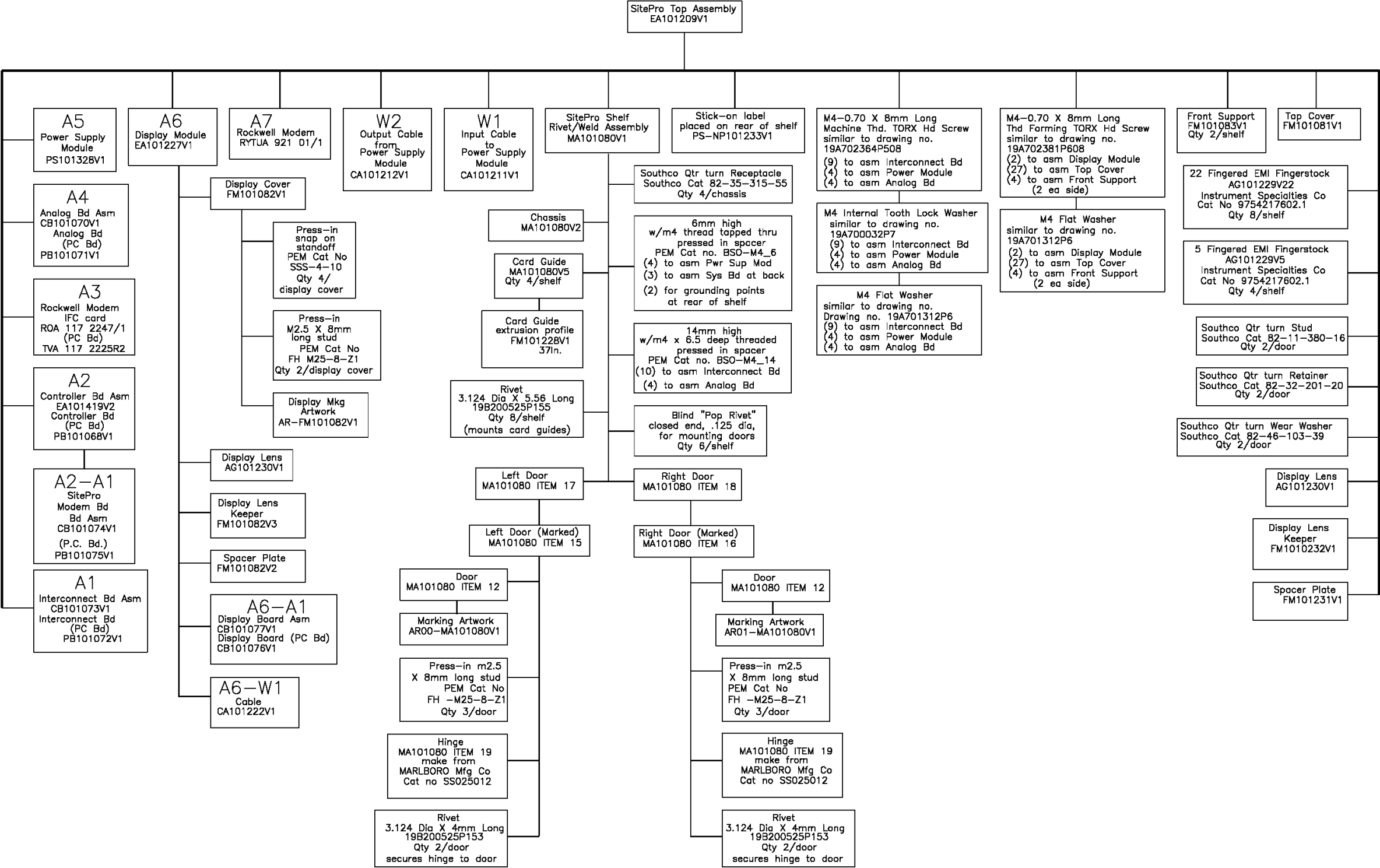
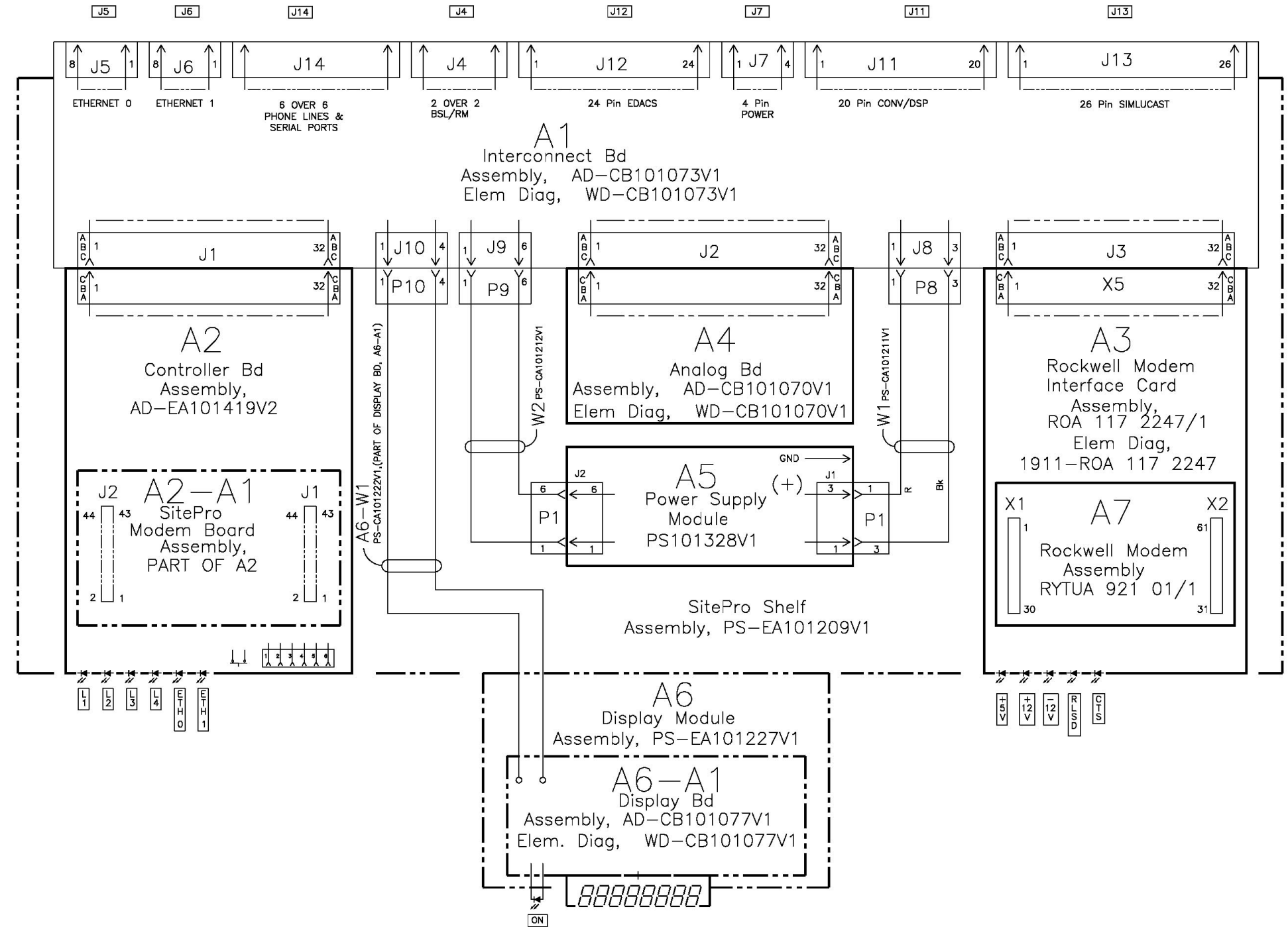


13.0 PRODUCT STRUCTURE



(PX01-EA101209V1, Rev. D)

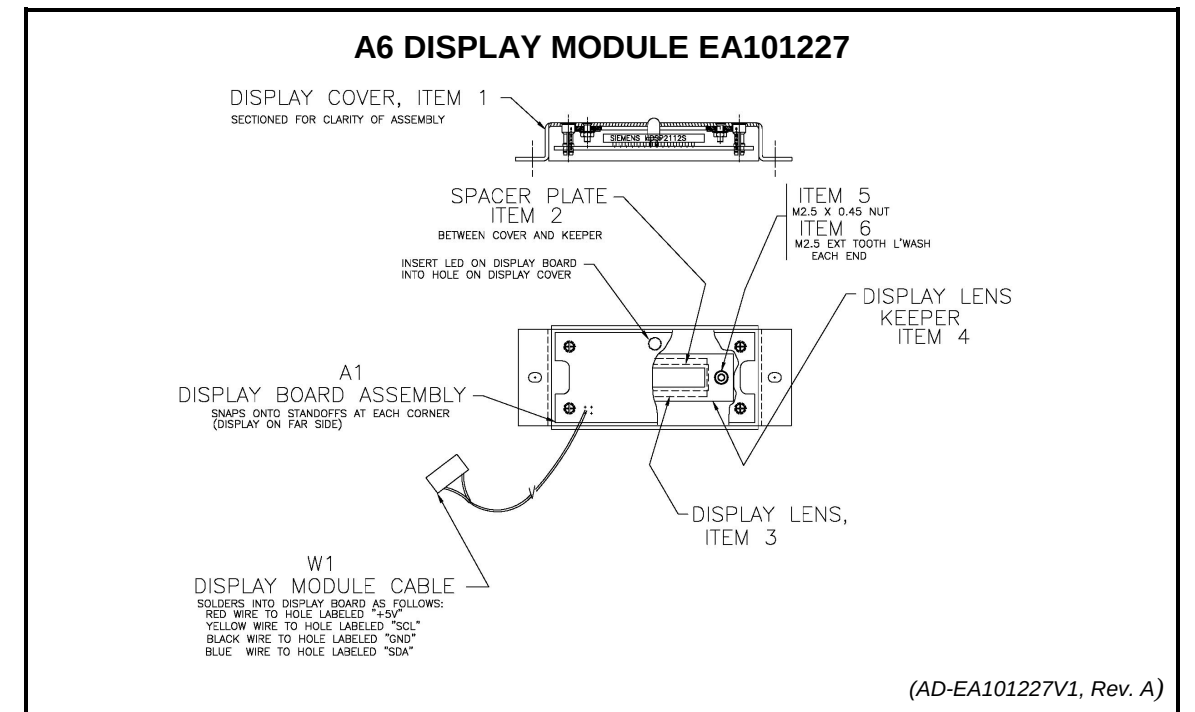
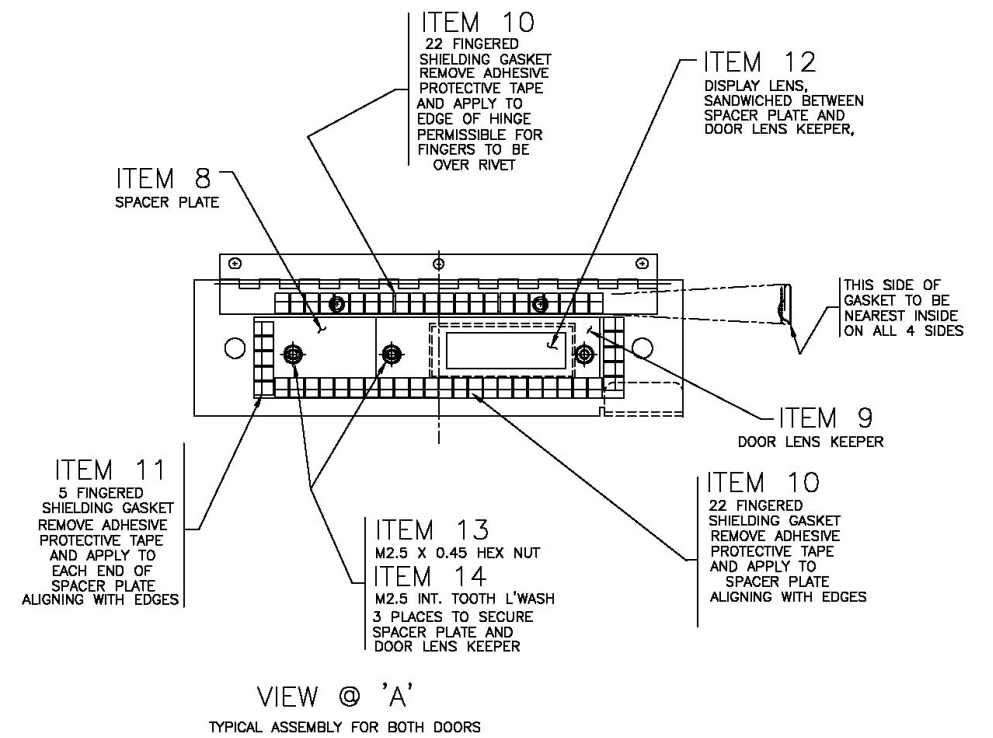
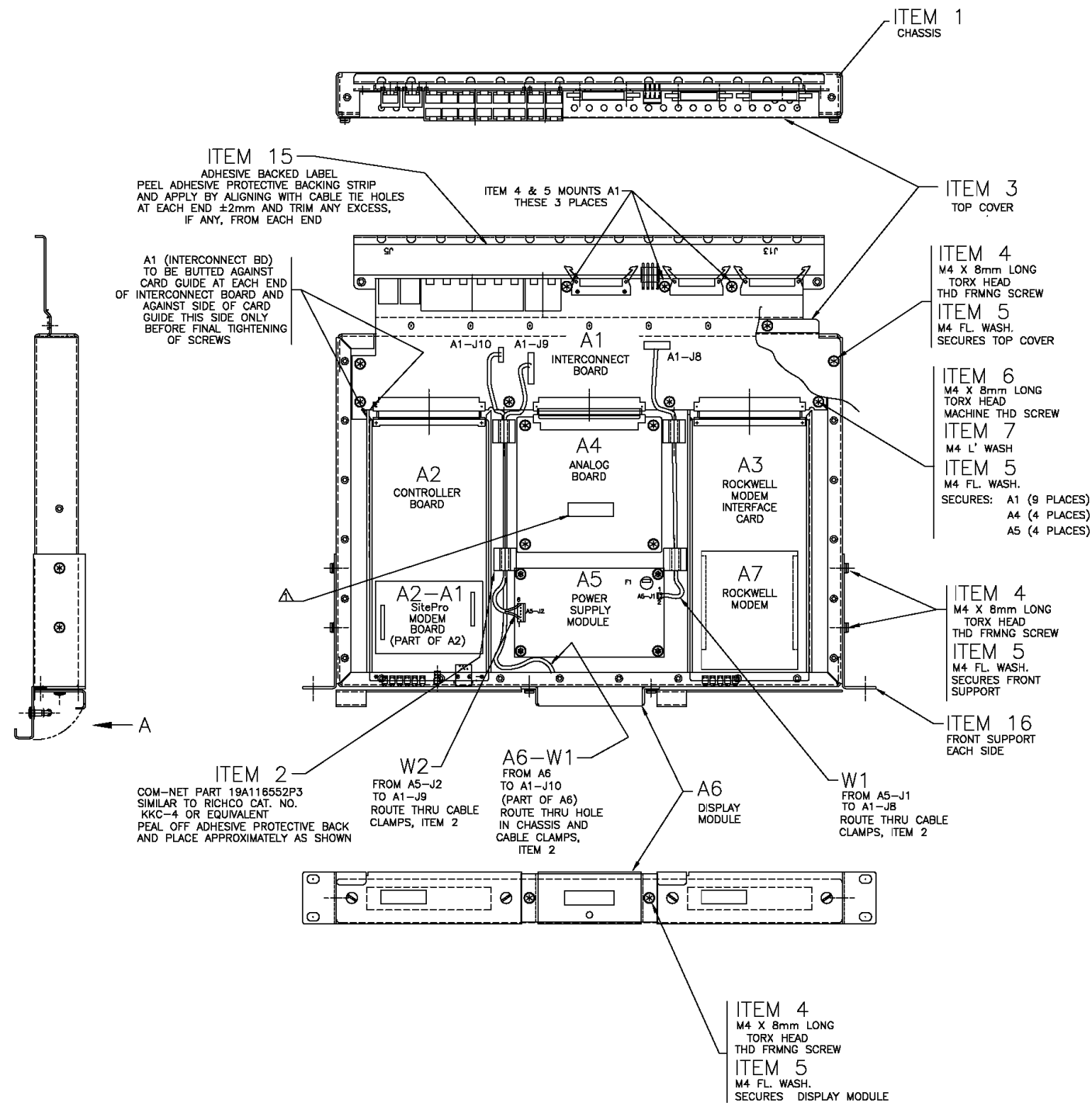
14.0 INTERCONNECTION DIAGRAM



SITEPRO SHELF EA101209V1

(ID-EA101209, Rev. A)

15.0 ASSEMBLY DIAGRAM



(AD-EA101227V1, Rev. A)

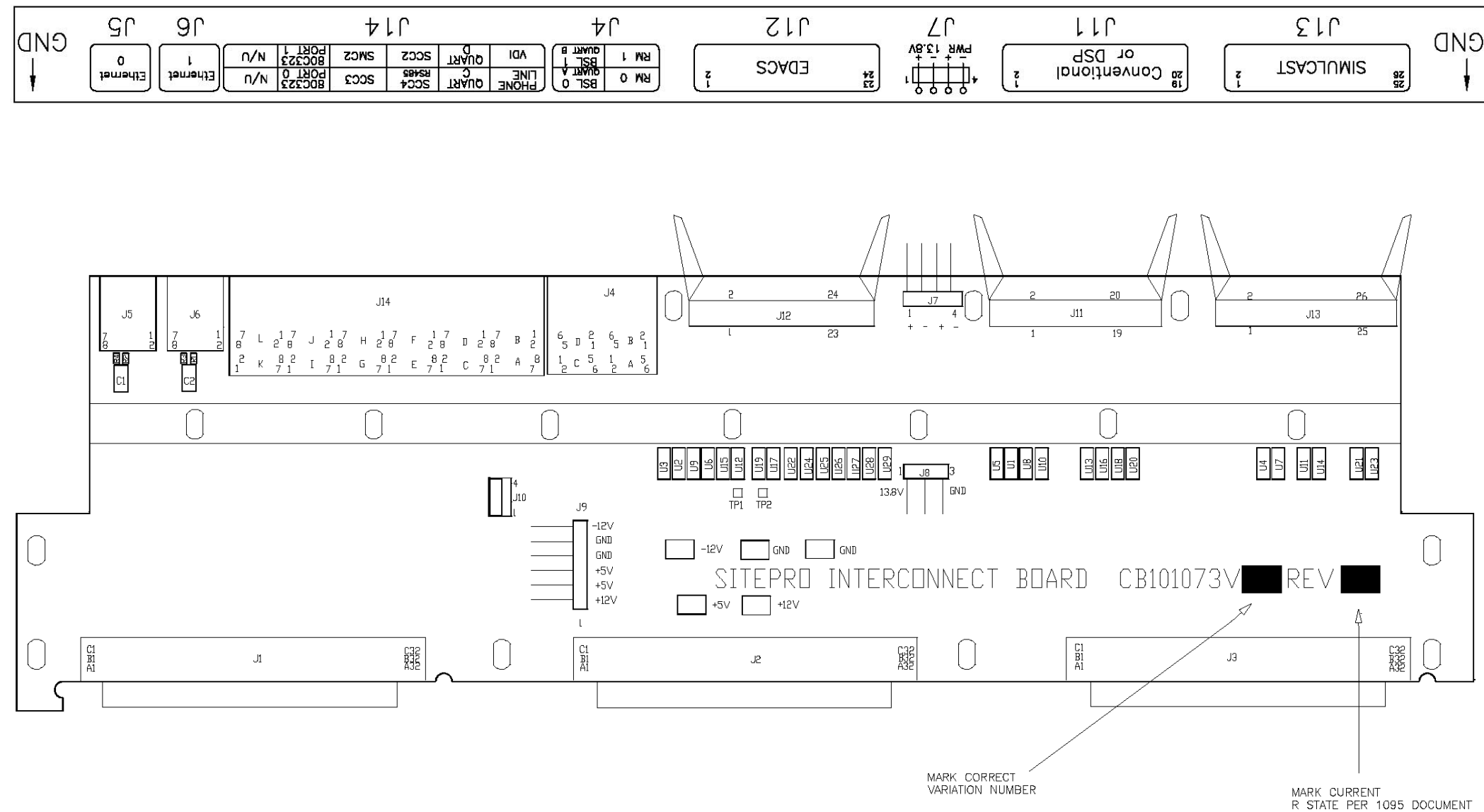
SITEPRO SHELF ASSEMBLY EA101209V1

(AD-EA101209V1, Rev. D)

16.0 OUTLINE DIAGRAMS

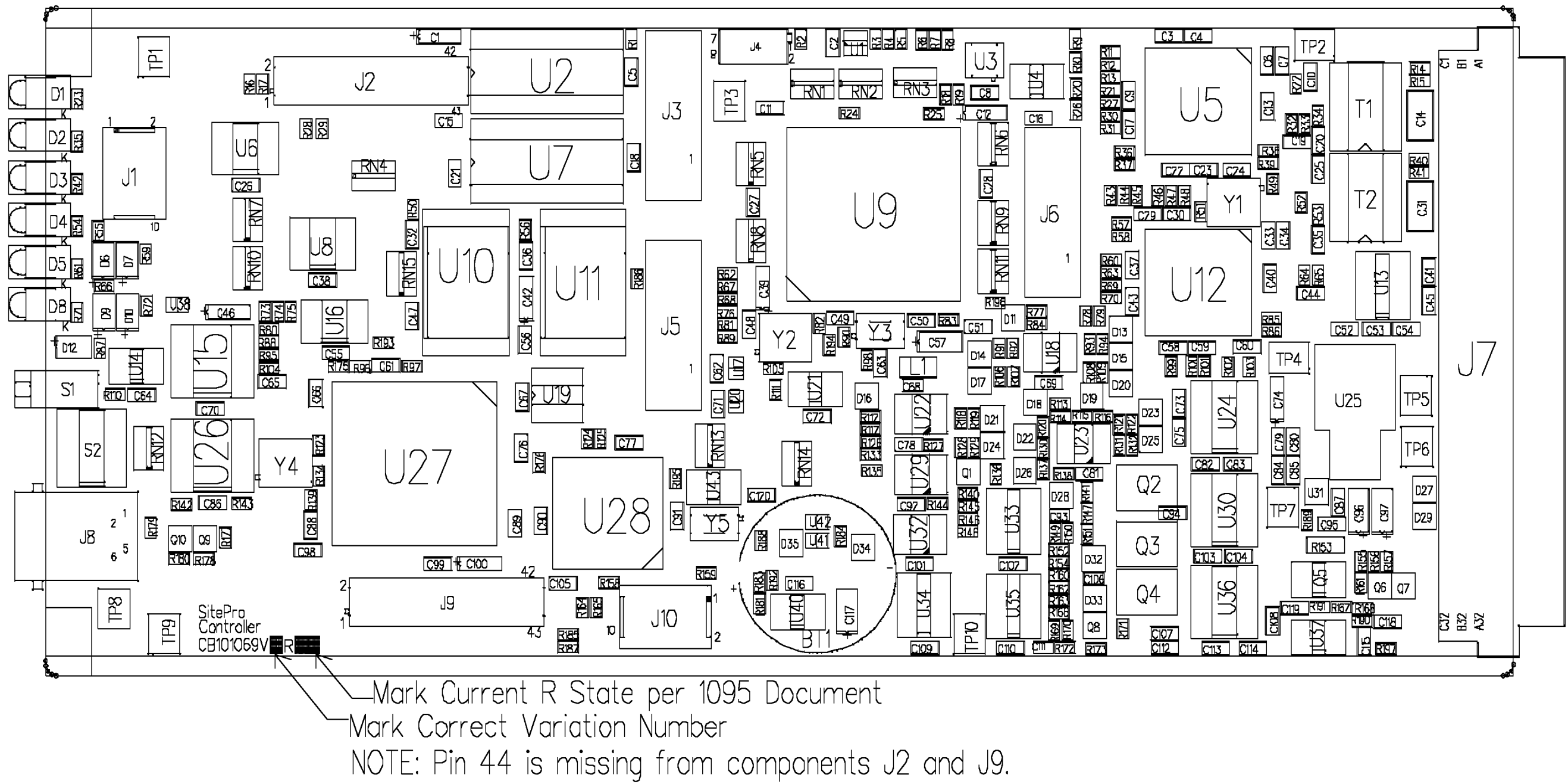
16.1 INTERCONNECT BOARD (A1) CB101073V1

REAR LABEL NP101233V1



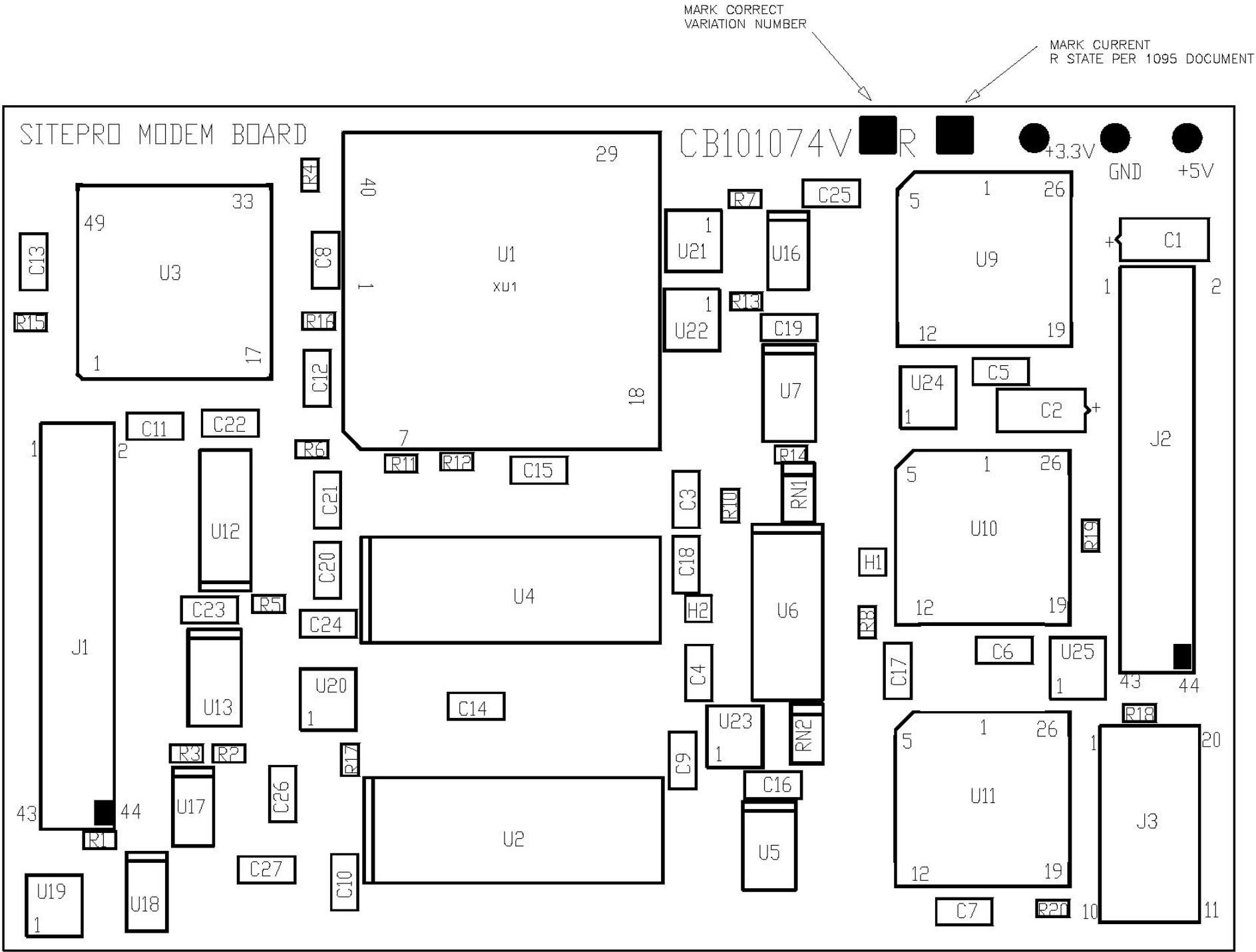
(AD-CB101073V1, Rev. A)

16.2 CONTROLLER BOARD (A2) CB101069V1



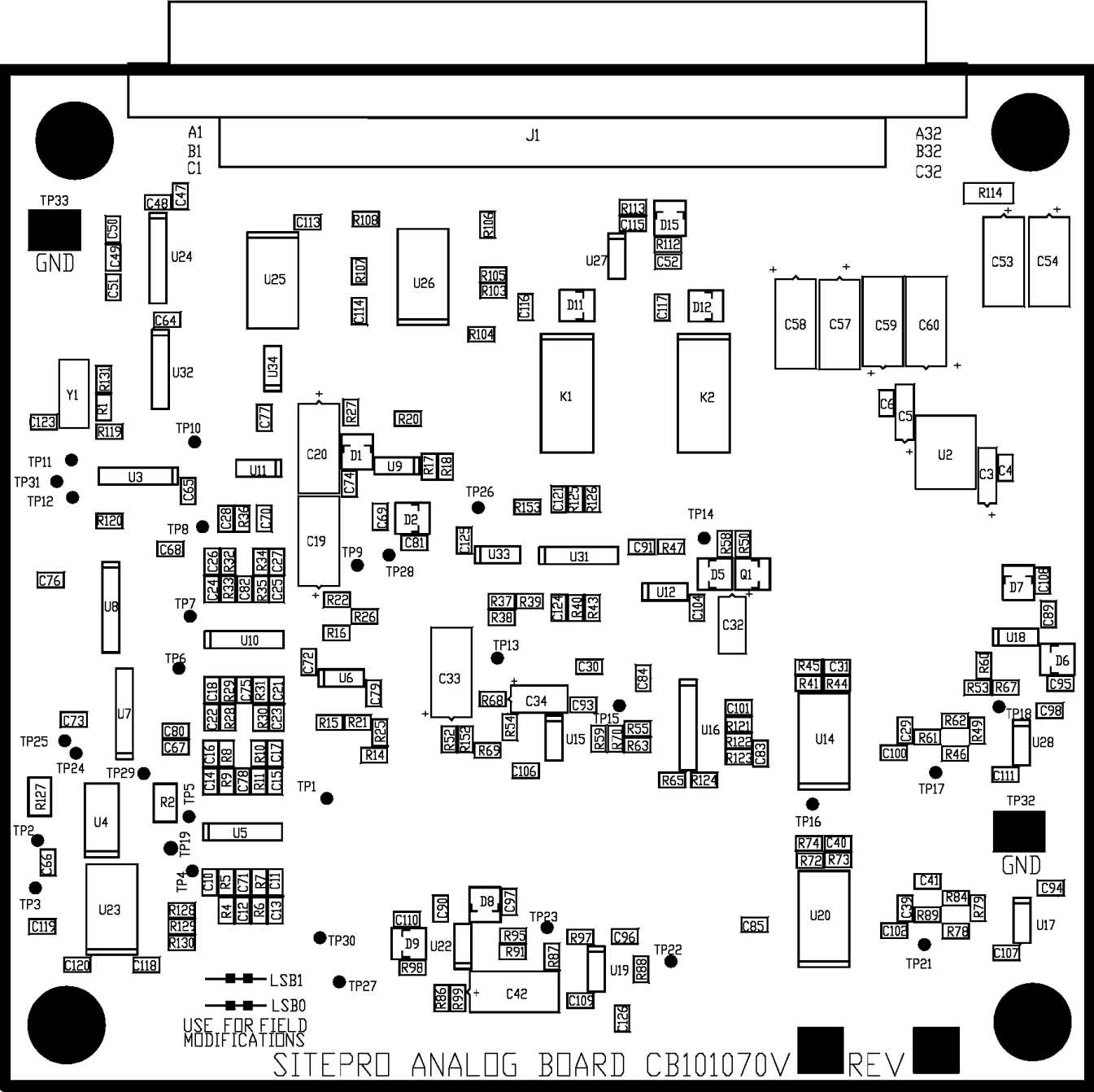
(AD-CB101069V1, Rev. A)

16.3 SitePro MODEM BOARD (A2-A1) CB101074V1



(AD-CB101074V1, Rev. A)

16.4 ANALOG BOARD (A4) CB101070V1

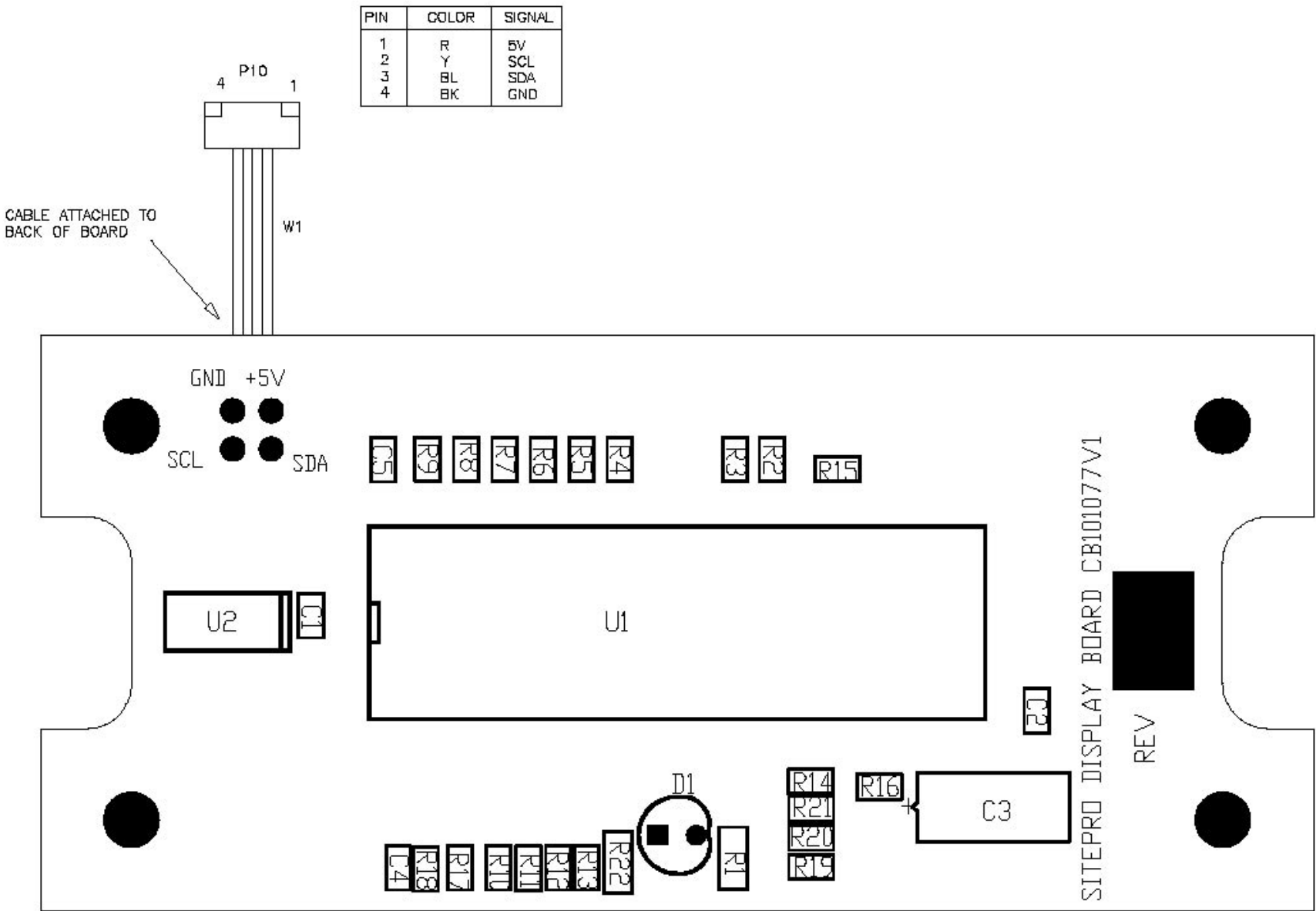


MARK CORRECT
VARIATION NUMBER

MARK CURRENT
R STATE PER
1095 DOCUMENT

(AD-CB101070V1, Rev. A)

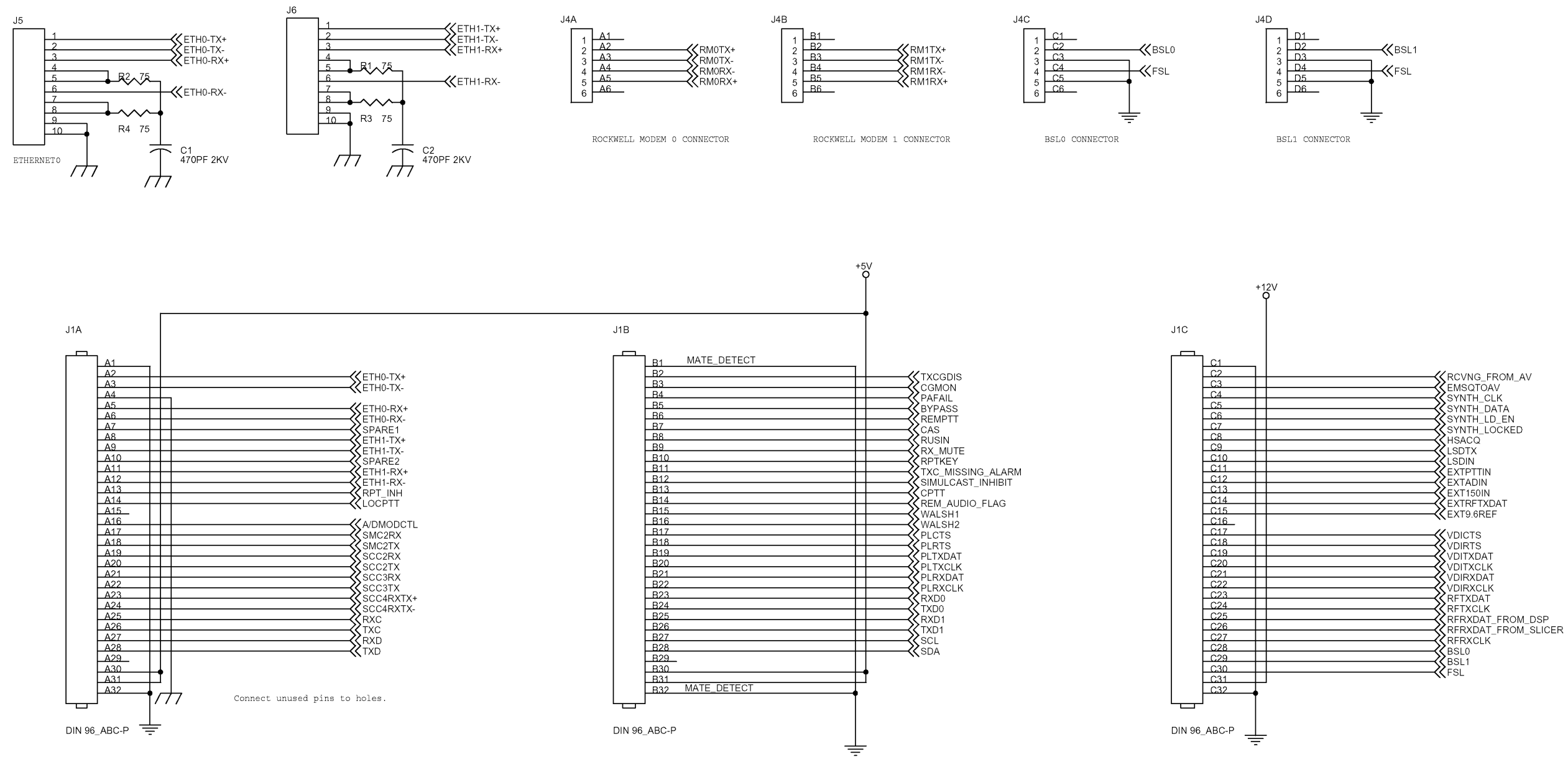
16.5 DISPLAY MODULE (A6-A1) CB101077V1



(AD-CB101077V1, Rev. A)

17.0 SCHEMATIC DIAGRAMS

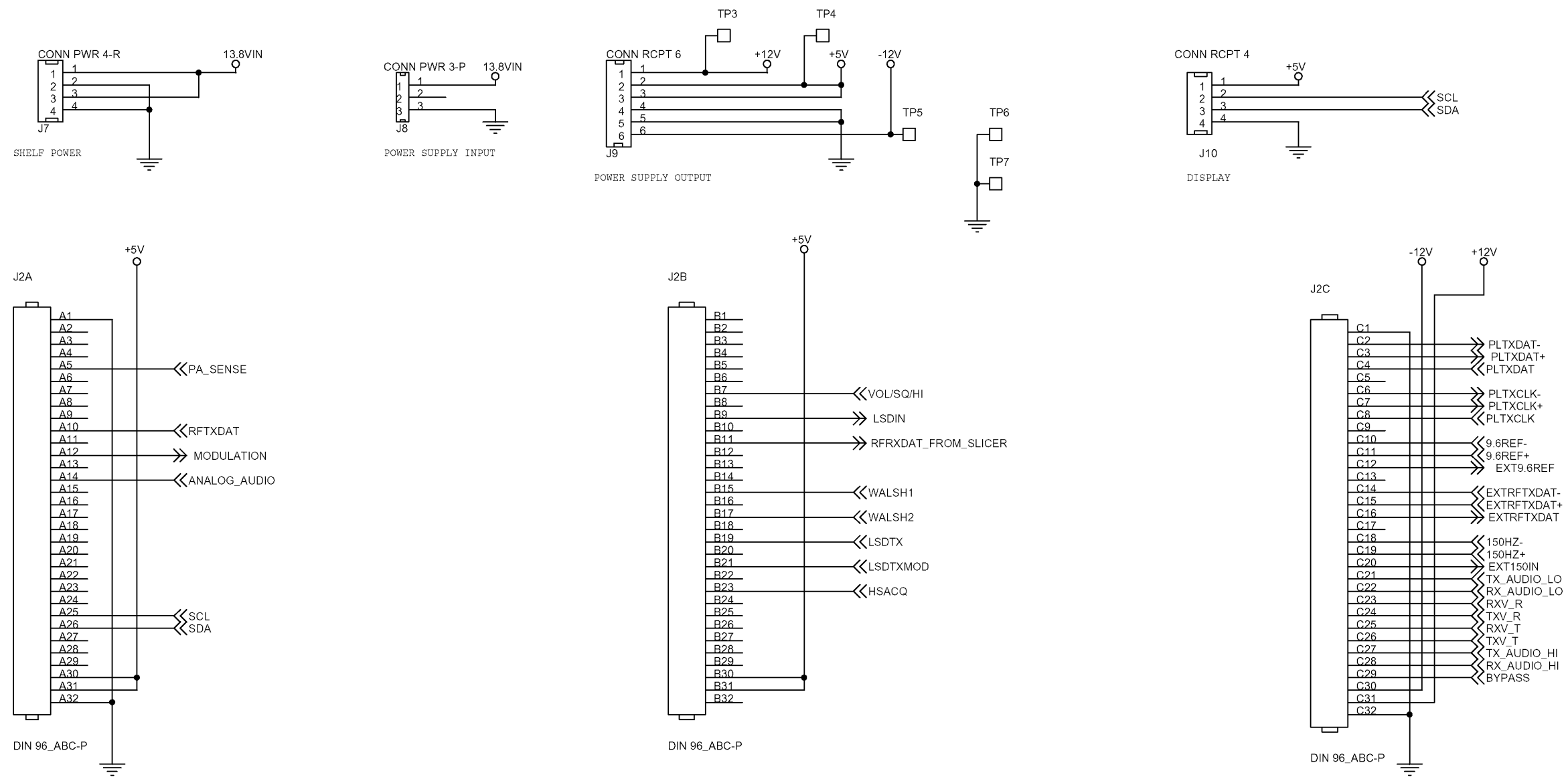
17.1 INTERCONNECT BOARD (A1) CB101073V1



CONTROLLER BOARD
CONNECTOR

(WD-CB101073V1, Sh. 1, Rev. A)

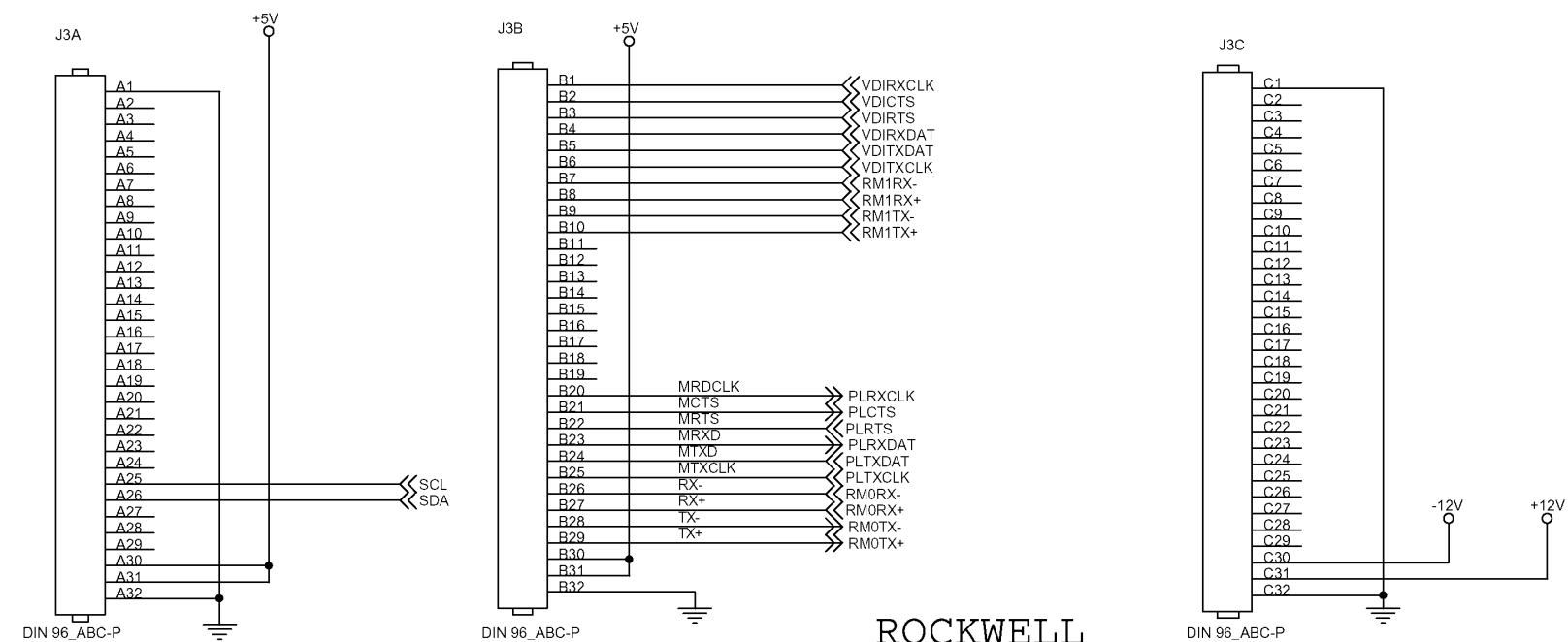
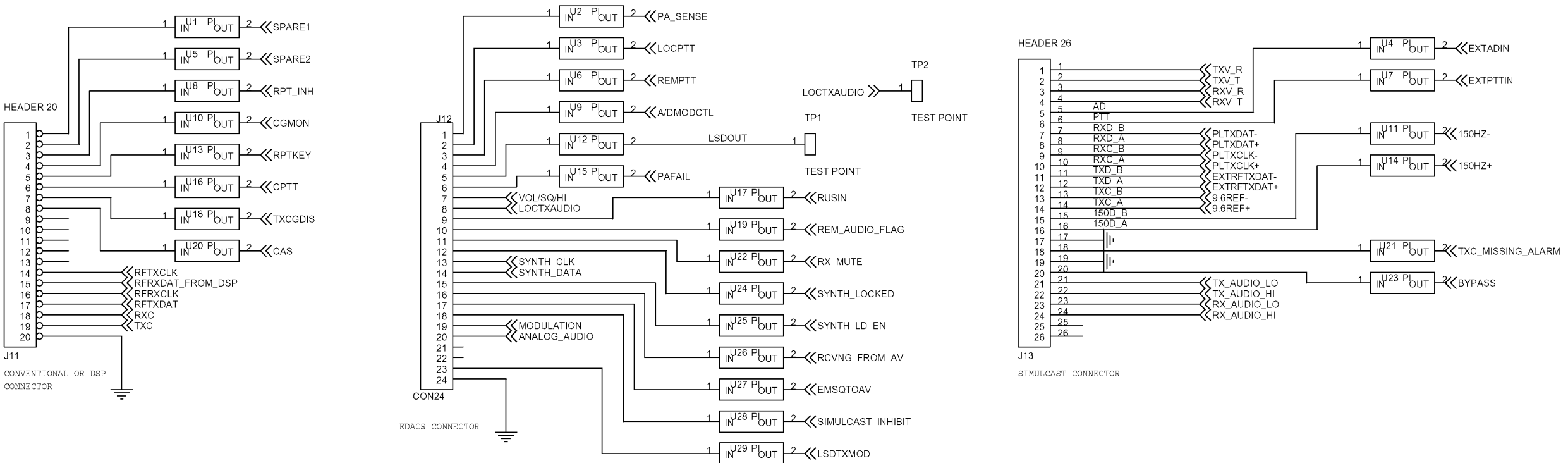
SCHEMATIC DIAGRAM



ANALOG
BOARD
CONNECTOR

INTERCONNECT BOARD (A1) CB101073V1

(WD-CB101073V1, Sh. 2, Rev. A)

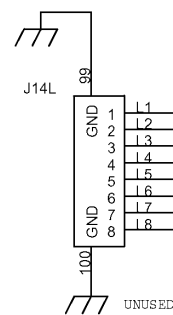
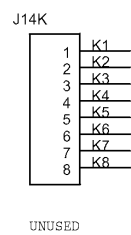
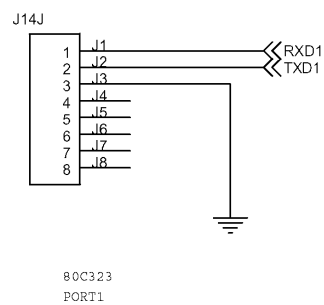
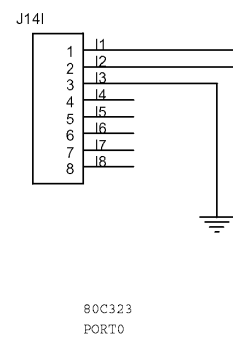
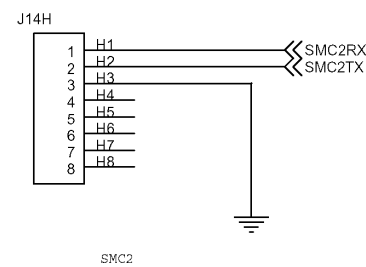
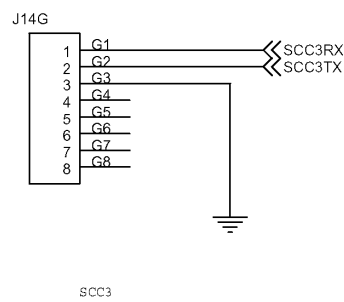
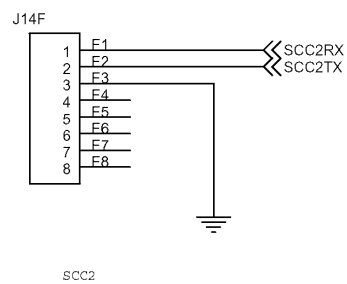
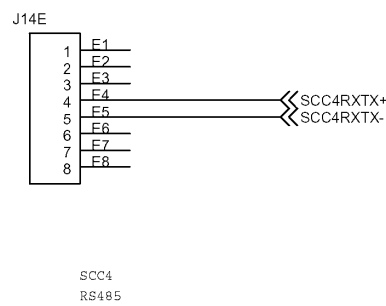
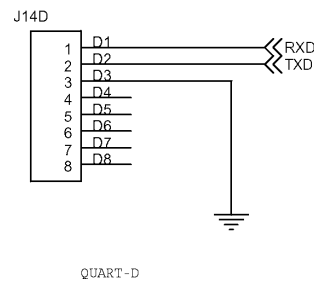
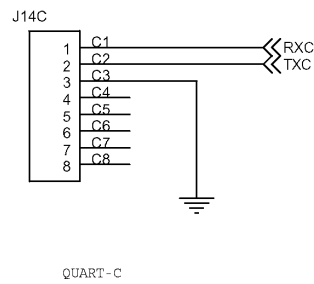
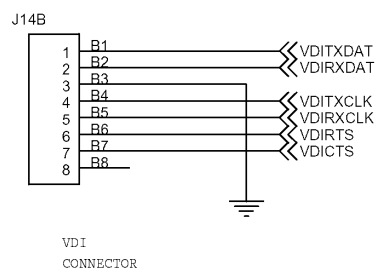
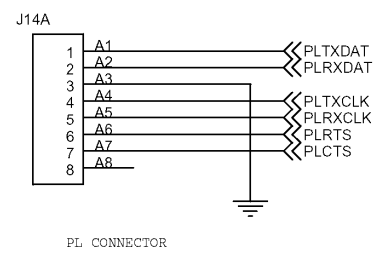


ROCKWELL
MODEM
CONNECTOR

INTERCONNECT BOARD (A1) CB101073V1

(WD-CB101073V1, Sh. 3, Rev. A)

SCHEMATIC DIAGRAM



SERIAL PORTS

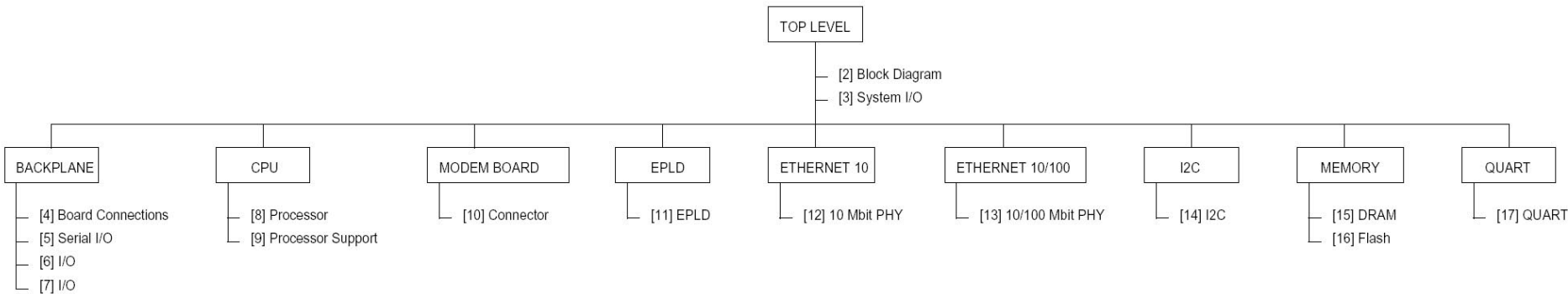
INTERCONNECT BOARD (A1) CB101073V1

(WD-CB101073V1, Sh. 4, Rev. A)

17.2 CONTROLLER BOARD (A2) CB101069V1

17.2.1 SitePro Board Title Page

SITEPRO BOARD TITLE PAGE



Notes:

- 1. The first index on all buses is the MSB (i.e., D0 is the MSB of D[0:31], and CS7 is the MSB of CS[7:0]).
- 2. Unless otherwise noted, all resistors are 1/16 Watt 0603 SMT type with a value tolerance of 5%.
- 3. Unless otherwise noted, all capacitors are 0805 SMT type.
- 4. See revlog.txt for revision information.

LAST USED REFERENCES

BT1
C120
D35
FD10
J10
L1
MP32
Q10
R197
RN15
S2
T2
TP10
U43
Y5

Chip Selects		
=====		
Device	Chip Select	

FLASH	CS0	
SDRAM	CS1	(UPMA)
QUART (Regs)	CS2	(SLV BUS)
QUART (Int Vect)	CS3	(SLV BUS)
CPLD	CS4	(SLV BUS)
MODEM DB Dual Port Ram	CS5	(UPMB) (SLV BUS)
MODEM DB Code Ram	CS6	(SLV BUS)
(SPARE - EPLD)	CS7	

IRQs		
=====		
Device	Chip Select	

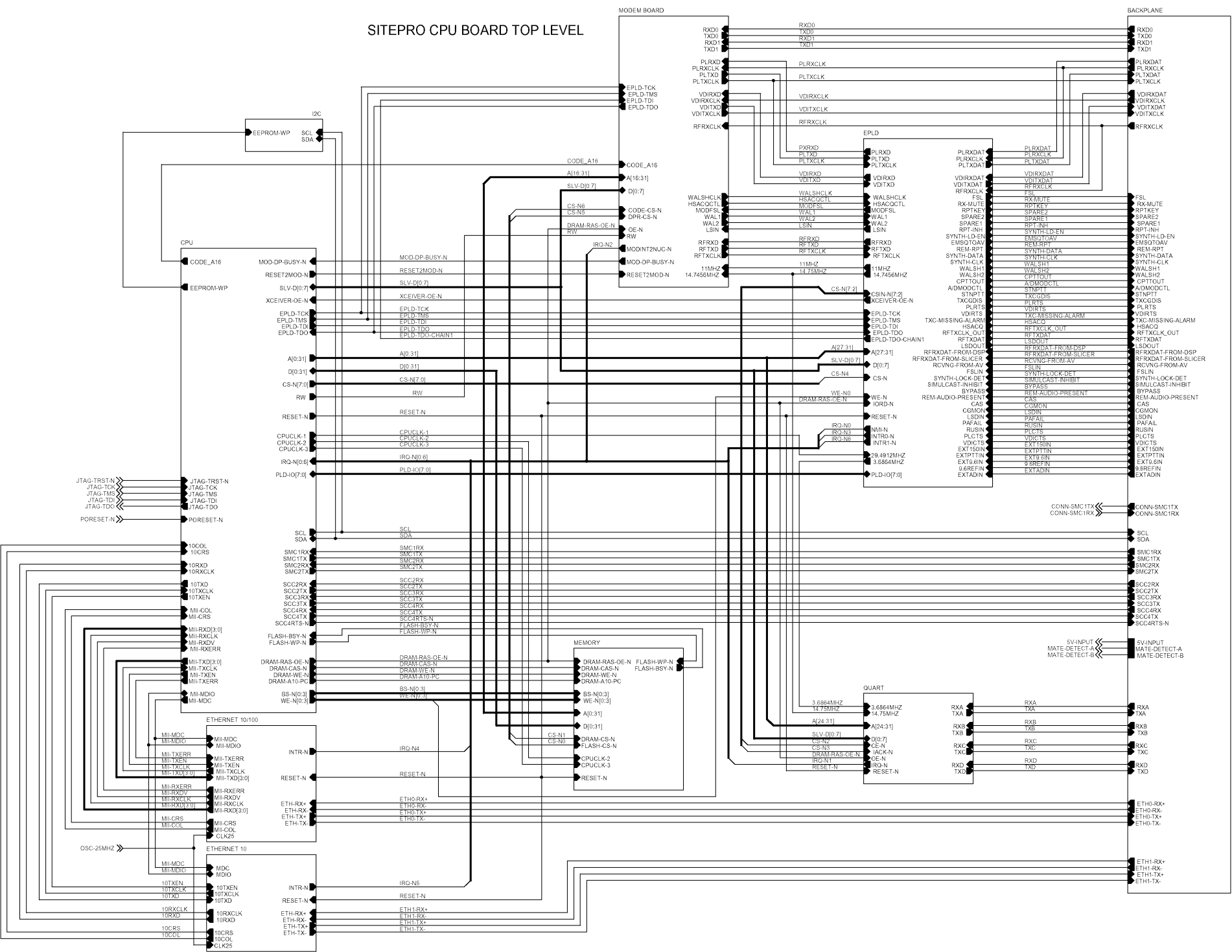
(SPARE - EPLD)	IRQ0	(NMI)
QUART	IRQ1	
MODEM DB DPR	IRQ2	
(SPARE - EPLD)	IRQ3	
ETHERNET 10/100	IRQ4	
ETHERNET 10	IRQ5	
(SPARE - EPLD)	IRQ6	

I2C ADDR		
=====		
Device	Read	Write

LEDs	0x41	0x40
DIPSWITCH	0x47	0x46
EEPROM	0xA1	0xA0
REAL-TIME CLOCK	0xD1	0xD0

(WD-CB101069V1, Sh. 1, Rev. A)

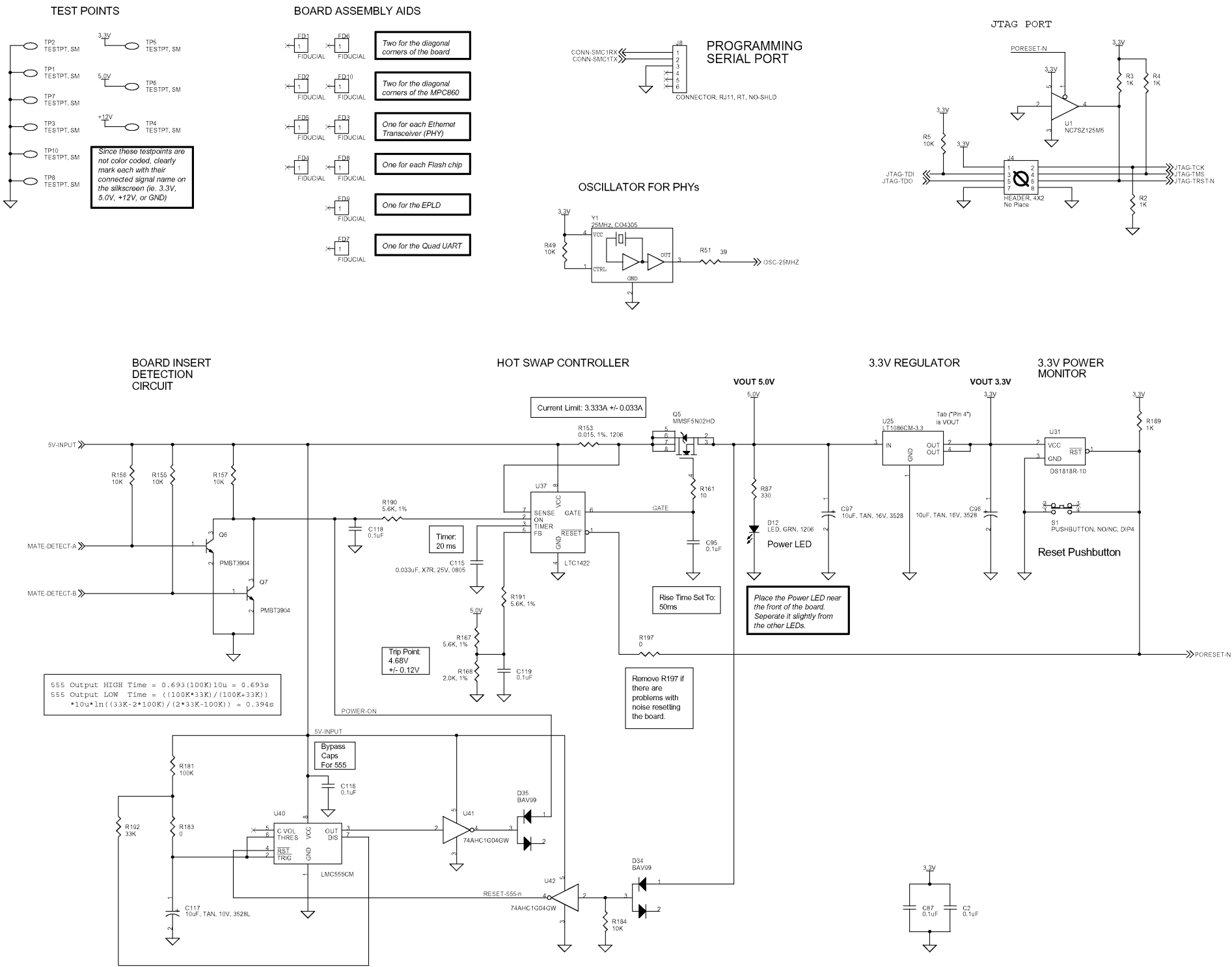
17.2.2 Block Diagram



CONTROLLER BOARD (A2) CB101069V1

(WD-CB101069V1, Sh. 2, Rev. A)

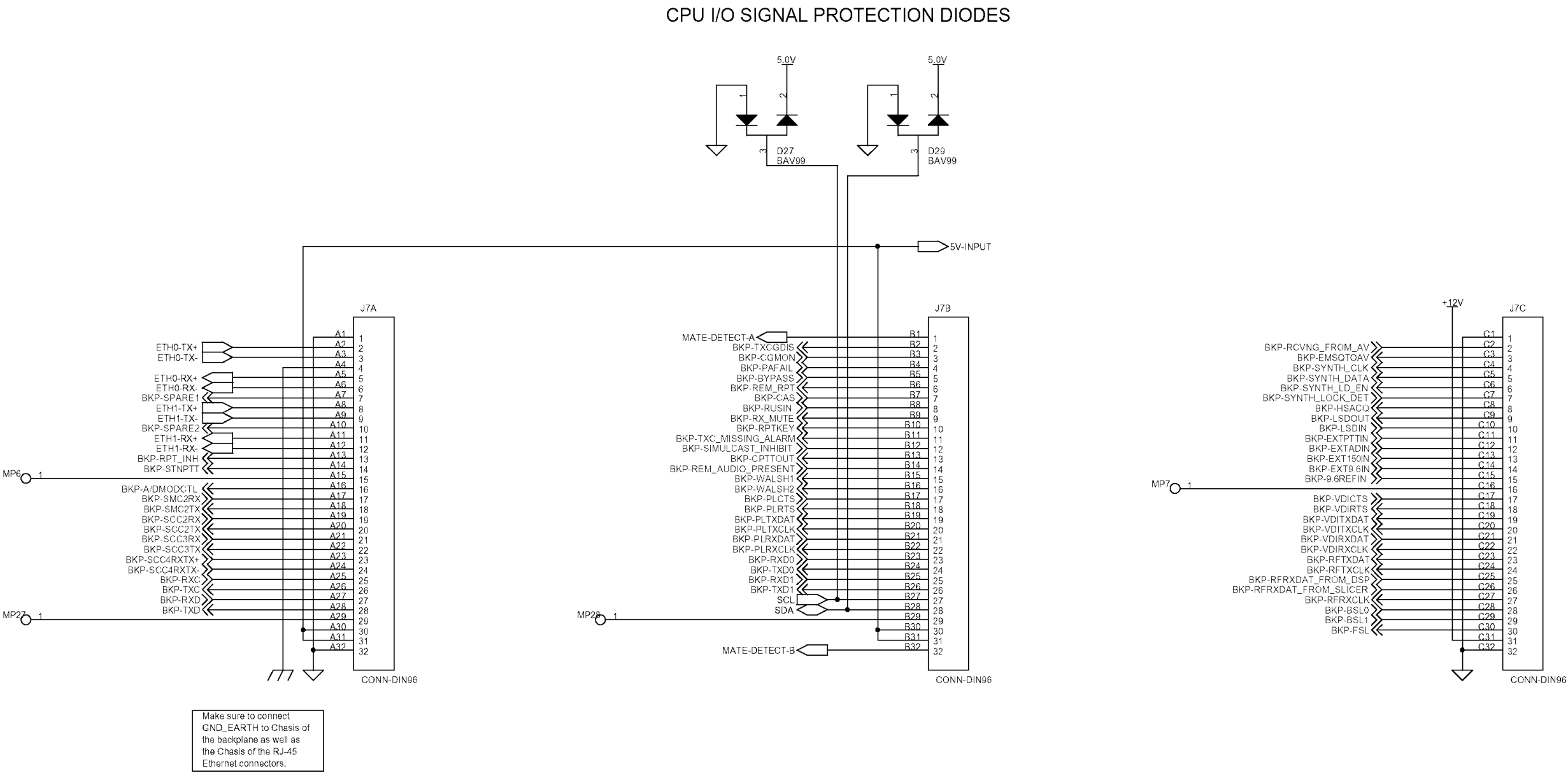
17.3 SYSTEM I/O



CONTROLLER BOARD (A2) CB101069V1

(WD-CB101069V1, Sh. 3, Rev. A)

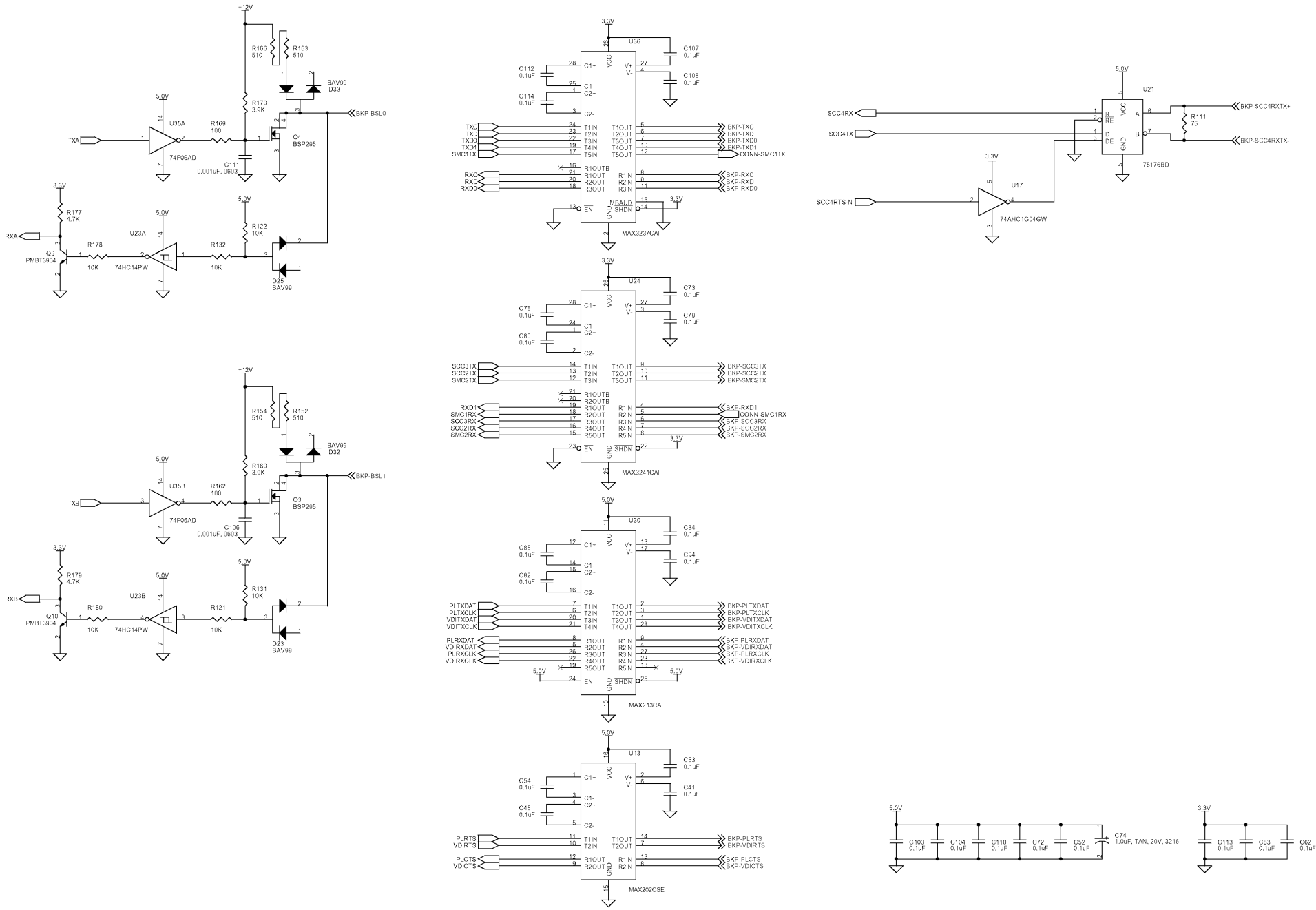
17.3.1 Backplane



CONTROLLER BOARD (A2) CB101069V1

Board Connections

(WD-CB101069V1, Sh. 4, Rev. A)



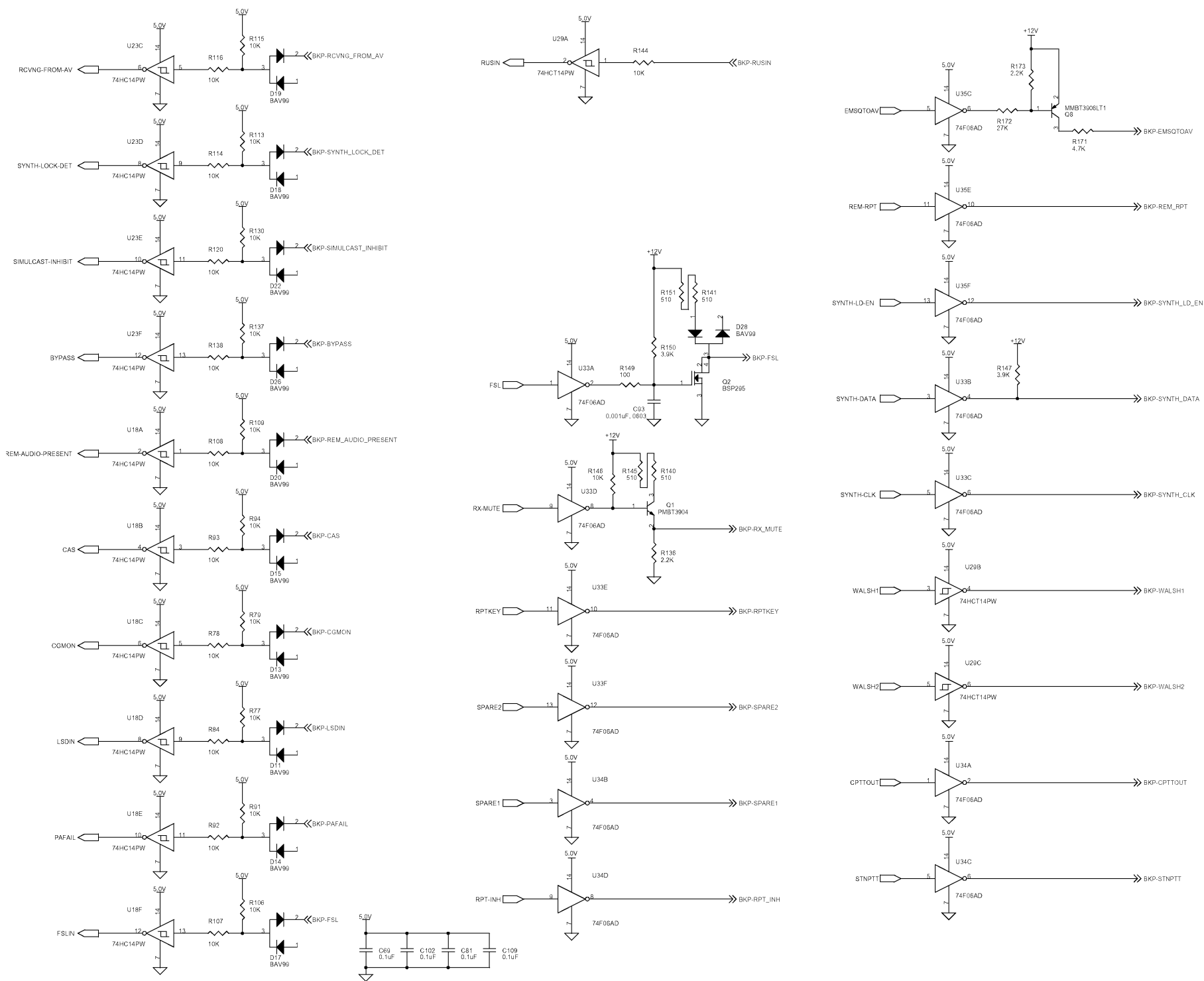
CONTROLLER BOARD (A2) CB101069V1

Backplane

Serial I/O

(WD-CB101069V1, Sh. 5, Rev. A)

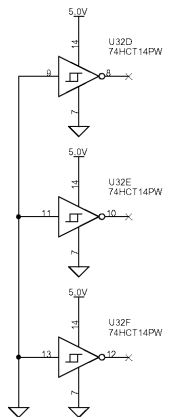
SCHEMATIC DIAGRAM



CONTROLLER BOARD (Backplane) A2 CB101069V1

I/O

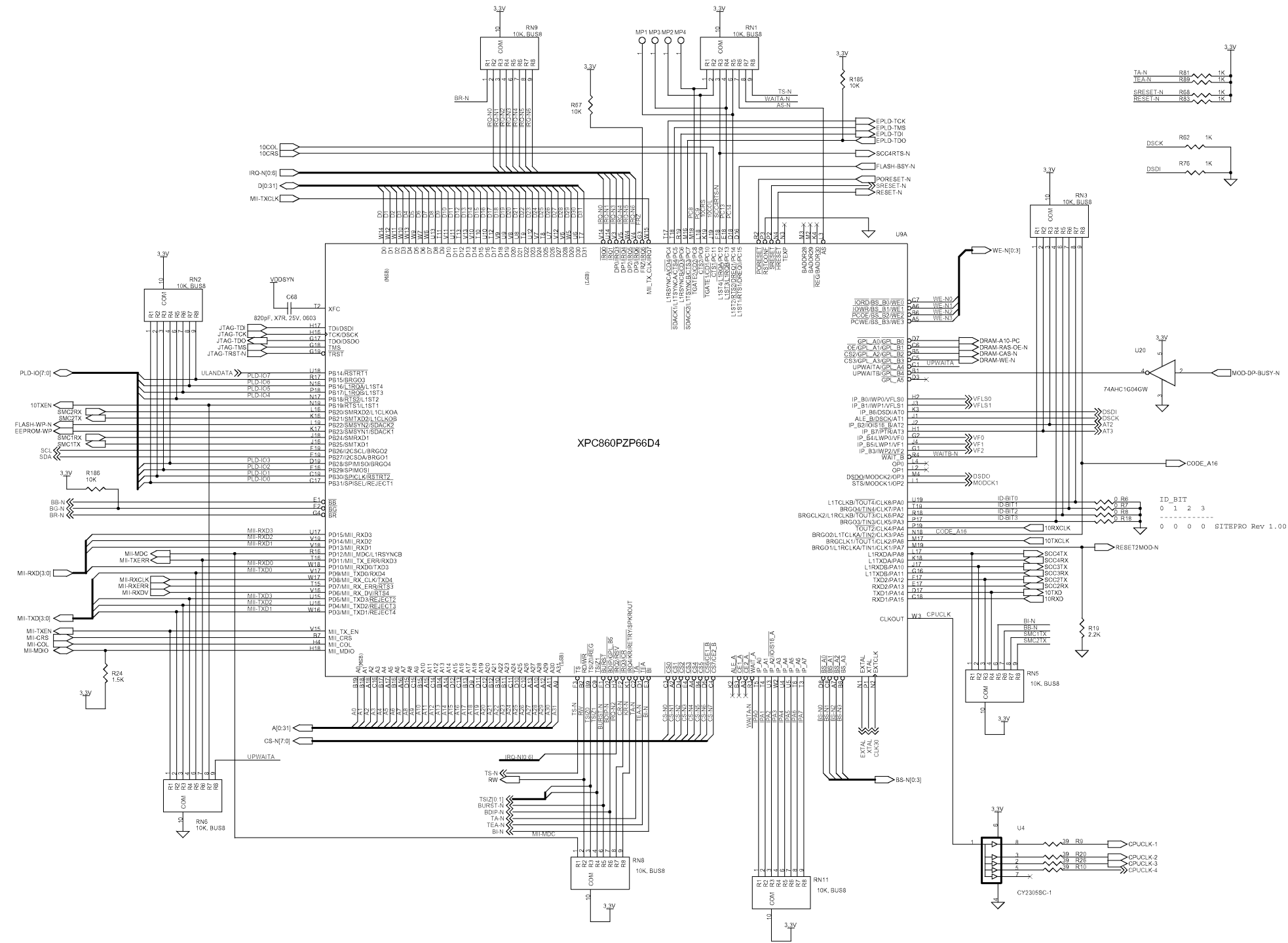
(WD-CB101069V1, Sh. 6, Rev. A)



Backplane

(WD-CB101069V1, Sh. 7, Rev. A)

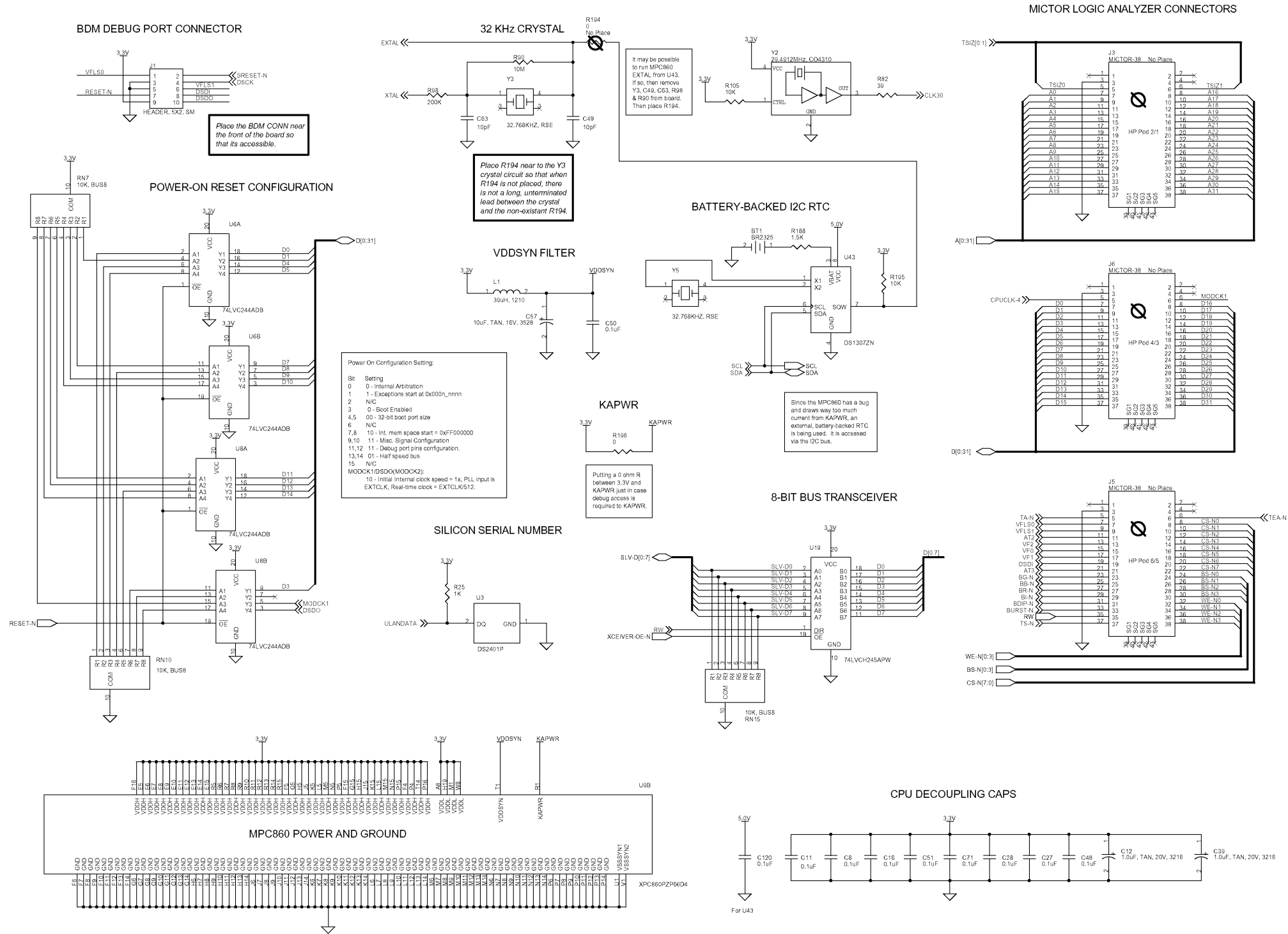
17.3.2 CPU



CONTROLLER BOARD (A2) CB101069V1

Processor

(WD-CB101069V1, Sh. 8, Rev. A)



CONTROLLER BOARD (A2) CB101069V1

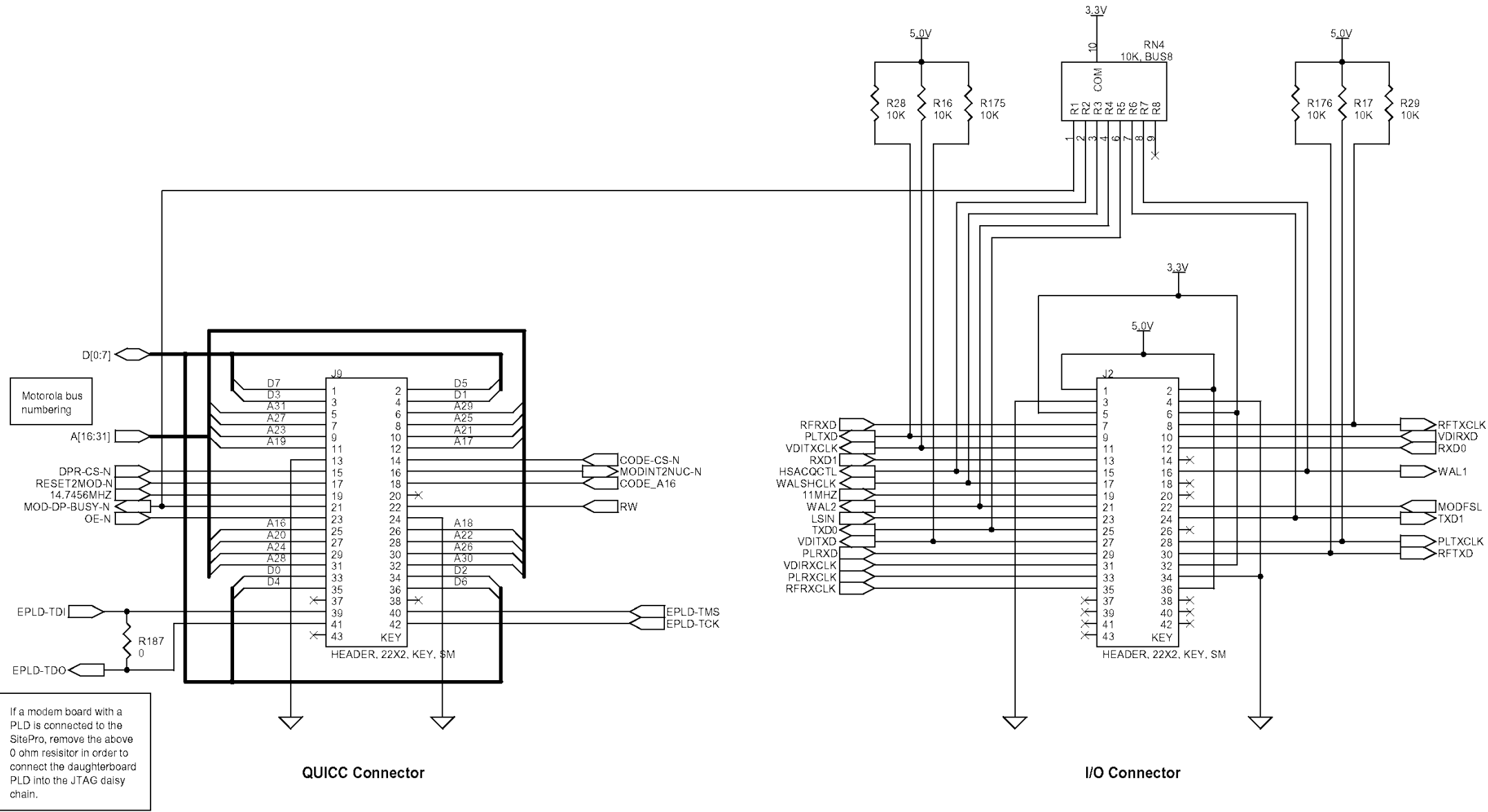
Processor Support
(WD-CB101069V1, Sh. 9, Rev. A)

17.3.3 Modem Board Connector

Motorola sets bit 0 as the MSB. Therefore, use the following tables to see how the data and address busses connect to the modem daughterboard.

MOT	DB
====	===
D0 (MSB)	D7
D1	D6
D2	D5
D3	D4
D4	D3
D5	D2
D6	D1
D7 (LSB)	D0

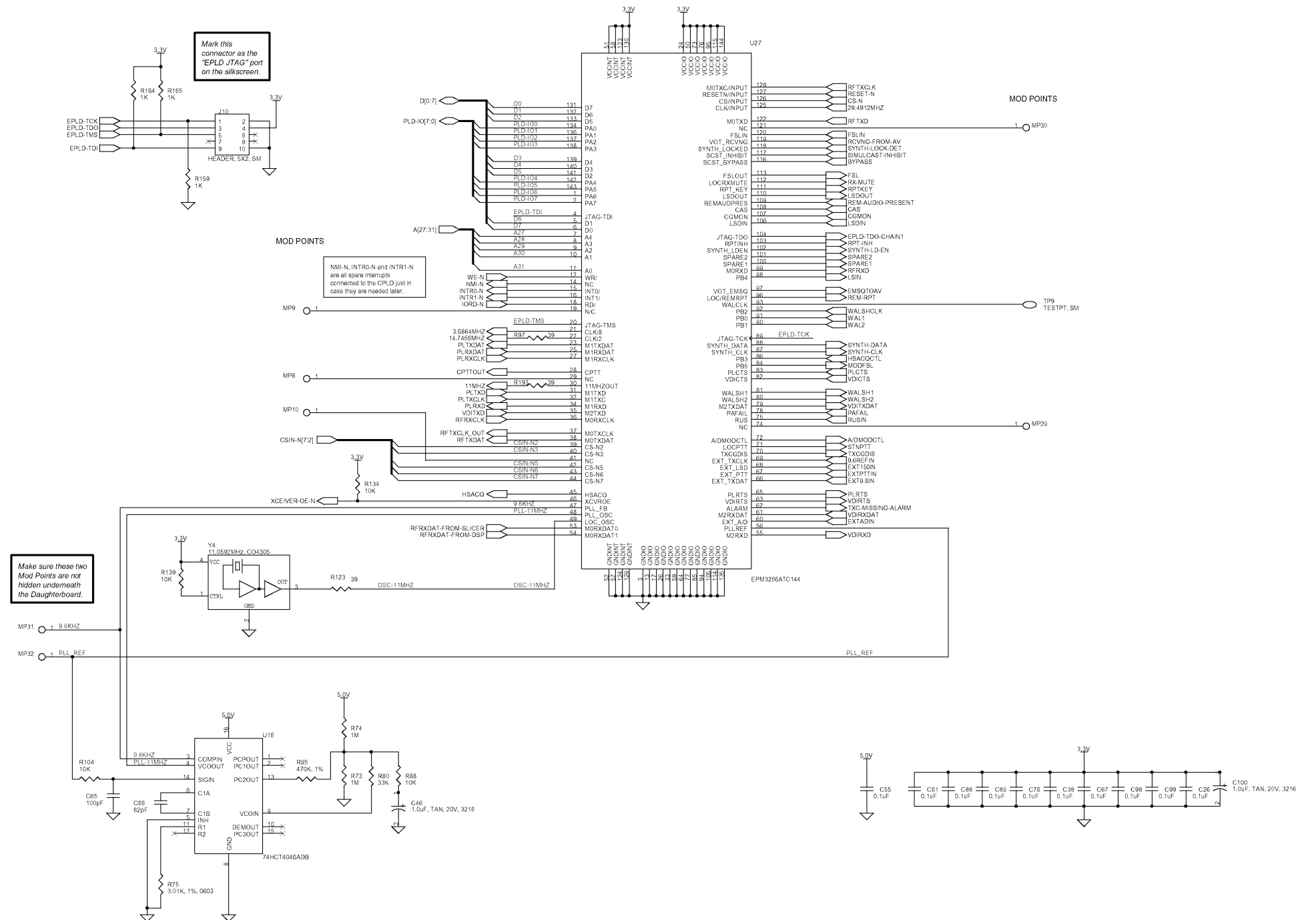
A16 (MSB)	A15
A17	A14
A18	A13
A19	A12
A20	A11
A21	A10
A22	A9
A23	A8
A24	A7
A25	A6
A26	A5
A27	A4
A28	A3
A29	A2
A30	A1
A31 (LSB)	A0



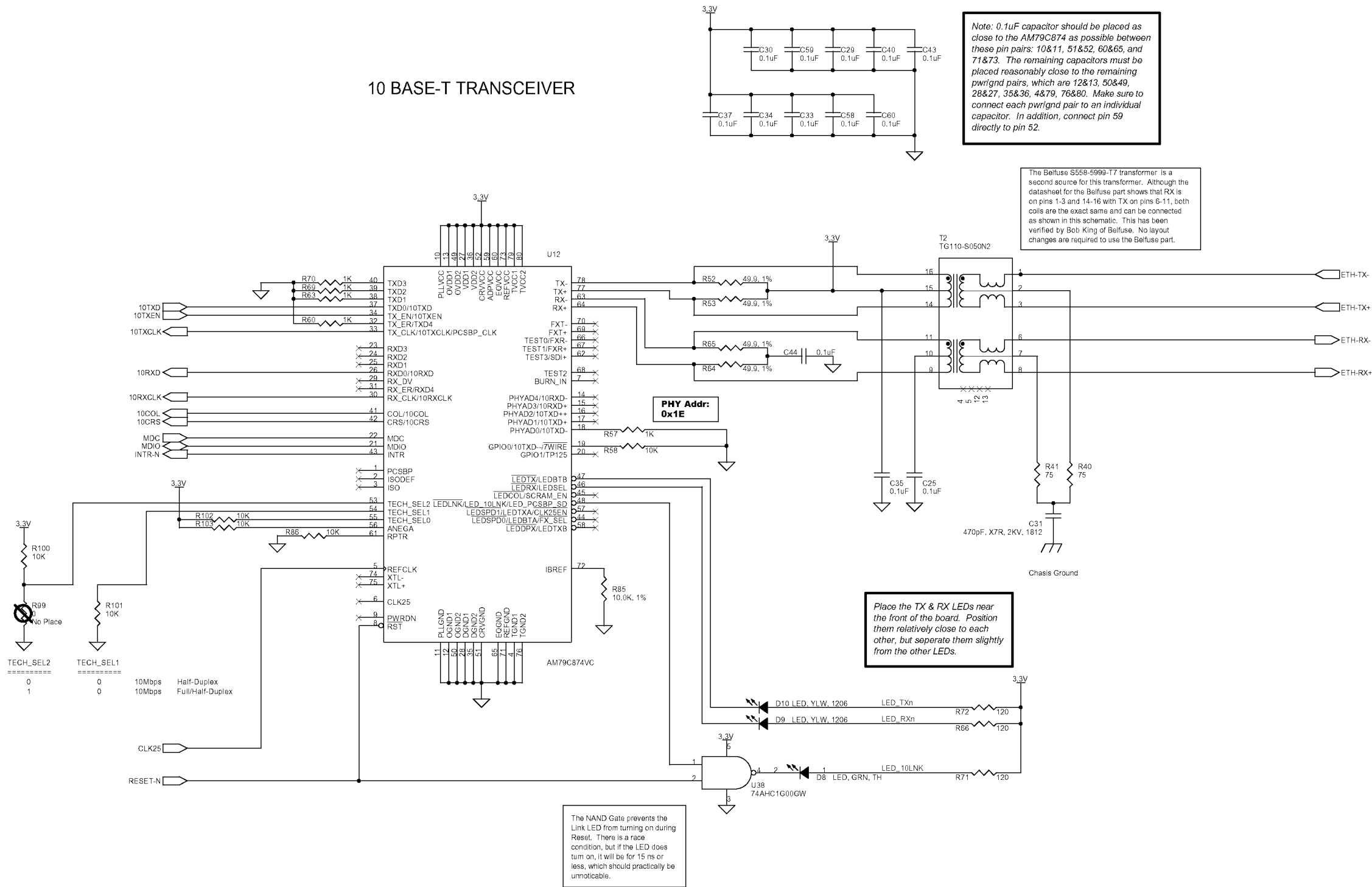
CONTROLLER BOARD (A2) CB101069V1

(WD-CB101069V1, Sh. 10, Rev. A)

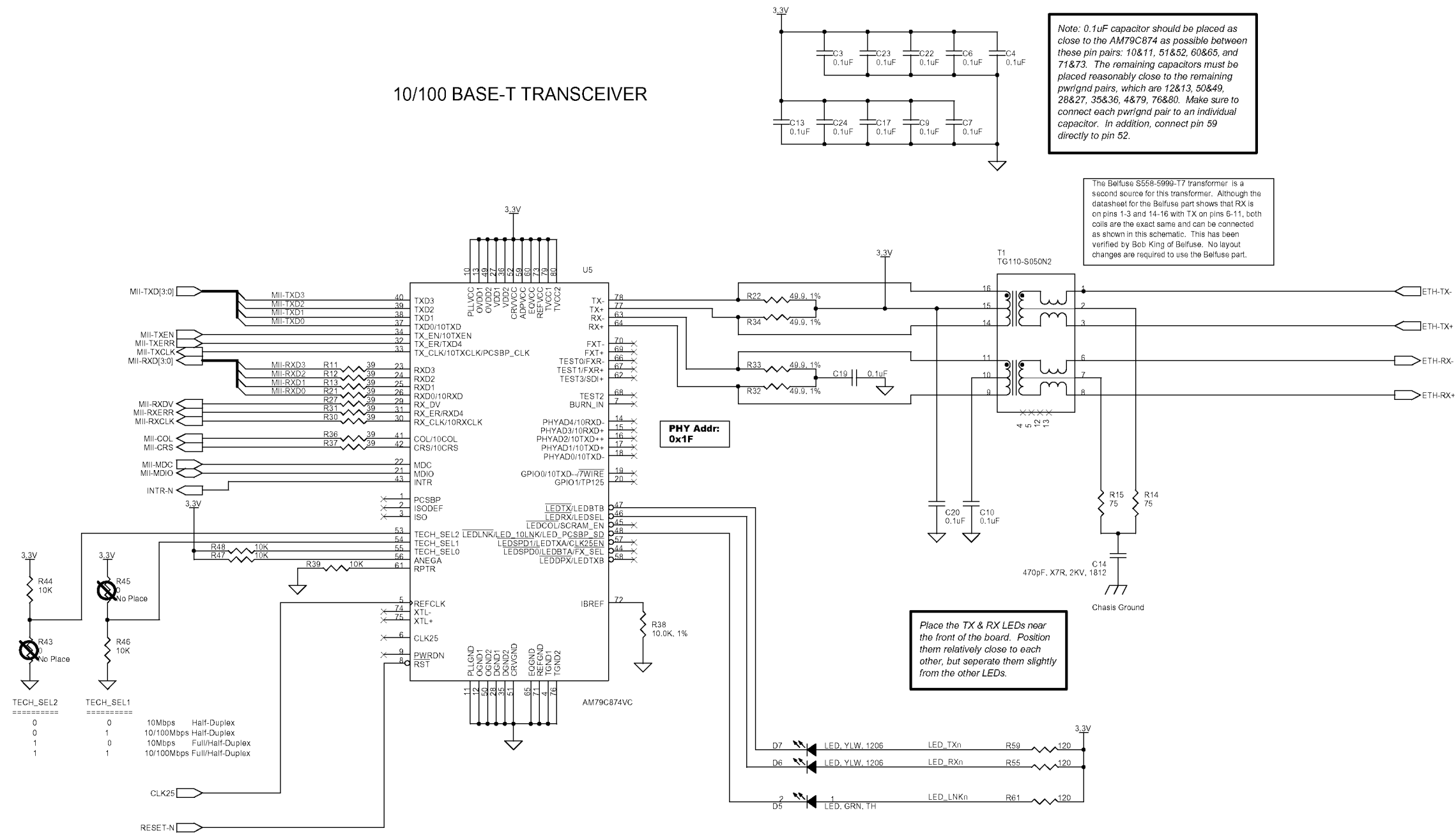
17.3.4 EPLD



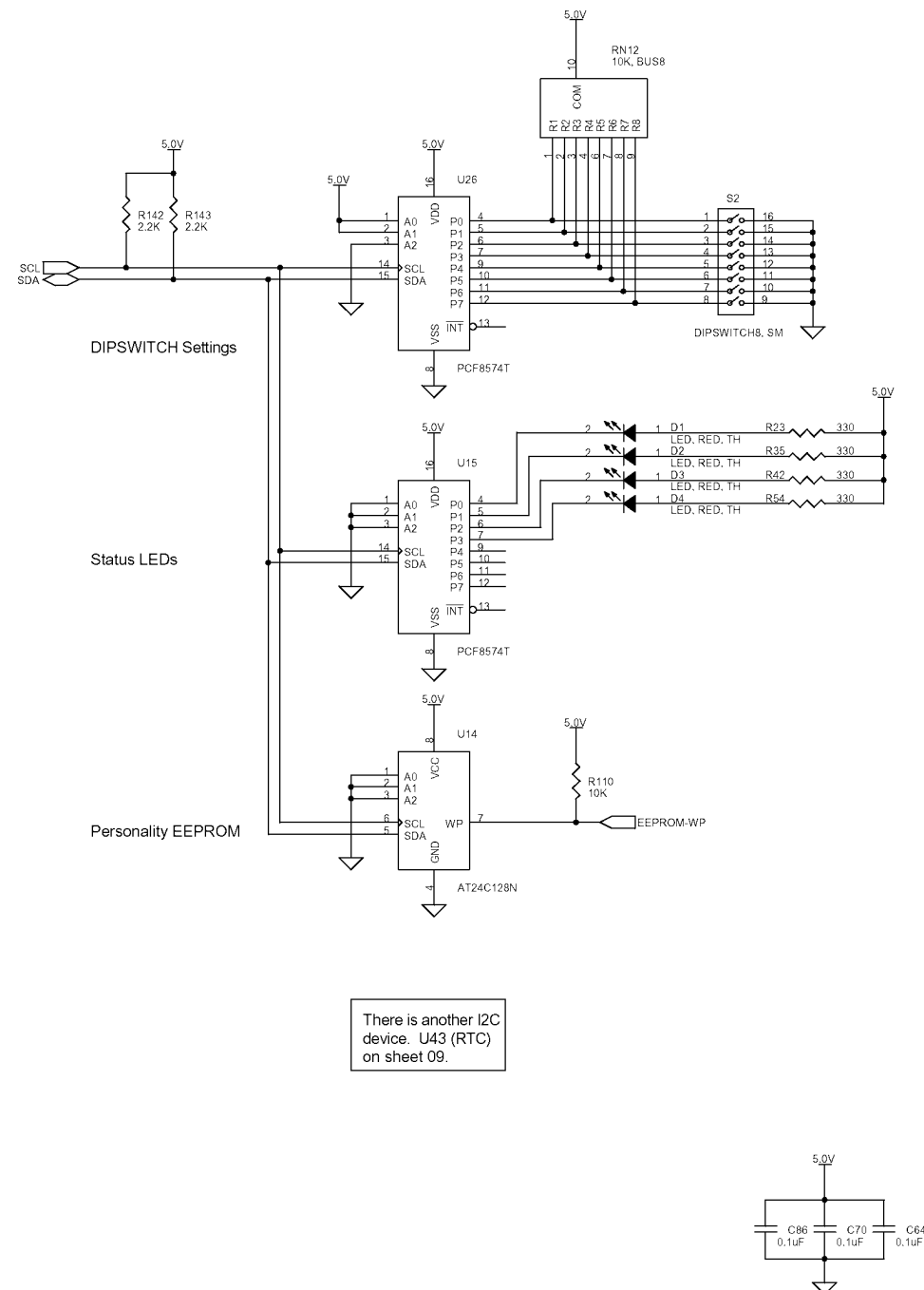
17.3.5 Ethernet 10 (10 Mbit PHY)



17.3.6 Ethernet 10/100 (10/100 Mbit PHY)



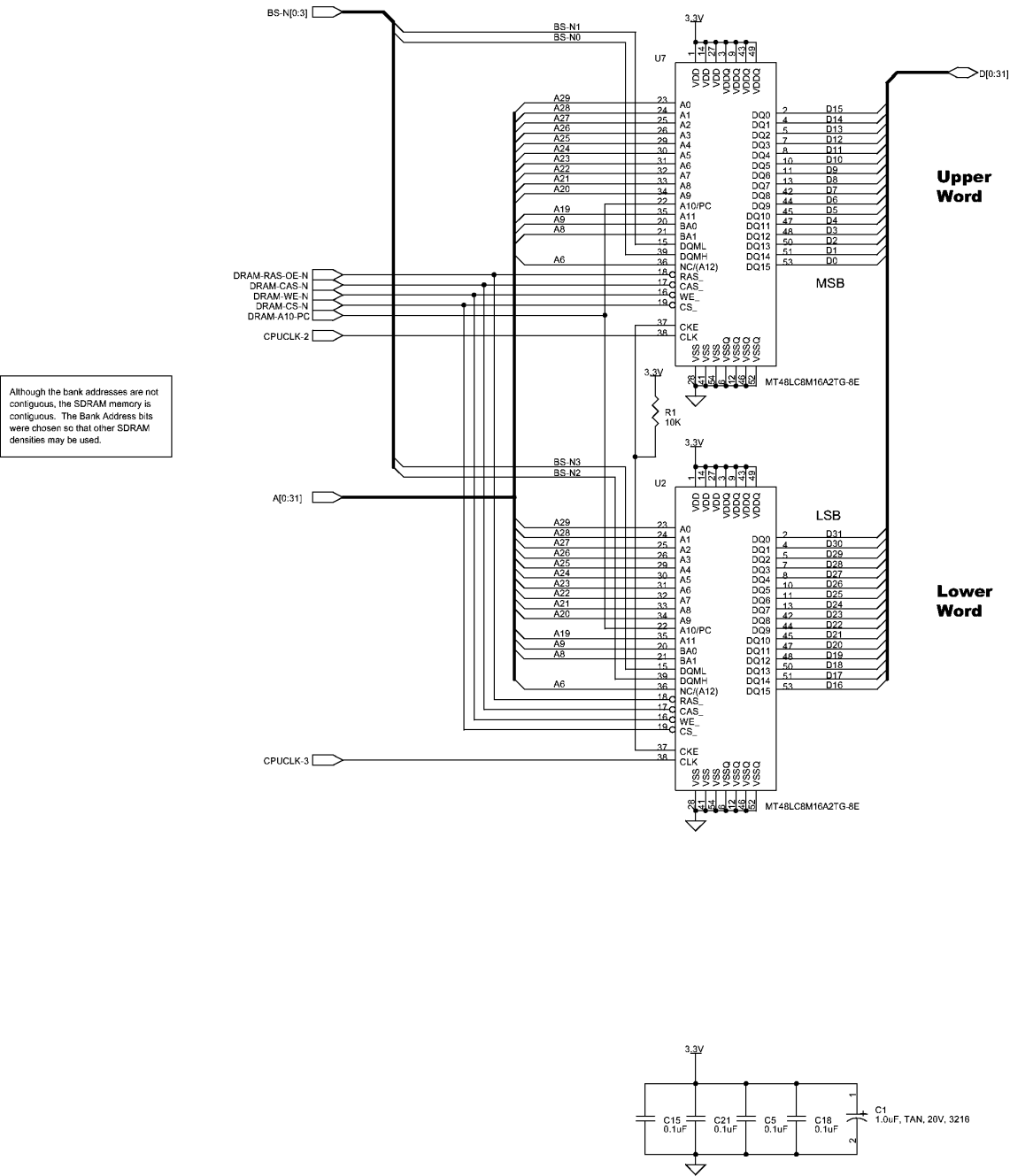
17.3.7 I²C Bus



CONTROLLER BOARD (A2) CB101069V1

(WD-CB101069V1, Sh. 14, Rev. A)

17.3.8 Memory

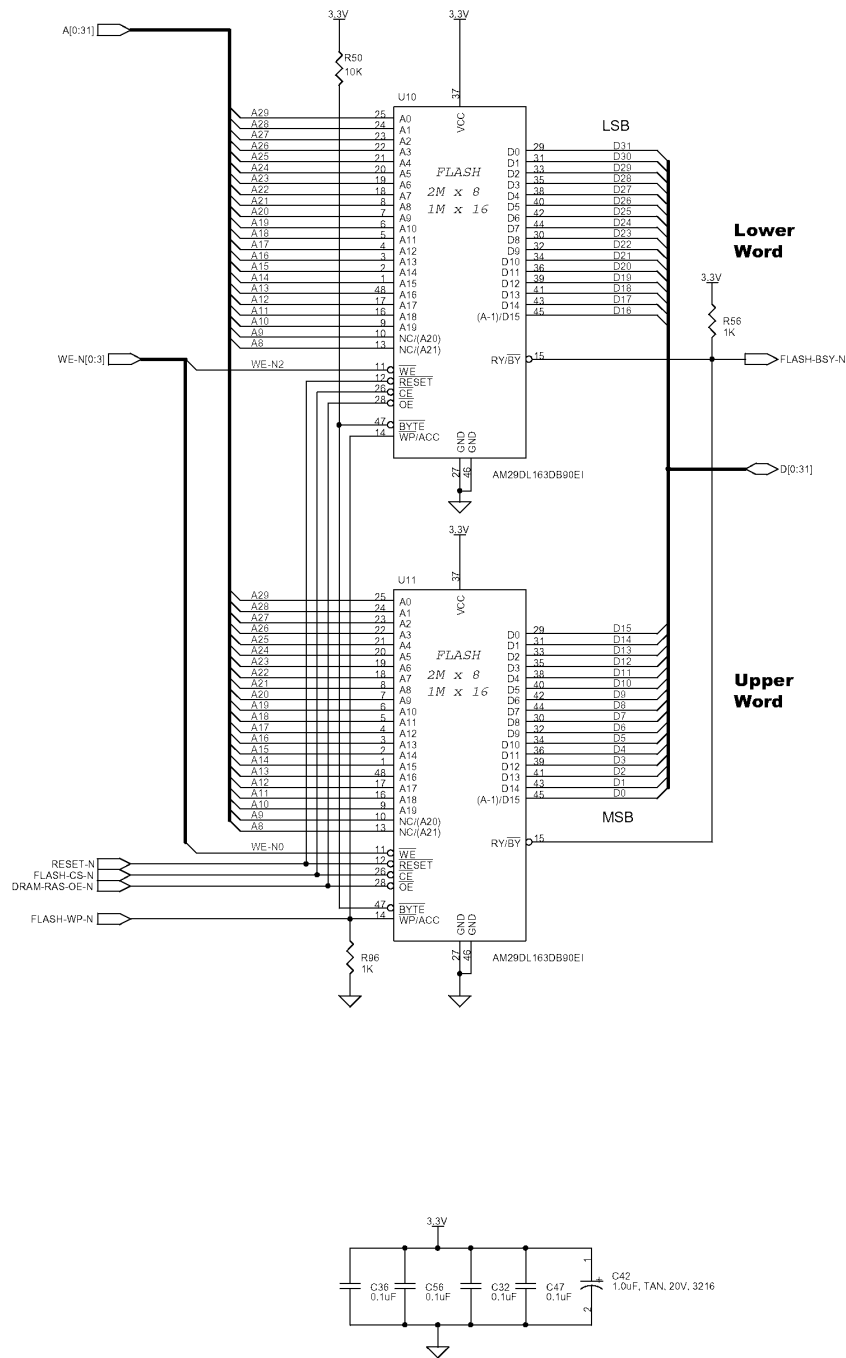


CONTROLLER BOARD (A2) CB101069V1

DRAM

(WD-CB101069V1, Sh. 15, Rev. 1.03)

SCHEMATIC DIAGRAM



SUPPORTED FLASH DENSITIES	
TOTAL FLASH SIZE	DEVICE TYPE
1M x 32 (4MB)	AM29DL162D/163D/164D
2M x 32 (8MB)	AM29DL322D/323D/324D
4M x 32 (16MB)	Does Not Exist

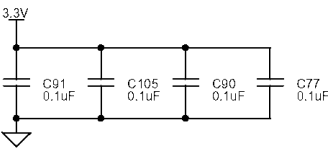
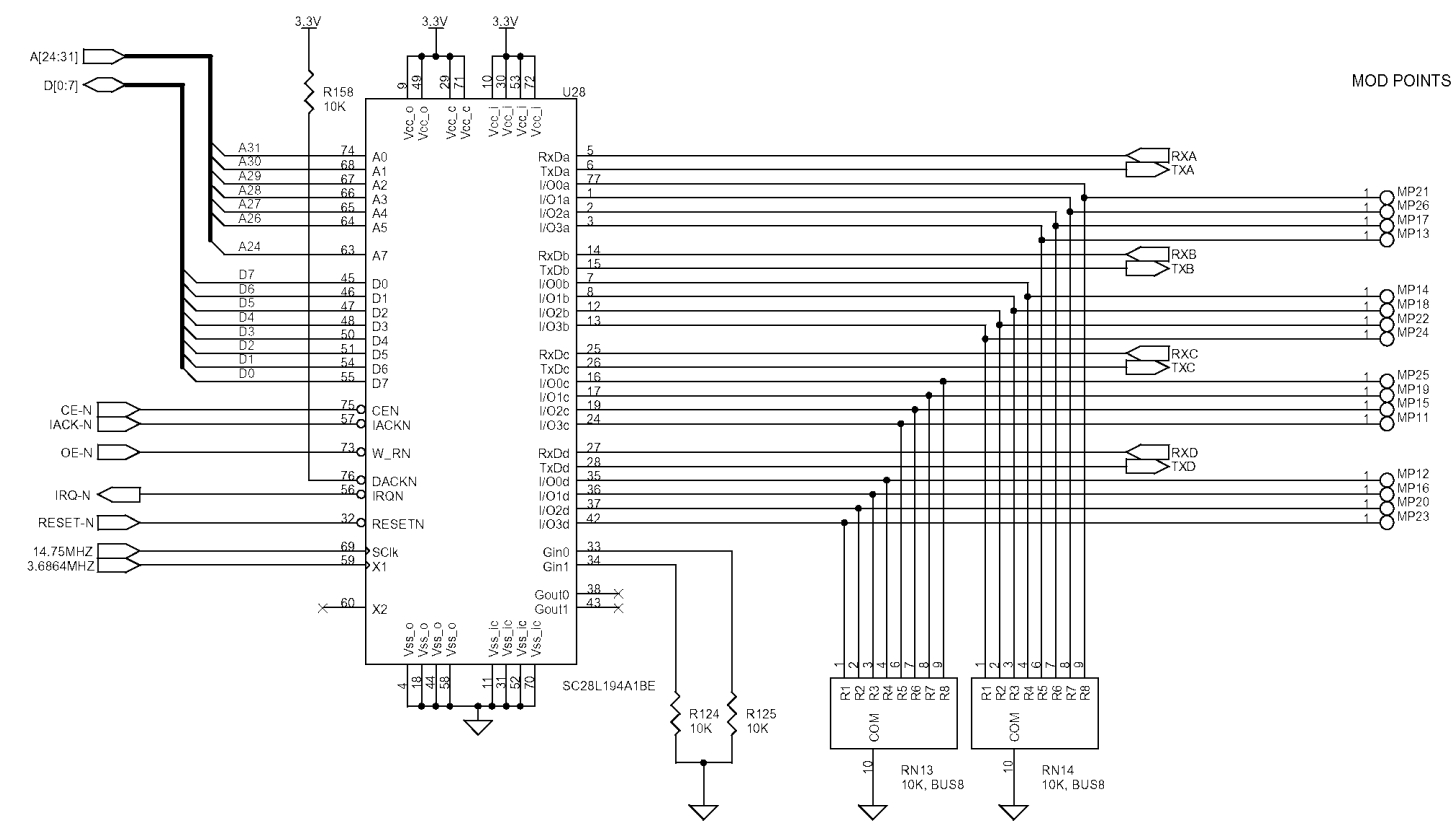
CONTROLLER BOARD (A2) CB101069V1

Memory

Flash

(WD-CB101069V1, Sh. 16, Rev. A)

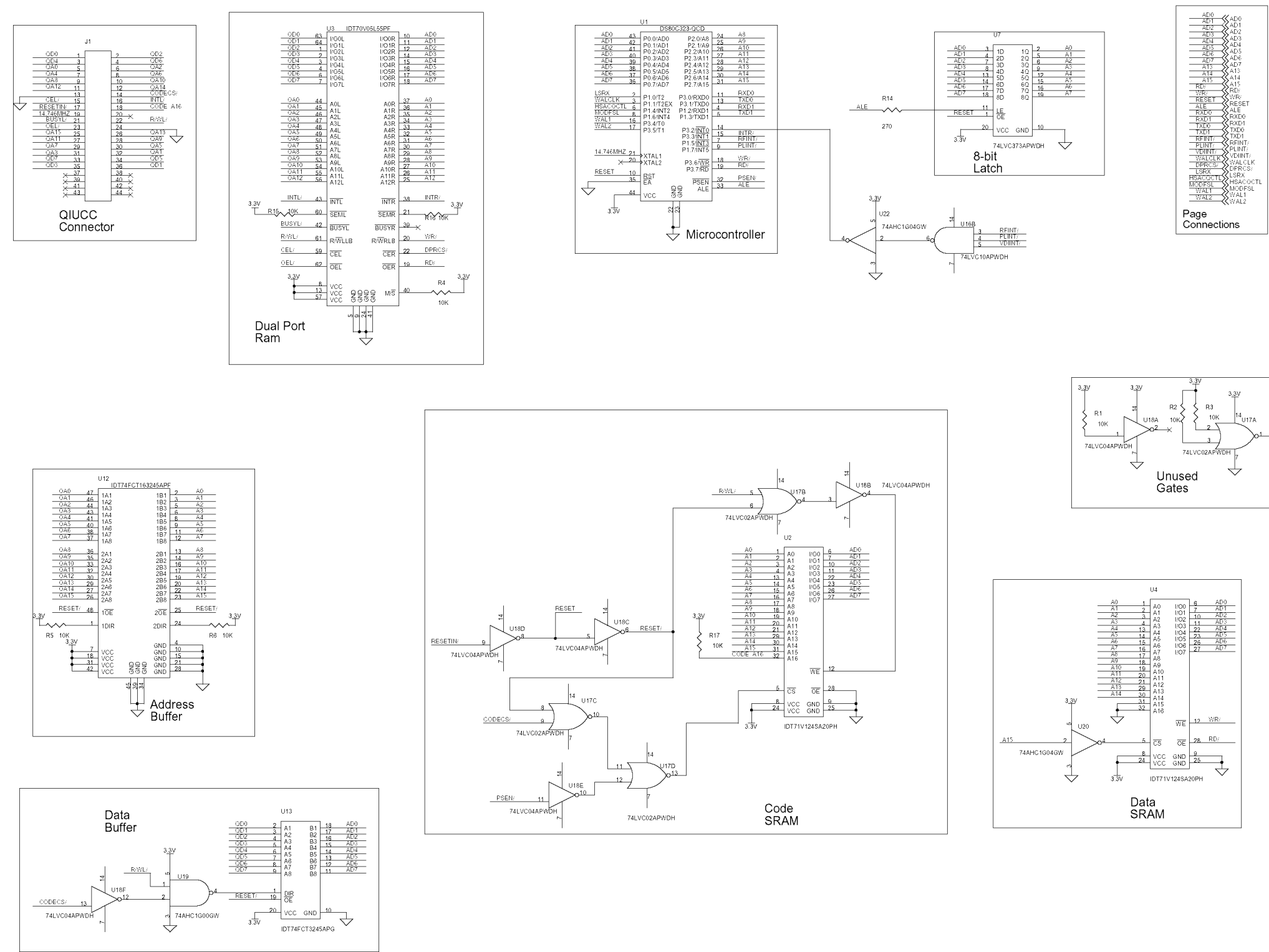
17.3.9 QUART



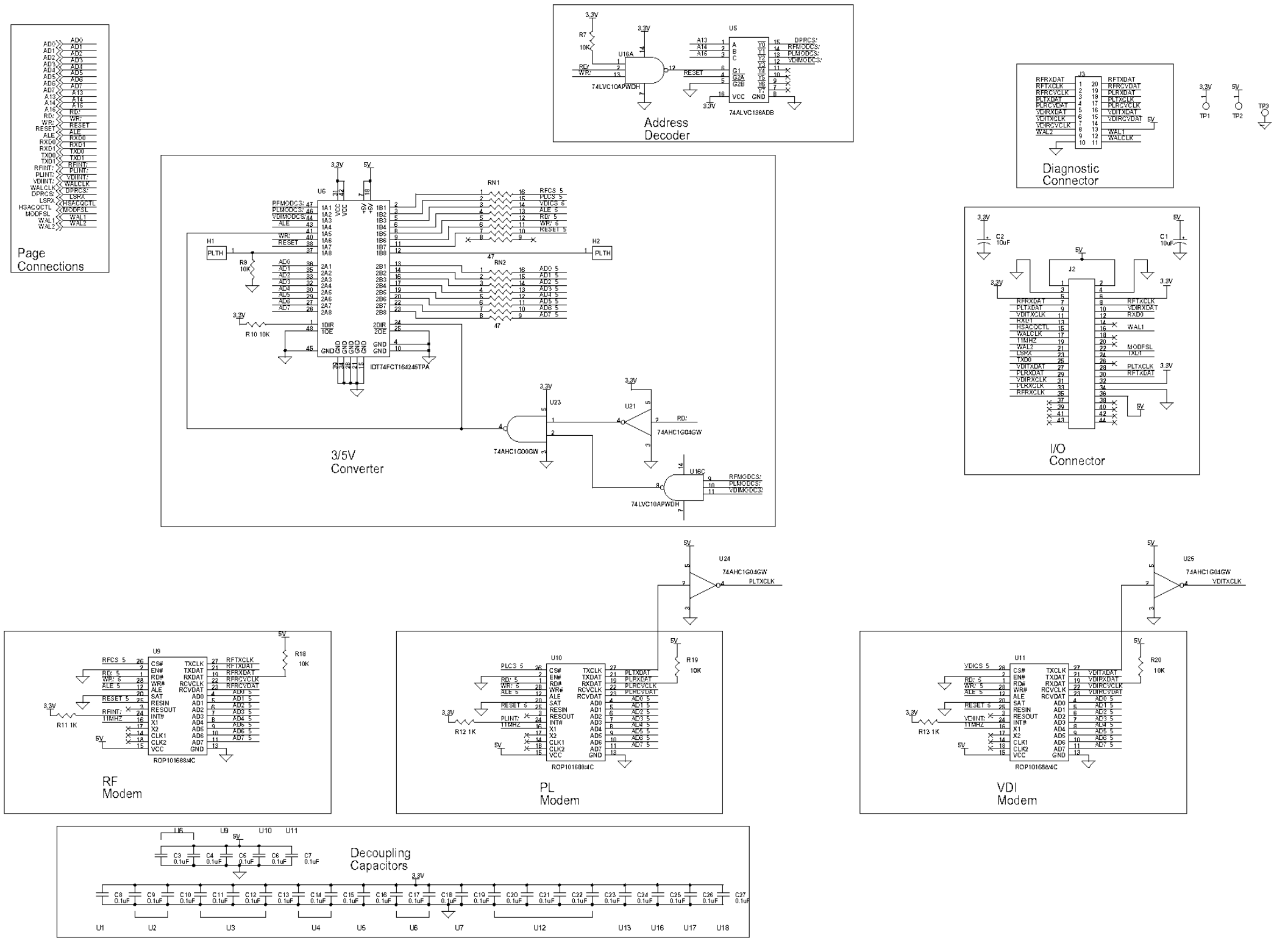
CONTROLLER BOARD (A2) CB101069V1

(WD-CB101069V1, Sh. 17, Rev. A)

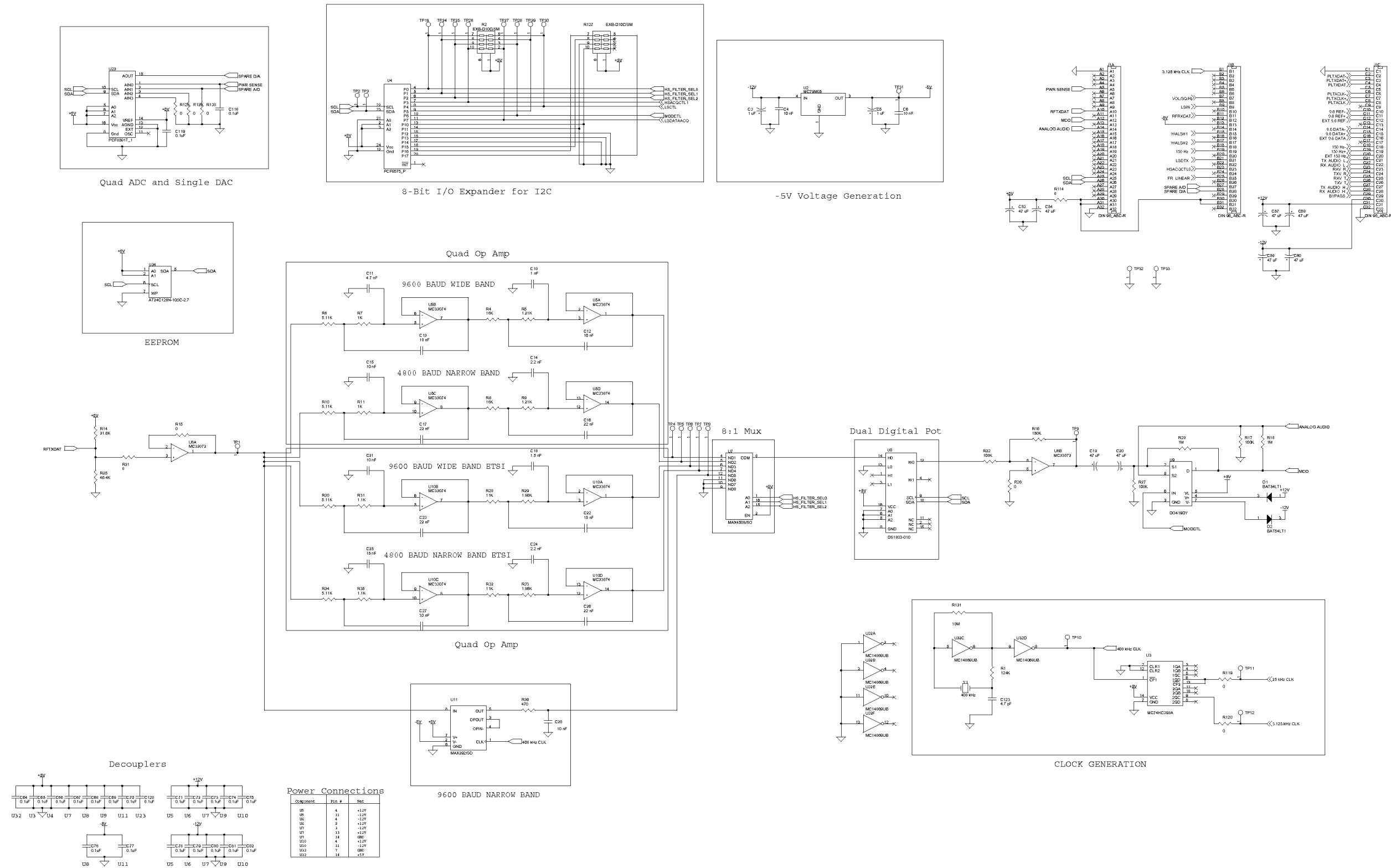
17.4 SitePro MODEM BOARD (A2-A1) CB101074V1



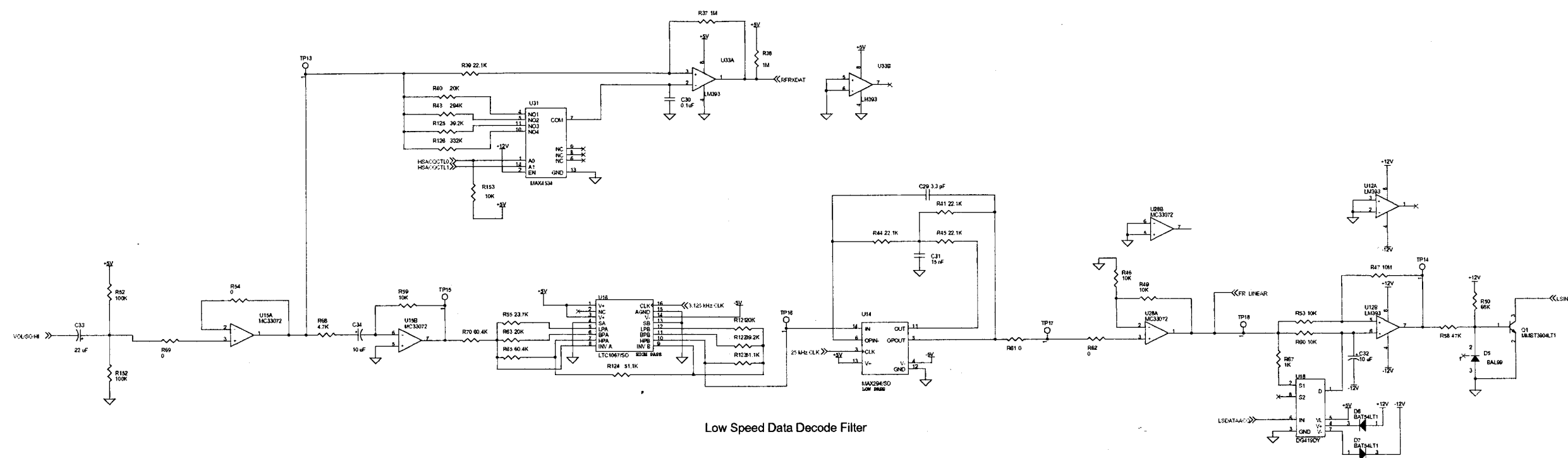
(WD-CB101074V1, Sh. 1, Rev. B)



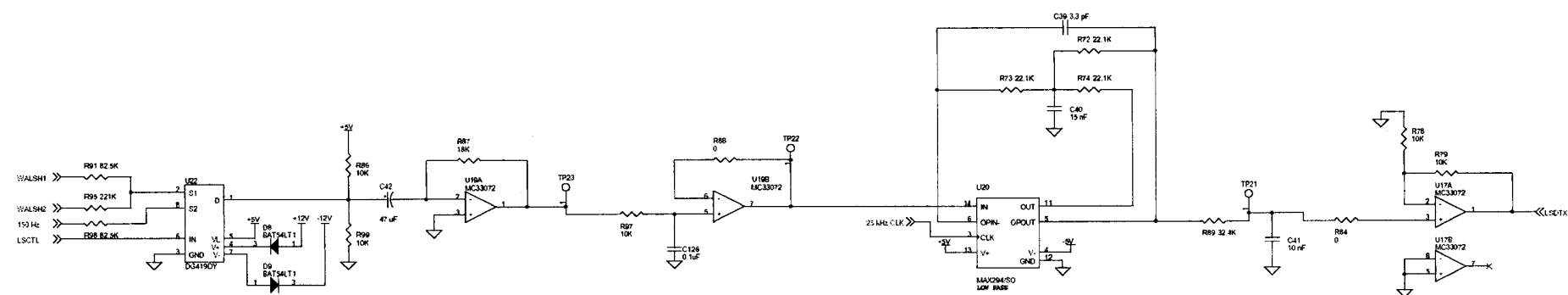
17.5 ANALOG FILTER BOARD (A4) CB101070V1



(WD-CB101070V1, Sh. 1, Rev. A)



Low Speed Data Decode Filter

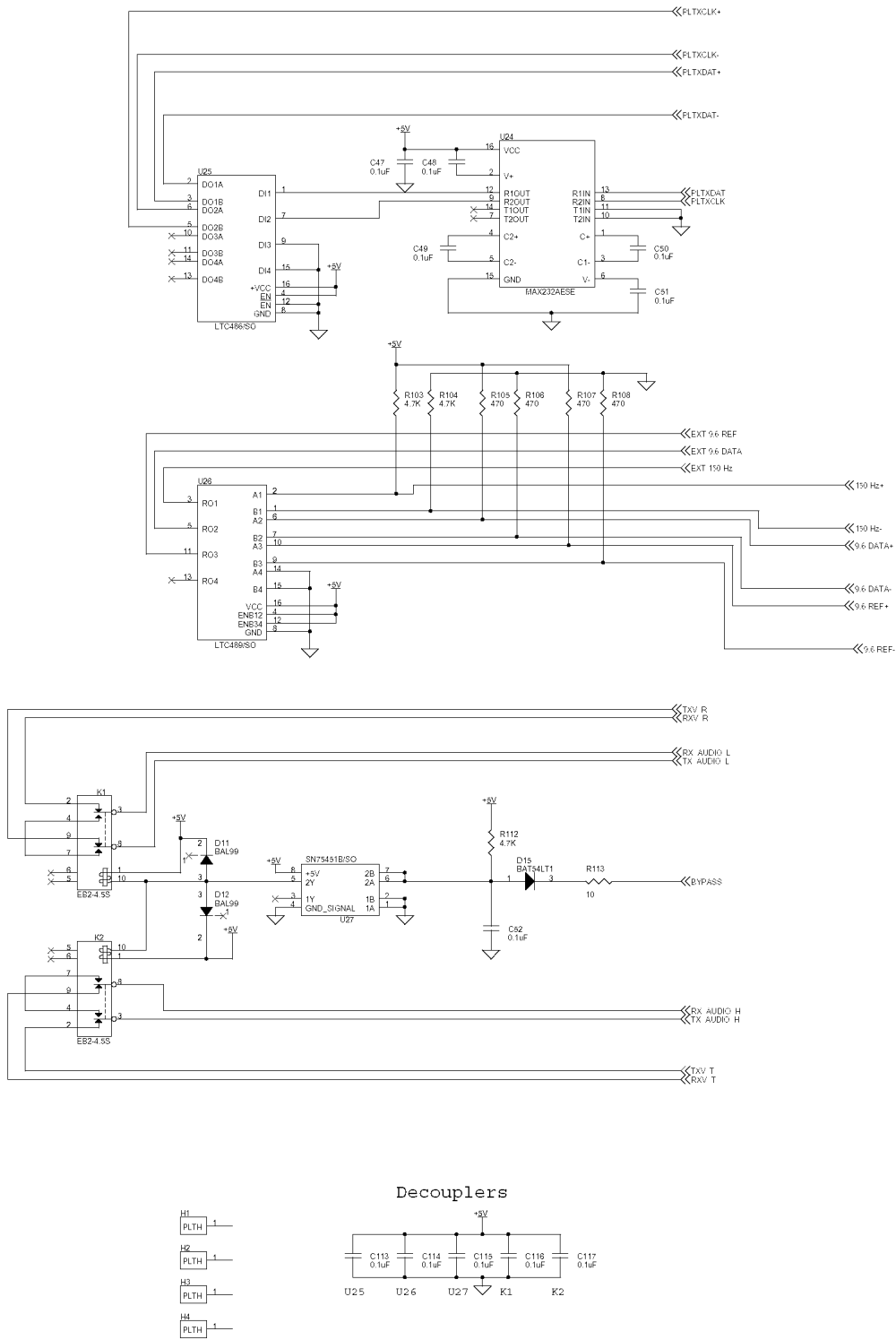


Low Speed Data Encode Filter

ANALOG FILTER BOARD (A4) CB101070V1

(WD-CB101070V1, Sh. 2, Rev. A)

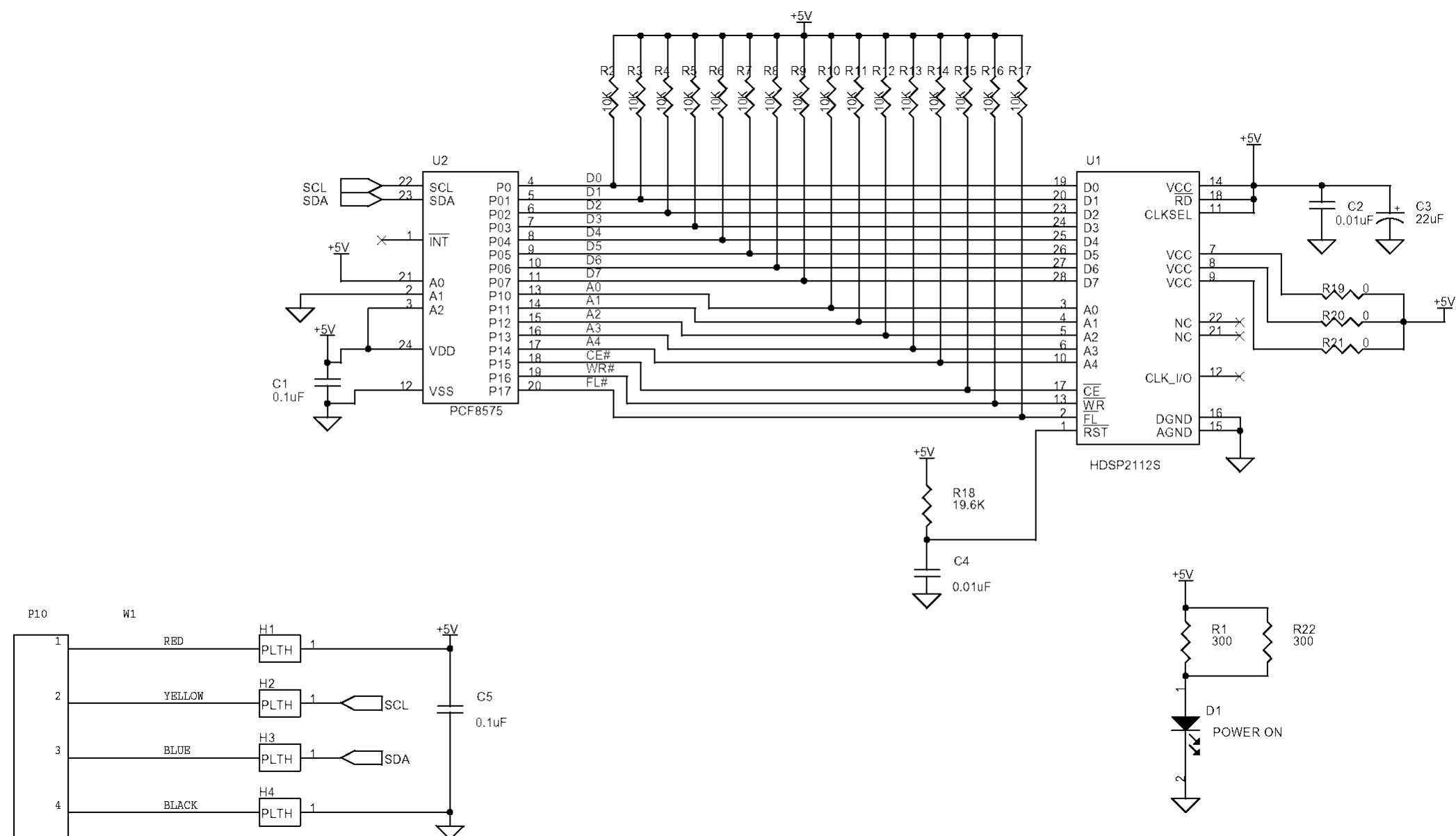
SCHEMATIC DIAGRAM



ANALOG FILTER BOARD (A4) CB101070V1

(WD-CB101070V1, Sh. 3, Rev. A)

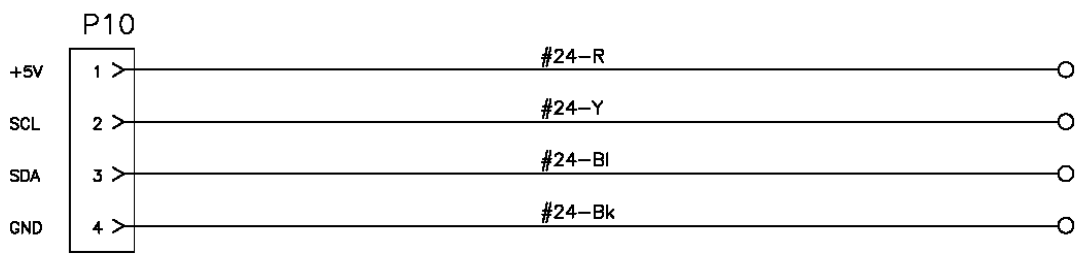
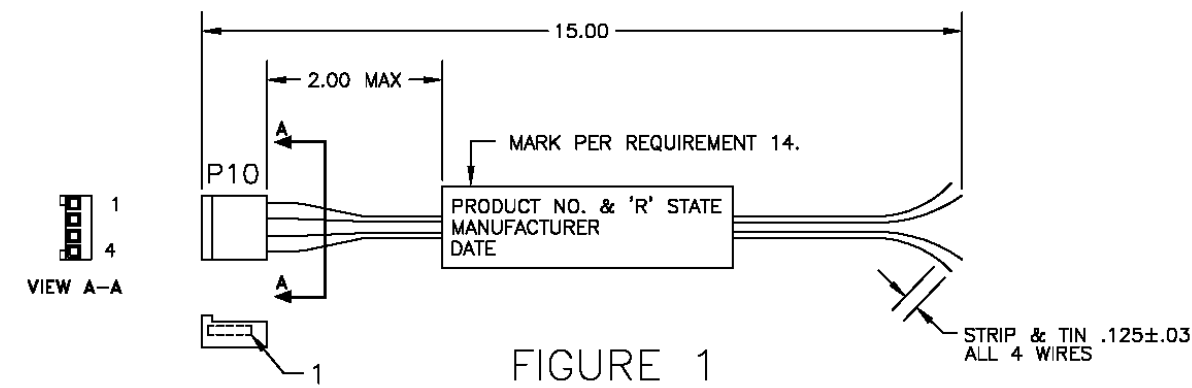
17.6 DISPLAY BOARD (A6-A1) CB101077V1



(WD-CB101077V1, Rev. A)

18.0 CABLE DIAGRAMS

18.1 DISPLAY BOARD (A6-W1) DATA CABLE CA101222V1



WIRING DIAGRAM

REQUIREMENTS (*" INDICATES REQUIREMENT NOT APPLICABLE TO THIS PRODUCT)

- 1 CABLE ASSEMBLY SHALL BE CONSTRUCTED AS SHOWN IN FIGURE 1.
- 2 LACE OR SPOT TIE AS REQUIRED TO MAINTAIN CABLE SHAPE AND DIMENSIONS.
- 3 ALL CONDUCTORS SHALL BE STRANDED COPPER UNLESS OTHERWISE SPECIFIED.
- 4 WIRE GAUGE AND COLOR SHALL BE AS SPECIFIED IN WIRING DIAGRAM OR CONNECTION LIST.
- 5 WIRING SHALL BE PER WIRING DIAGRAM OR CONNECTION LIST AND 100% INSPECTED.
- 6 ALL WIRE TERMINATIONS SHALL PROVIDE INSULATION SUPPORT
- 7 ALL CONNECTORS TO PROVIDE INSULATION SUPPORT
- 8 UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN INCHES.
- 9 UNLESS OTHERWISE SPECIFIED, TOLERANCE: FOR ALL DIMENSIONS IS [+/-0.50]
- 10 LIMITING VALUES: RATED VOLTAGE FOR CABLE ASSEMBLY = 300 VAC MIN.
- 11 MARK CONNECTORS AS SHOWN IN FIGURE 1
- 12 MATERIAL LIST

ITEM	M/A-COM PART NO. (FOR REF. ONLY)	VENDOR PART NO. (OR EQUIVALENT)	DESCRIPTION	QTY
P10	19A700041P30 ----	MOLEX Pt No 22-01-2045	4 POSITION MOLDED HOUSING ----	1
1	19A704779P26 ----	MOLEX Pt No 08-55-0101	CONTACT ----	4

- 13 ENVIRONMENTAL RESISTANCE: OPERATING TEMPERATURE RANGE -20 TO +75 DEG. C
MAX CONTINUOUS TEMPERATURE +75 DEG. C
- 14 MARKING - EACH ASSEMBLY AND ITS PACKING CONTAINER
SHALL BE MARKED WITH THE FOLLOWING INFORMATION

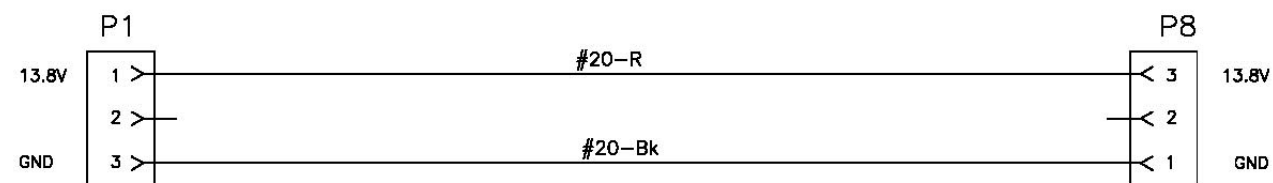
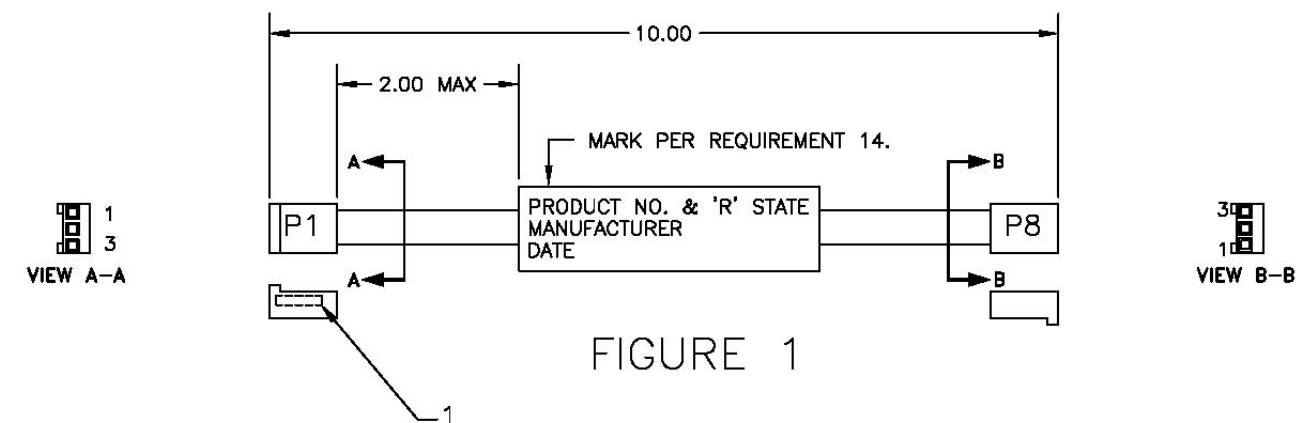
PRODUCT CODE - (SEE REQUIREMENT 15 "PRODUCT LIST")	
'R' STATE PER DOCUMENT 1095	
MANUFACTURERS NAME OR TRADEMARK.	
DATE OF MANUFACTURE CODE	

THE MARKING SHALL BE RESISTANT TO MECHANICAL WEARING THAT CAN ARISE AT NORMAL HANDLING, STORAGE AND OPERATION.

15 PRODUCT LIST	
PRODUCT NUMBER	DESCRIPTION
CA101222V1	DISPLAY MODULE CABLE

(WD-CA101222V1, Rev. B)

18.2 13.8V INPUT POWER CABLE (W1) (Input) CA101211V1



WIRING DIAGRAM

REQUIREMENTS (* indicates requirement not applicable to this product)

- 1 CABLE ASSEMBLY SHALL BE CONSTRUCTED AS SHOWN IN FIGURE 1.
- 2 LACE OR SPOT TIE AS REQUIRED TO MAINTAIN CABLE SHAPE AND DIMENSIONS.
- 3 ALL CONDUCTORS SHALL BE STRANDED COPPER UNLESS OTHERWISE SPECIFIED.
- 4 WIRE GAUGE AND COLOR SHALL BE AS SPECIFIED IN WIRING DIAGRAM OR CONNECTION LIST.
- 5 WIRING SHALL BE PER WIRING DIAGRAM OR CONNECTION LIST AND 100% INSPECTED.
- 6 ALL WIRE TERMINATIONS SHALL PROVIDE INSULATION SUPPORT
- 7 ALL CONNECTORS TO PROVIDE INSULATION SUPORT
- 8 UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN INCHES.
- 9 UNLESS OTHERWISE SPECIFIED, TOLERANCE: FOR ALL DIMENSIONS IS [+/-0.50]
- 10 LIMITING VALUES: RATED VOLTAGE FOR CABLE ASSEMBLY = 300 VAC MIN.
- 11 MARK CONNECTORS AS SHOWN IN FIGURE 1
- 12 MATERIAL LIST

ITEM	PART NO. (FOR REF. ONLY)	VENDOR PART NO. (OR EQUIVALENT)	DESCRIPTION	QTY
P1	19A116659P14 ----	MOLEX Pt No 09-50-3031	3 POSITION MOLDED HOUSING -----	1
P8	19A116659P14 ----	MOLEX Pt No 09-50-3031	3 POSITION MOLDED HOUSING -----	1
1	19A116781P5 ----	MOLEX Pt No 08-50-0106 OR MOLEX Pt No 08-50-0105	LOOSE PIECE CONTACT OR CHAINED PIECE CONTACT	4

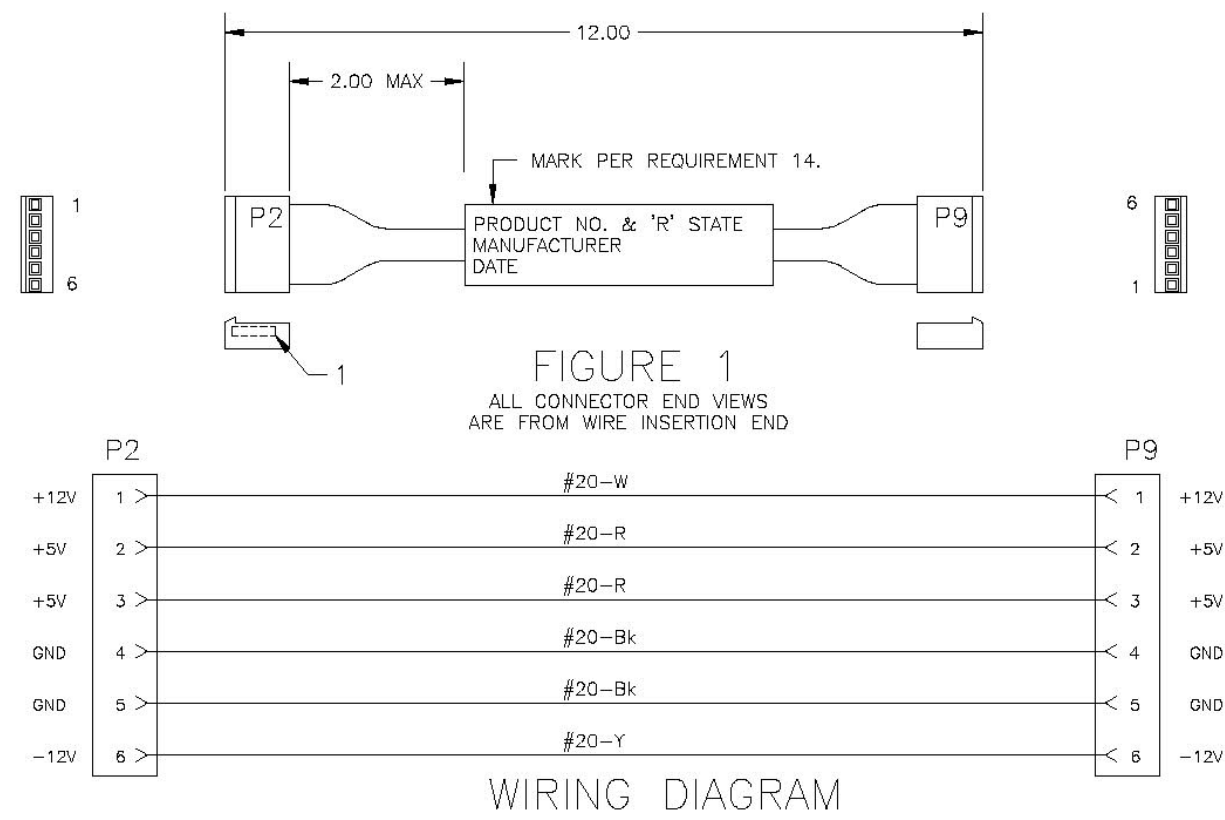
- 13 ENVIRONMENTAL RESISTANCE: OPERATING TEMPERATURE RANGE -20 TO +75 DEG. C
MAX CONTINUOUS TEMPERATURE +75 DEG. C
- 14 MARKING - EACH ASSEMBLY AND ITS PACKING CONTAINER
SHALL BE MARKED WITH THE FOLLOWING INFORMATION

PRODUCT CODE - (SEE REQUIREMENT 15 "PRODUCT LIST")
'R' STATE PER DOCUMENT 1095
MANUFACTURERS NAME OR TRADEMARK.
DATE OF MANUFACTURE CODE

THE MARKING SHALL BE RESISTANT TO MECHANICAL WEARING THAT CAN ARISE
AT NORMAL HANDLING, STORAGE AND OPERATION.

PRODUCT LIST	
PRODUCT NUMBER	DESCRIPTION
CA101211V1	13.8 VOLT INPUT POWER CABLE

18.3 POWER CABLE (W2) (Output) CA101212V1



REQUIREMENTS ("*" INDICATES REQUIREMENT NOT APPLICABLE TO THIS PRODUCT)

- 1 CABLE ASSEMBLY SHALL BE CONSTRUCTED AS SHOWN IN FIGURE 1.
- 2 LACE OR SPOT TIE AS REQUIRED TO MAINTAIN CABLE SHAPE AND DIMENSIONS.
- 3 ALL CONDUCTORS SHALL BE STRANDED COPPER UNLESS OTHERWISE SPECIFIED.
- 4 WIRE GAUGE AND COLOR SHALL BE AS SPECIFIED IN WIRING DIAGRAM OR CONNECTION LIST.
- 5 WIRING SHALL BE PER WIRING DIAGRAM OR CONNECTION LIST AND 100% INSPECTED.
- 6 ALL WIRE TERMINATIONS SHALL PROVIDE INSULATION SUPPORT
- 7 ALL CONNECTORS TO PROVIDE INSULATION SUPORT
- 8 UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN INCHES.
- 9 UNLESS OTHERWISE SPECIFIED, TOLERANCE: FOR ALL DIMENSIONS IS [+/-0.50]
- 10 LIMITING VALUES: RATED VOLTAGE FOR CABLE ASSEMBLY = 300 VAC MIN.
- 11 MARK CONNECTORS AS SHOWN IN FIGURE 1
- 12 MATERIAL LIST

ITEM	PART NO. (FOR REF. ONLY)	VENDOR PART NO. (OR EQUIVALENT)	DESCRIPTION	QTY
P2	19A116659P19 ----	MOLEX Pt No 09-50-3061	3 POSITION MOLDED HOUSING -----	1
P9	19A116659P19 ----	MOLEX Pt No 09-50-3061	3 POSITION MOLDED HOUSING -----	1
1	19A116781P5 ---- OR 19A116781P3	MOLEX Pt No 08-50-0106 OR MOLEX Pt No 08-50-0105	LOOSE PIECE CONTACT OR CHAINED PIECE CONTACT	12

- 13 ENVIRONMENTAL RESISTANCE: OPERATING TEMPERATURE RANGE -20 TO +75 DEG. C
MAX CONTINUOUS TEMPERATURE +75 DEG. C
- 14 MARKING - EACH ASSEMBLY AND ITS PACKING CONTAINER
SHALL BE MARKED WITH THE FOLLOWING INFORMATION

PRODUCT CODE - (SEE REQUIREMENT 15 "PRODUCT LIST")
'R' STATE PER DOCUMENT 1095
MANUFACTURERS NAME OR TRADEMARK.
DATE OF MANUFACTURE CODE

THE MARKING SHALL BE RESISTANT TO MECHANICAL WEARING THAT CAN ARISE
AT NORMAL HANDLING, STORAGE AND OPERATION.

15	PRODUCT LIST	
	PRODUCT NUMBER	DESCRIPTION
	CA101212V1	12 & 5 VOLT OUTPUT POWER CABLE

18.4 ETHERNET CABLES CV101301V1 THRU V8

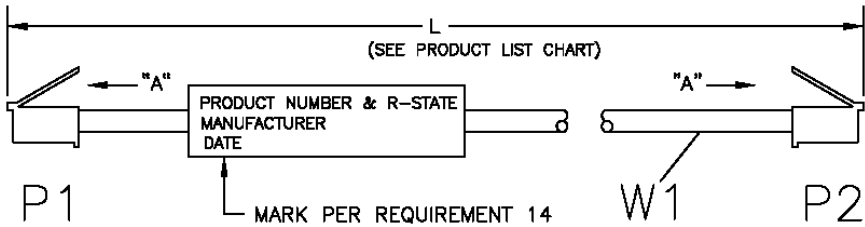
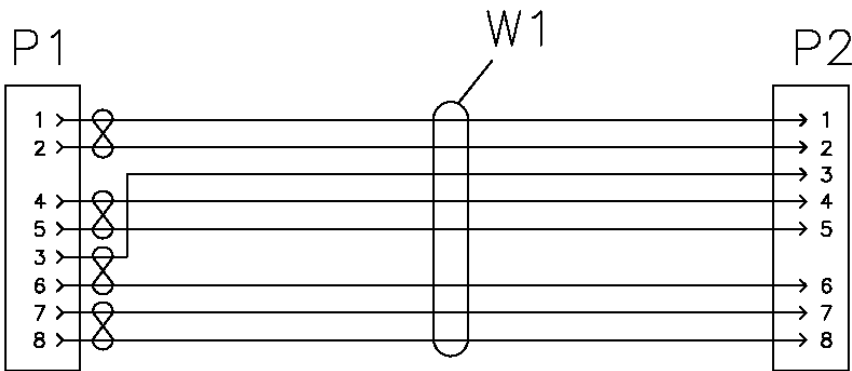
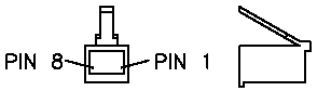


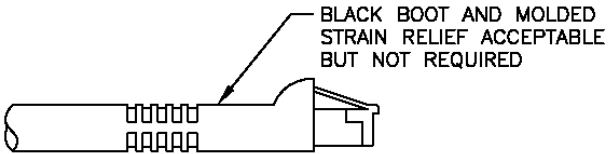
FIGURE 1



WIRING DIAGRAM



VIEW "A"
WIRING SIDE SHOWN



ALTERNATE CONSTRUCTION
FOR P1 AND P2

REQUIREMENTS (*" INDICATES REQUIREMENT NOT APPLICABLE TO THIS PRODUCT)

- 1 CABLE ASSEMBLY SHALL BE CONSTRUCTED AS SHOWN IN FIGURE 1.
- 2 LACE OR SPOT TIE AS REQUIRED TO MAINTAIN CABLE SHAPE AND DIMENSIONS.
- 3 ALL CONDUCTORS SHALL BE STRANDED COPPER UNLESS OTHERWISE SPECIFIED.
- 4 WIRE GAUGE AND COLOR SHALL BE AS SPECIFIED IN WIRING DIAGRAM OR CONNECTION LIST.
- 5 WIRING SHALL BE PER WIRING DIAGRAM OR CONNECTION LIST AND 100% INSPECTED.
- 6 ALL WIRE TERMINATIONS SHALL PROVIDE INSULATION SUPPORT
- 7 ALL CONNECTORS TO PROVIDE INSULATION SUPPORT
- 8 UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN INCHES.
- 9 UNLESS OTHERWISE SPECIFIED, TOLERANCE: FOR ALL DIMENSIONS IS [+/-0.50]
- 10 LIMITING VALUES: RATED VOLTAGE FOR CABLE ASSEMBLY = 300 VAC MIN.
- 11 MARK CONNECTORS AS SHOWN IN FIGURE 1
- 12 MATERIAL LIST

ITEM	PART NO. (FOR REF. ONLY)	VENDOR PART NO. (OR EQUIVALENT)	DESCRIPTION	QTY
P1	-----	AMP #5-558530-2	8 POSITION MODULAR CONNECTOR	1
P2	-----	AMP #5-558530-2	8 POSITION MODULAR CONNECTOR	1
W1	-----	AMP #558573-1	QTY 4 TWISTED PAIR, 24 AWG STRANDED 7/32, POLYETHYLENE INSULATED PRIMARIES WITH PVC JACKET CAT 5 PER TIA/EIA 568A COLOR: SUPPLIERS DISCRETION	PER "L" DIM IN PRODUCT LIST CHART

- 13 ENVIRONMENTAL RESISTANCE: OPERATING TEMPERATURE RANGE -20 TO +70 DEG. C
MAX CONTINUOUS TEMPERATURE +70 DEG. C
- 14 MARKING - EACH ASSEMBLY AND ITS PACKING CONTAINER
SHALL BE MARKED WITH THE FOLLOWING INFORMATION

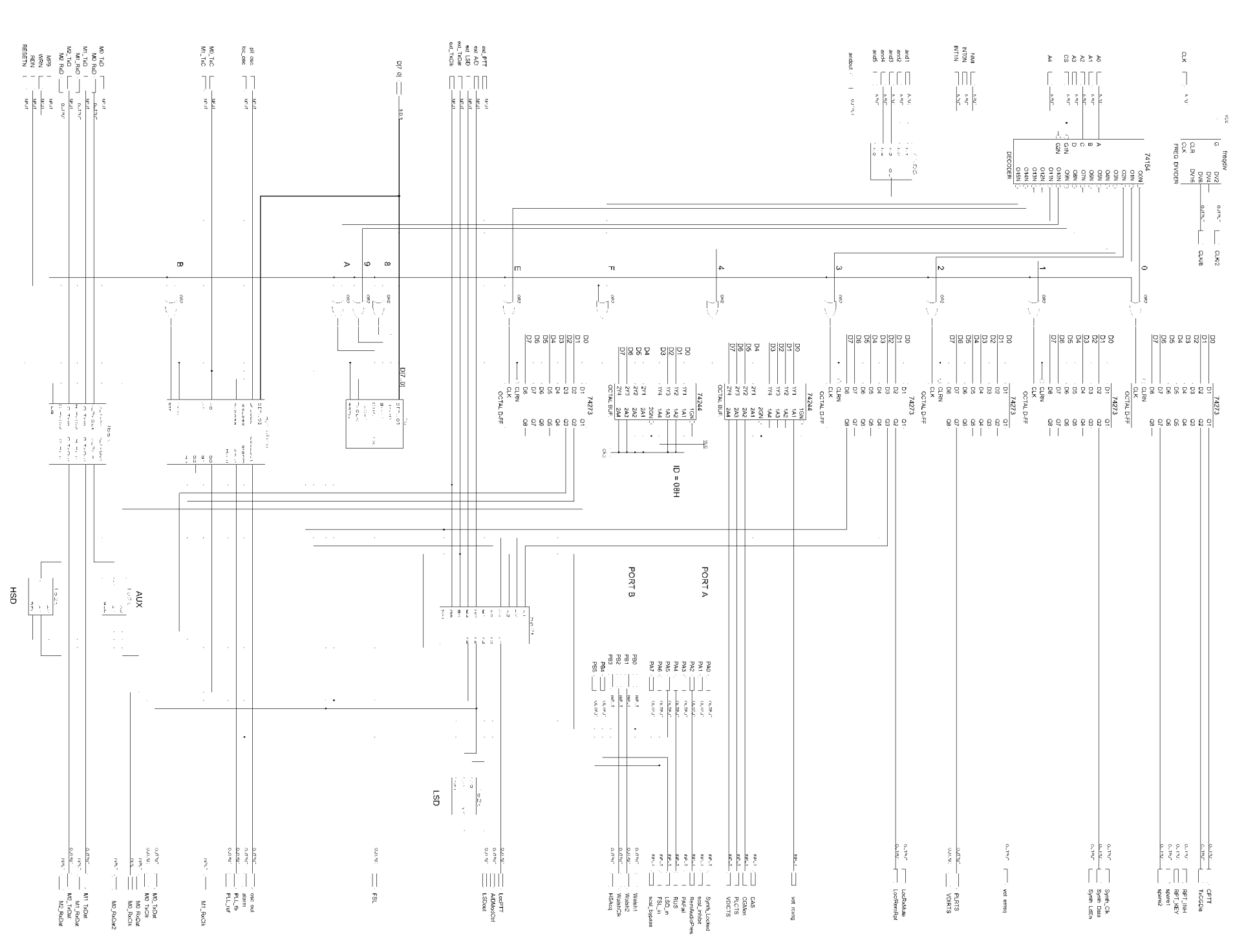
PRODUCT CODE - (SEE REQUIREMENT 15 "PRODUCT LIST")
"R" STATE PER DOCUMENT 1095
MANUFACTURERS NAME OR TRADEMARK.
DATE OF MANUFACTURE CODE

THE MARKING SHALL BE RESISTANT TO MECHANICAL WEARING THAT CAN ARISE
AT NORMAL HANDLING, STORAGE AND OPERATION.

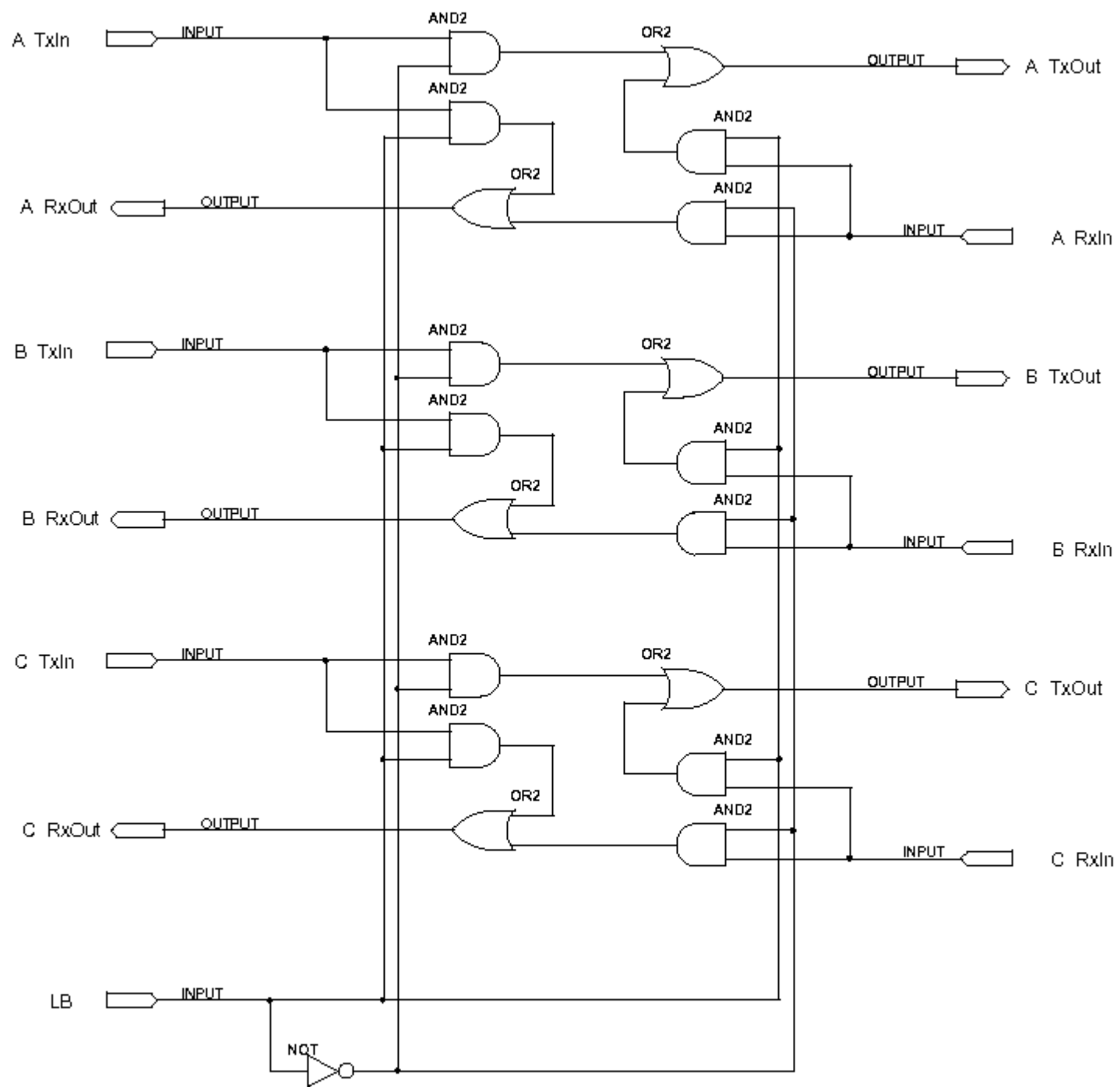
PRODUCT LIST		
PRODUCT NUMBER	DESCRIPTION AND 'L' DIM	SIMILAR TO AMP PT NO OR EQUIV
CA101301V2	CABLE, ETHERNET, 'L' DIM = 2 FEET	406483-2
CA101301V4	CABLE, ETHERNET, 'L' DIM = 4 FEET	406483-4
CA101301V5	CABLE, ETHERNET, 'L' DIM = 5 FEET	406483-5
CA101301V6	CABLE, ETHERNET, 'L' DIM = 6 FEET	406483-6
CA101301V8	CABLE, ETHERNET, 'L' DIM = 8 FEET	406483-8
CA101301V10	CABLE, ETHERNET, 'L' DIM = 10 FEET	1-406483-0
CA101301V15	CABLE, ETHERNET, 'L' DIM = 15 FEET	1-406483-5
CA101301V20	CABLE, ETHERNET, 'L' DIM = 20 FEET	2-406483-0
CA101301V25	CABLE, ETHERNET, 'L' DIM = 25 FEET	2-406483-5

19.0 EPLD DRAWINGS

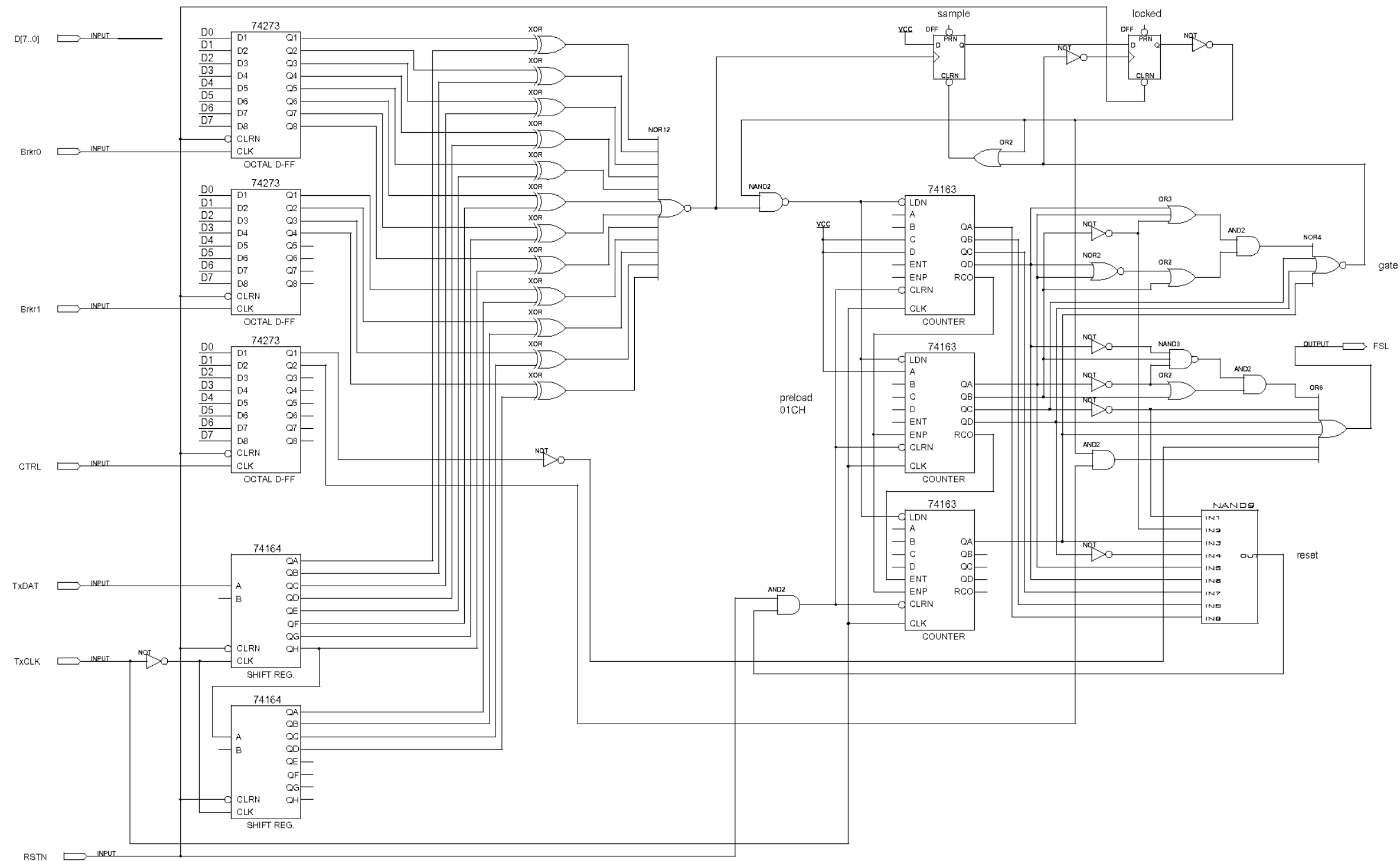
19.1 SITEPRO TOP LEVEL



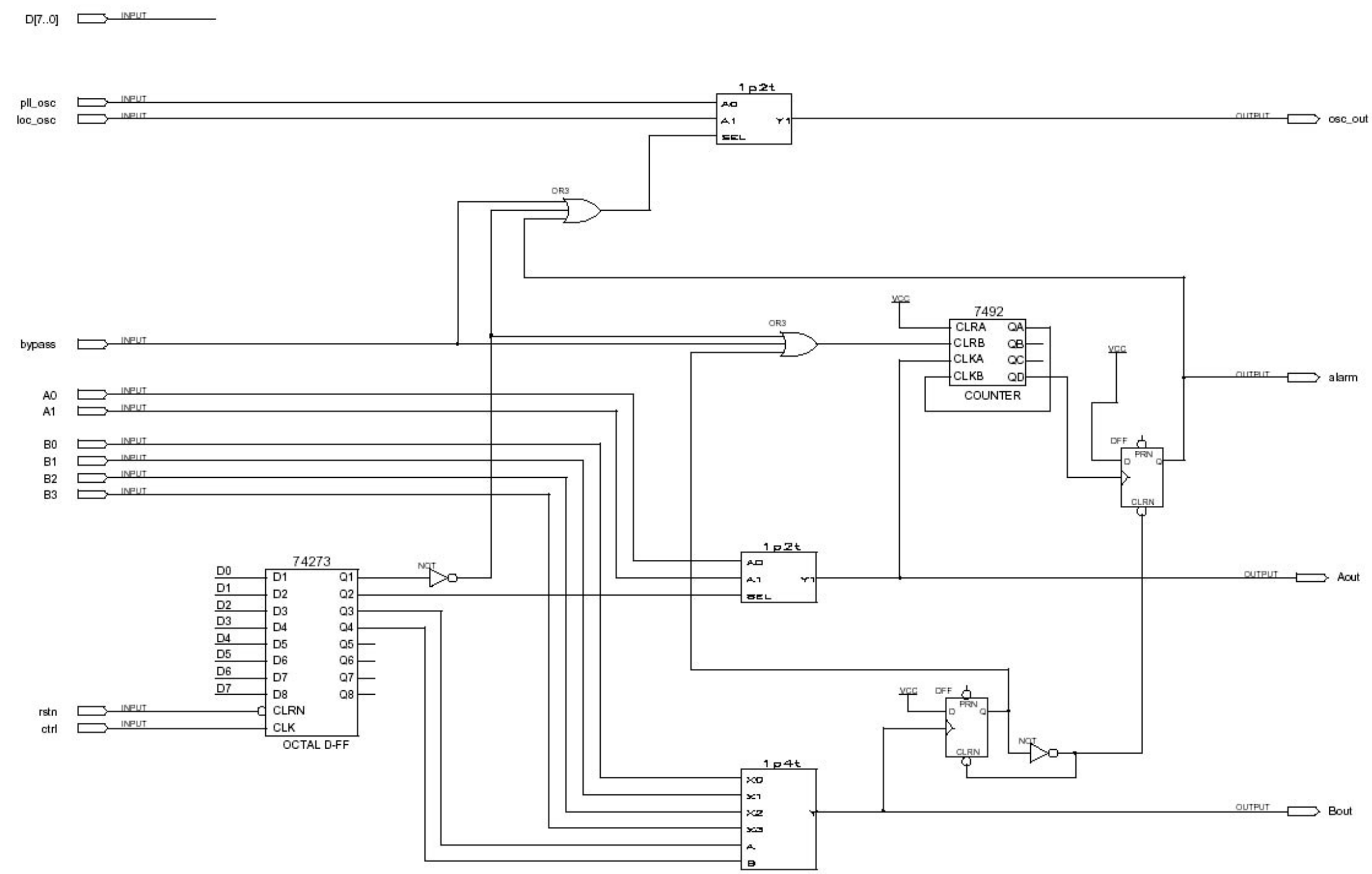
19.2 LBSW



19.3 FSL



19.4 SYNCHRO



19.5 PIN NAMES

PLD PIN NAME	PIN TYPE	PLD PIN NUMBER	SIGNAL NAME
CLK	IN	125	29.4912MHZ
CLK/2	OUT	22	14.7456MHZ
CLK/8	OUT	21	3.6864MHZ
A0	IN	11	A31
A1	IN	10	A30
A2	IN	9	A29
A3	IN	8	A28
CS	IN	126	CS-N
A4	IN	7	A27
NMI	IN	14	NMI-N
INT0N	IN	15	INTRO-N
INT1N	IN	16	INTR1-N
and1	IN	39	CSIN-N2
and2	IN	40	CSIN-N3
and3	IN	42	CSIN-N5
and4	IN	43	CSIN-N6
and5	IN	44	CSIN-N7
andout	OUT	46	XCEIVER-OE-N
ext_PTT	IN	67	EXTPTTIN
ext_A/D	IN	60	EXTADIN
ext_LSD	IN	68	EXT150IN
ext_TxDat	IN	66	EXT9.6IN
ext_TxCIk	IN	69	9.6REFIN
Pll_osc	IN	48	PLL-11MHz
Loc_osc	IN	49	OSC-11MHz
M0_TxC	IN	128	RFTXCLK
M1_TxC	IN	32	PLTXCLK
M0_TxD	IN	122	RFTXD
M0_RxD	OUT	99	RFRXD
M1_TxD	IN	31	PLTXD
M1_RxD	OUT	34	PLRXD
M2_TxD	IN	35	VDITXD
M2_RxD (OUTPUT	OUT	55	VDIRXD
M9	IN	12	WE-N
WRN	IN	19	MP9
RDN	IN	18	IORD-N
RESETN	IN	127	RESET-N
CPTT	OUT	28	CPTTOUT
TxCGDis	OUT	70	TXTCGDIS
RPT_INH	OUT	103	RPT-INH
RPT_KEY	OUT	111	RPTKEY
Spare 1	OUT	100	SPARE1
Spare 2	OUT	101	SPARE2
Synth_Clk	OUT	87	SYNTH-CLK
Synth_Data	OUT	88	SYNTH-DATA

PLD PIN NAME	PIN TYPE	PLD PIN NUMBER	SIGNAL NAME
Synth_LdEn	OUT	102	SYNTH-LD-EN
Vot_emsq	OUT	97	EMSQTOAV
PLRTS	OUT	65	PLRTS
VDIRTS	OUT	63	VDIRTS
LocRxMute	OUT	112	RX-MUTE
Loc/RemRpt	IN	96	REM-RPT
vot_rcvng	IN	119	RCVNG-FROM-AV
CAS	IN	108	CAS
CGMon	IN	107	CGMON
PLCTS	IN	83	PLCTS
VDIRTS	IN	82	VDIRTS
Synth_Locked	IN	118	SYNTH-LOCK-DET
scst_inhibit	IN	117	SIMULCAST-INHIBIT
RemAudioPres	IN	109	REM-AUDIO-PRESENT
PAFail	IN	78	PAFAIL
RUS	IN	75	RUSIN
LSD_in	IN	106	LSDIN
FSL_in	IN	120	FSLIN
scst_bypass	OUT	116	BYPASS
PA0	OUT	134	PLD-IO0
PA1	OUT	136	PLD-IO1
PA2	OUT	137	PLD-IO2
PA3	OUT	138	PLD-IO3
PA4	OUT	142	PLD-IO4
PA5	OUT	143	PLD-IO5
PA6	OUT	1	PLD-IO6
PA7	OUT	2	PLD-IO7
Walsh1	OUT	81	WALSH1
Walsh2	OUT	80	WALSH2
WalshClk	OUT	93	TP9
HSAcq	IN	45	HSACQ
PB0	IN	91	WAL1
PB1	IN	90	WAL2
PB2	IN	92	WALSHCLK
PB3	OUT	86	HSACQCTL
PB4	OUT	98	LSIN
PB5	OUT	84	MODFSL
LocPTT	OUT	71	STNPTT
A/DModCtrl	OUT	72	A/DMODCTL
LSDout	OUT	110	LSDOUT
FSL	OUT	113	FSL
Osc_out	OUT	30	11MHZ
Alarm	OUT	62	TXC-MISSING-ALARM
PLL_fb	OUT	47	9.6KHZ
PLL_ref	IN	56	PLL_REF
M1_RxCIk	OUT	27	PLRXCLK
M0_TxDat	OUT	38	RFTXDAT

PLD PIN NAME	PIN TYPE	PLD PIN NUMBER	SIGNAL NAME
M0_TxCIk	IN	37	RFTXCLK-OUT
M0_RxDat	IN	53	RFTXDAT-FROM-SLICER
M0_RxCIk	IN	36	RFRXCLK
M0_RxDat2	OUT	54	RFRXDAT-FROM-DSP
M1xDat	IN	23	PLTXDAT
M1_RxDat	OUT	25	PLRXDAT
M2_TxDat	IN	79	VDITXDAT
M2_RxDat	IN	61	VDIRXDAT
D7	BIDIR	131	D0
D6	BIDIR	132	D1
D5	BIDIR	133	D2
D4	BIDIR	139	D3
D3	BIDIR	140	D4
D2	BIDR	141	D5
D1	BIDIR	5	D6
D0	BIDIR	6	D7
JTAG-TDI	IN	4	EPLD-TDI
JTAG-TMS	IN	20	EPLD-TMS
JTAG-TDO	OUT	104	EPLD-TDO-CHAIN1
JTAG-TCK	IN	89	EPLD-TCK
NC		29	MP8
NC		41	MP10
NC		121	MP30
NC		74	MP29

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