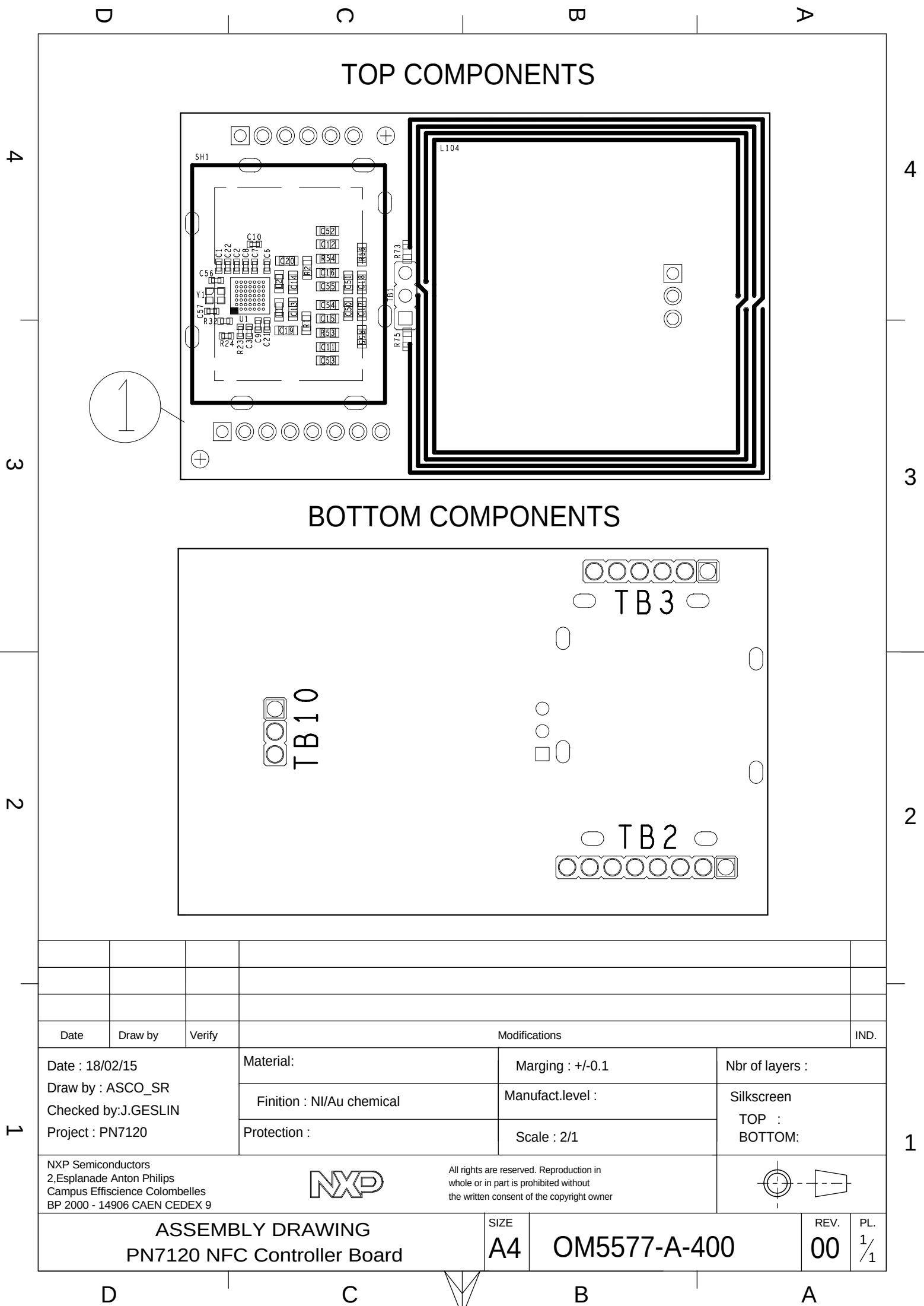




TOP COMPONENTS

BOTTOM COMPONENTS

1

Date	Draw by	Verify	Modifications	IND.
Date : 18/02/15		Material:	Marging : +/-0.1	Nbr of layers :
Draw by : ASCO_SR		Finition : Ni/Au chemical	Manufact.level :	Silkscreen TOP : BOTTOM:
Checked by: J.GESLIN		Protection :	Scale : 2/1	
Project : PN7120				
NXP Semiconductors 2, Esplanade Anton Philips Campus Effiscience Colombelles BP 2000 - 14906 CAEN CEDEX 9			All rights are reserved. Reproduction in whole or in part is prohibited without the written consent of the copyright owner	
ASSEMBLY DRAWING PN7120 NFC Controller Board			SIZE A4	OM5577-A-400
			REV. 00	PL. 1/1

