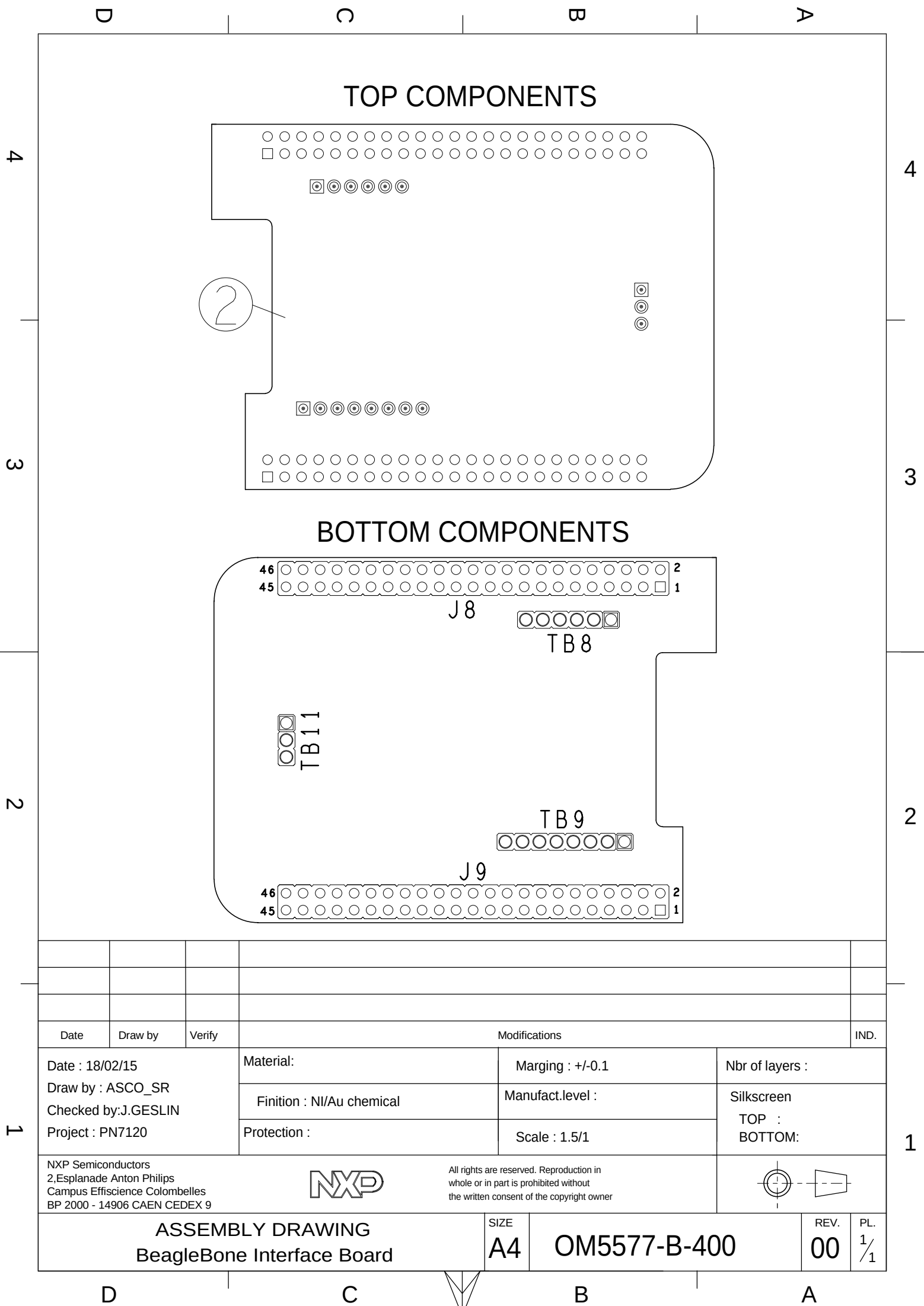




Date	Draw by	Verify	Modifications	IND.
Date : 18/02/15 Draw by : ASCO_SR Checked by: J.GESLIN Project : PN7120		Material:	Marging : +/-0.1	Nbr of layers :
		Finition : NI/Au chemical	Manufact.level :	Silkscreen
		Protection :	Scale : 1.5/1	TOP : BOTTOM:
NXP Semiconductors 2, Esplanade Anton Philips Campus Effiscience Colombelles BP 2000 - 14906 CAEN CEDEX 9			All rights are reserved. Reproduction in whole or in part is prohibited without the written consent of the copyright owner	
ASSEMBLY DRAWING BeagleBone Interface Board			SIZE A4	OM5577-B-400
			REV. 00	PL. 1/1

