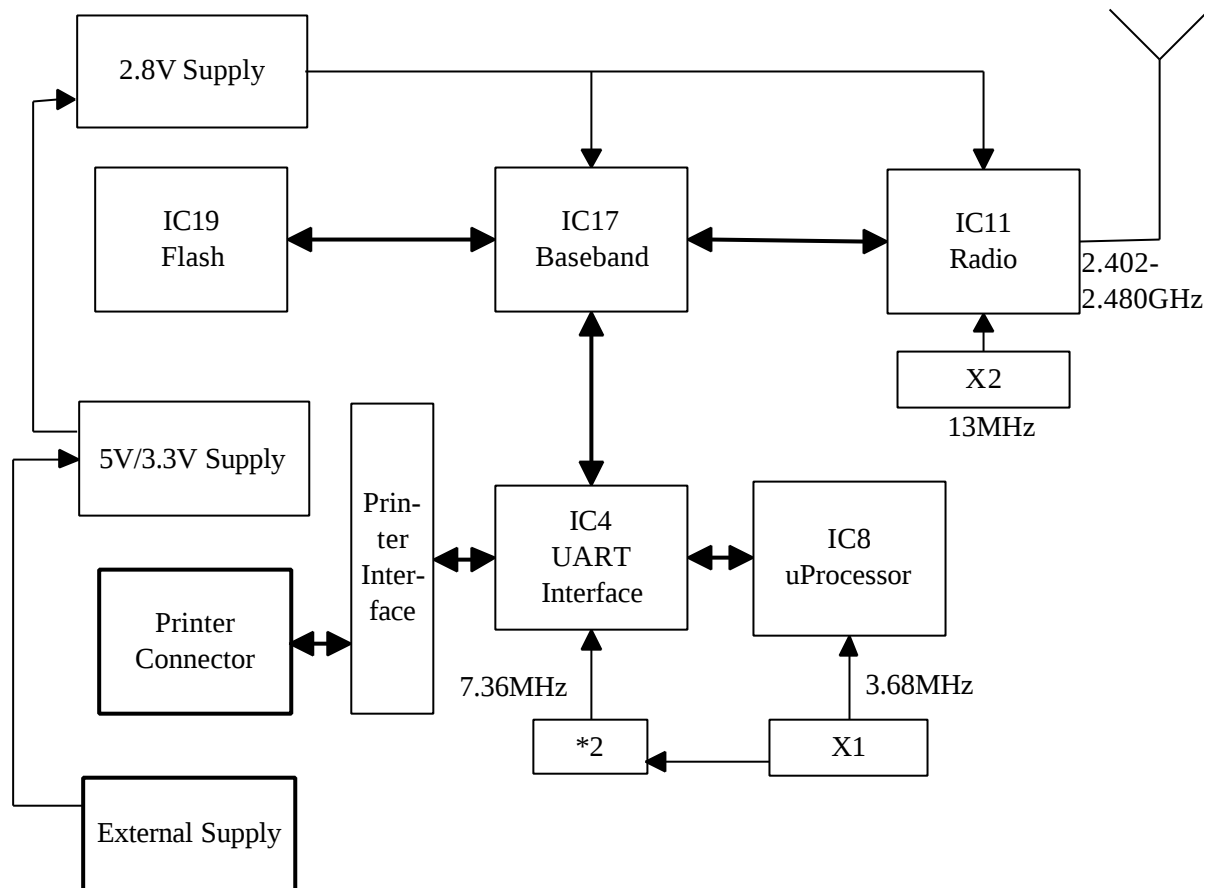


Uppgjord (även faktaansvarig om annan) - WSS Tomas Blom		Nr - No 1551-SPB030	
Dokansv/Godk - Doc respons/Approved WSS Tomas Blom	Kontr - Checked	Datum - Date 2001-02-28	Rev A
		File 1551_SPB030_B	

1 Block Diagram



2 Circuit Description

There is a 3.68 MHz clock generated by IC8 and X1. This clock is used by CPU IC8. This clock is frequency doubled in IC3 for use also as baudrate clock by UART IC4, max UART speed is 460 kbps.

The radio IC11 is using the 13MHZ reference X2.

Power to the card is supplied at Power Connector P3. It is regulated in IC6 to 5.0 volts and in IC7 to 3.3 volts.

IC18 regulates from 3.3 to 2.8 volts for the BT-radio parts IC11, IC17 and IC19.

IC18 is shut on/off under program control via signal BTON.

Power to Printer Interface circuits IC1-3 is fed via a diode in D3 from 5 volt, (=IC6 out)

There is a CPU, IC8, with an external UART, IC4, as link to the BT-parts.

The antenna is a standing PIFA (Planar Inverted F Antenna) type antenna with 0dBi gain.