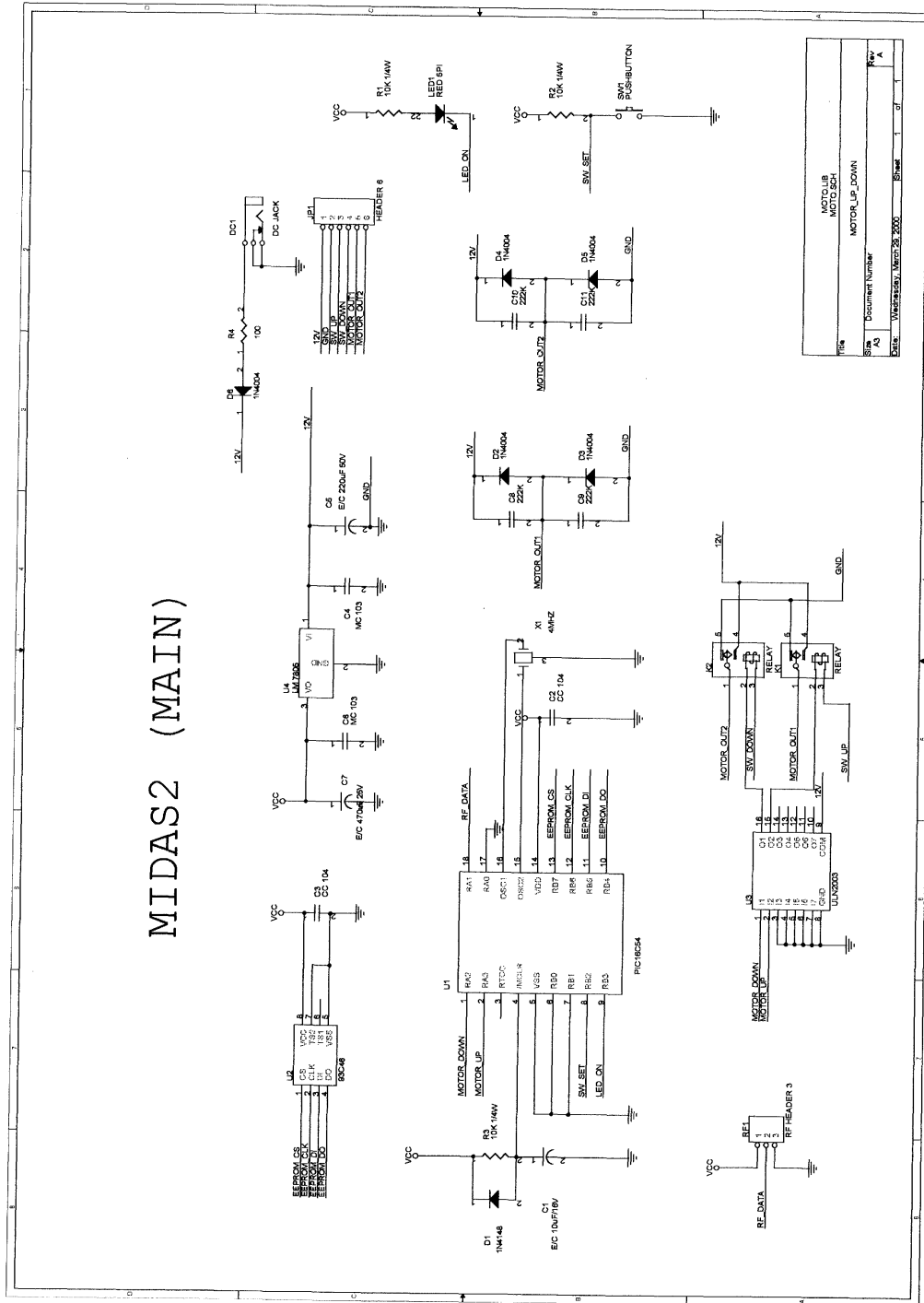


CIRCUIT

MIDAS2 (MAIN)



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MIDAS2 (RF MODULE)

The schematic diagram illustrates the MIDAS2 (RF MODULE) circuit. It begins with an ANTENNA input connected to a matching network consisting of capacitor C1 (10pF) and inductor L1 (15uH). This network is followed by a series of components including capacitor C2 (10pF), inductor L2 (10uH), and a 5V regulator (IC1). The circuit then branches into two parallel paths, each containing a 10uH inductor (L3 and L4) and a 10K resistor (R1 and R2). These paths lead to two LM2004 operational amplifiers (IC1A and IC1B) configured as comparators. The comparators are biased using a 5V supply and a 10K resistor (R3). The output of the comparators is connected to a HEADER 2, which is also connected to a 10K resistor (R4) and a 10uH inductor (L5). The circuit is powered by a 5V supply (VCC) and includes various passive components for signal conditioning and biasing, such as capacitors C3 through C15 and resistors R5 through R18.

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