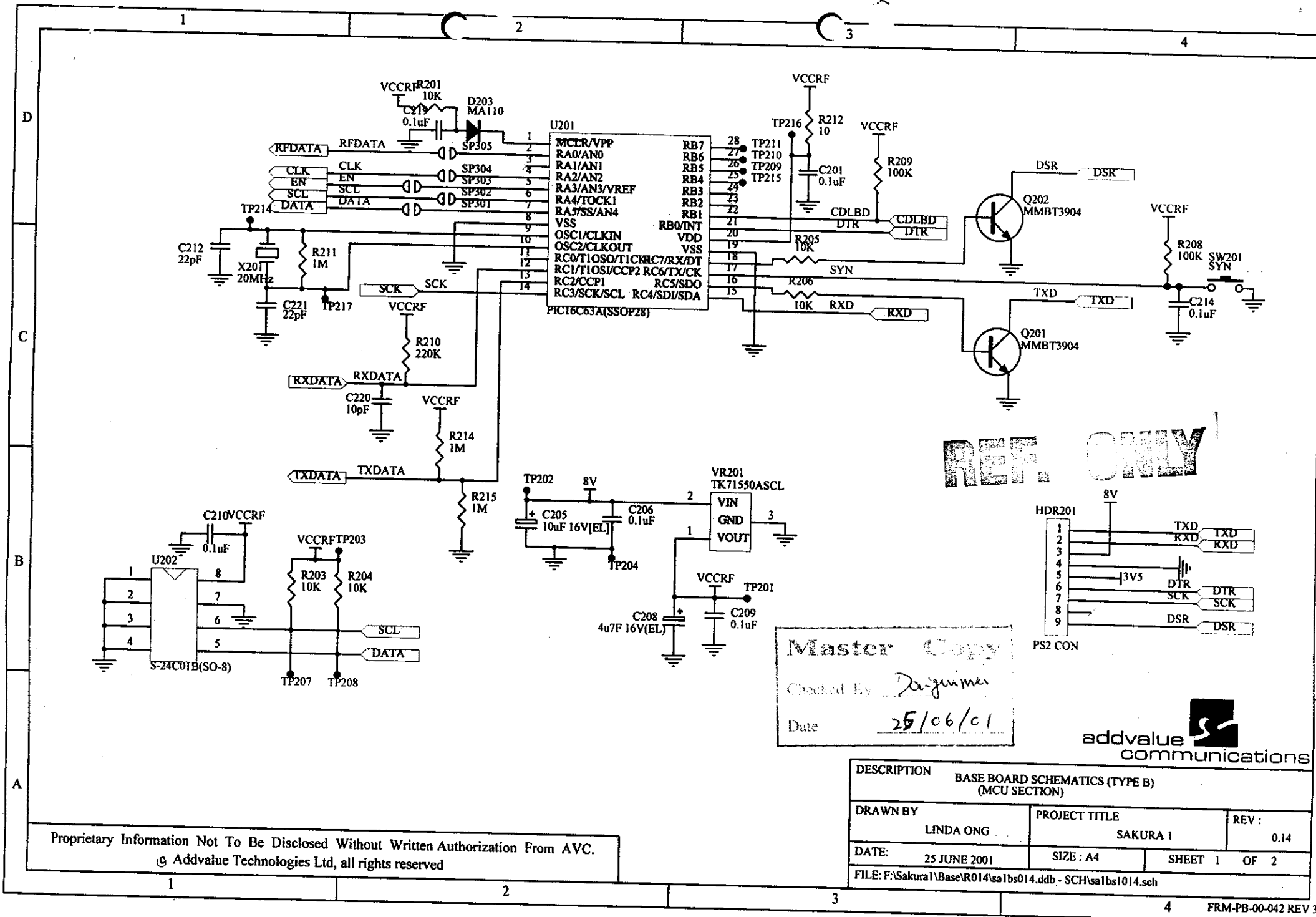




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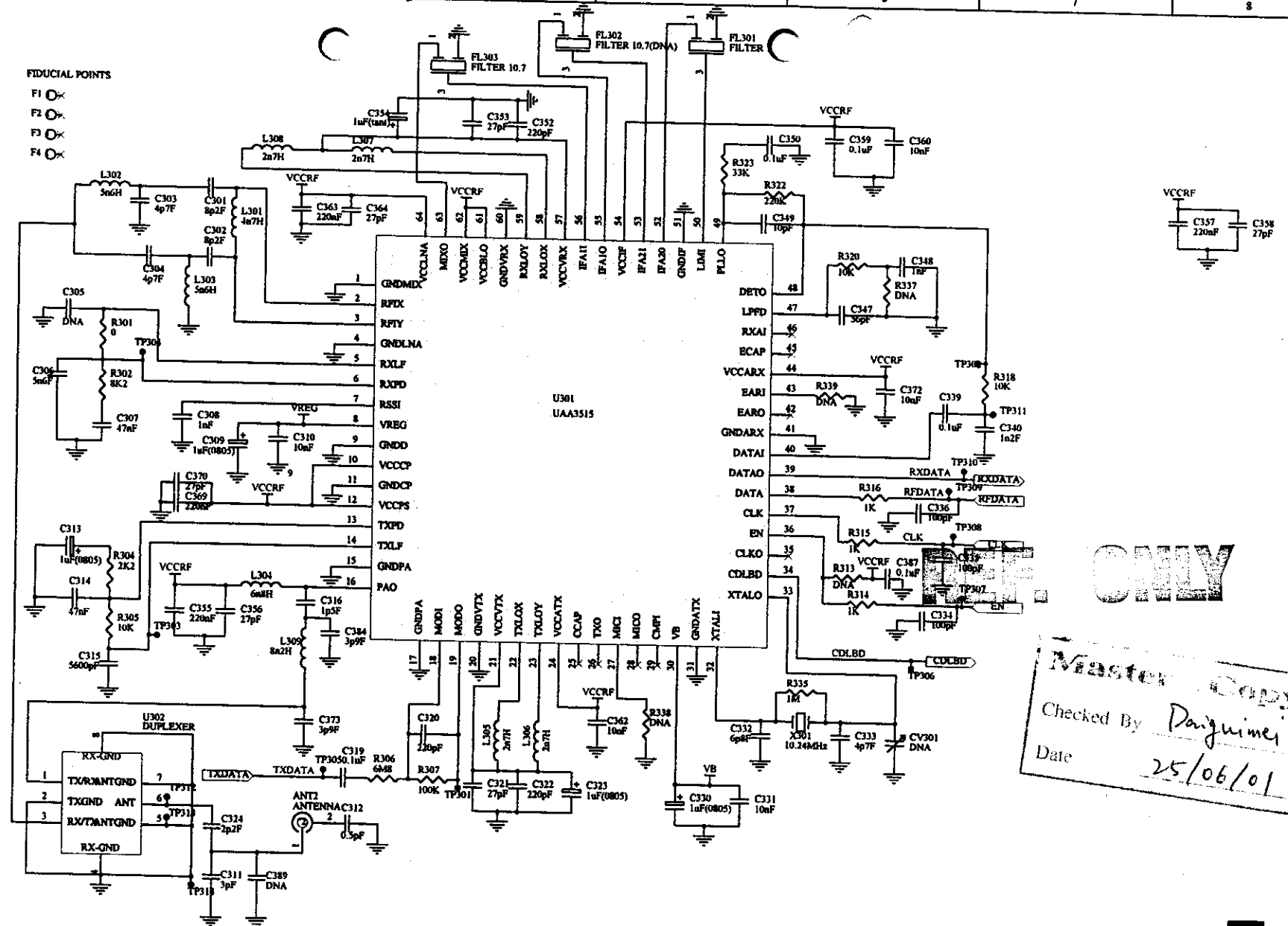
Master Copy
 Checked By *Darguimer*
 Date *25/06/01*

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DESCRIPTION			
BASE BOARD SCHEMATICS (TYPE B) (MCU SECTION)			
DRAWN BY	LINDA ONG	PROJECT TITLE	SAKURA 1
DATE:	25 JUNE 2001	SIZE : A4	SHEET 1 OF 2
FILE: F:\Sakura\1\Base\R014\sa1bs014.ddb - SCH\sa1bs1014.sch			

FIDUCIAL POINTS

- F1
- F2
- F3
- F4



ONLY

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NOTES:

1. ALL CAPACITORS & RESISTORS USED ARE SMD 0603 UNLESS OTHERWISE SPECIFIED.
2. ALL INDUCTORS AND DIODES USED ARE SMD 0603 UNLESS OTHERWISE SPECIFIED.

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DESCRIPTION		
BASE BOARD SCHEMATICS (TYPE B) (RF SECTION)		
DRAWN BY	PROJECT TITLE	REV :
LINDA ONG	SAKURA I	0.14
DATE: 25 JUNE 2001	SIZE : A3	SHEET 2 OF 2
FILE: F:\Sakura I\Baser\014\sa1ba014.ddb - SCH\sa1ba2014.sch		