

The WA601 circuit description:

The usb flash disk controller AU9384 USB signal powered by the hub AU9254 that is an integrated single chip USB hub controller designed for the emerging industry-standard Universal Serial Bus (USB) .

The AU9254 supports four USB downstream ports. Each downstream port has power switch control, and over-current sensing.

The NO.1 downstream port connected the USB flash disk data signal D+ and d-, by right of this, The usb flash disk controller bridged the flash memory and host USB bus.

The NO.4 downstream port connected the wireless LAN dongle, In Schematic file,the j1 is wireless LAN dongle connector.

From the PC HOST on carrying the copular USB BUS through two electric resistance for matchings R13, R15 links the USB HUB AU9254, among them HUB of next a the first port links the controller of UFD 6685, HUB of next a the fourth port links WIRELESS LAN module, descend a port match the electric resistance is a R16/17/11/12.

USB HUB next an USB for the first with the fourth port linking DEVICE all from the power supply of USB BUS, the controller of UFD manages a NAND FLASH, such as:U3/ U4.(K9F6408/2808 ...) by the I/O BUS of the itself.

The crystal oscillates the machine(Y1) work in the 12 MHZ, for the controller of UFD and standard clock in offering in HUB in USB signal.

The transmitter of the EUT is powered by host equipment. The antenna used in this product is External antenna with URL connector.

The other instruction, please have a look at the users manual.

DSSS Information

This device is Direct Sequence Spread Spectrum, the data is mixed by pseudorandom code which is an orthogonal code. The mixed data is digital modulated by BPSK and QPSK technique depends on the data rates.

The CCK coding is applied for increasing the data rate, and also the processing gain will be increased. The bit rates are 1, 2, 5.5, 11Mbps, the symbol rates are 1, 1, 1.375, 1.375Mbps, and the chip rates are always 11Mbps.

So, the Chip/symbol is 11, 11, 8 and 8 respectively. Although is higher bit rate, the processing gain is lower than 10, but the CCK coding used in higher bit rate will provide 2.2dB coding gain.