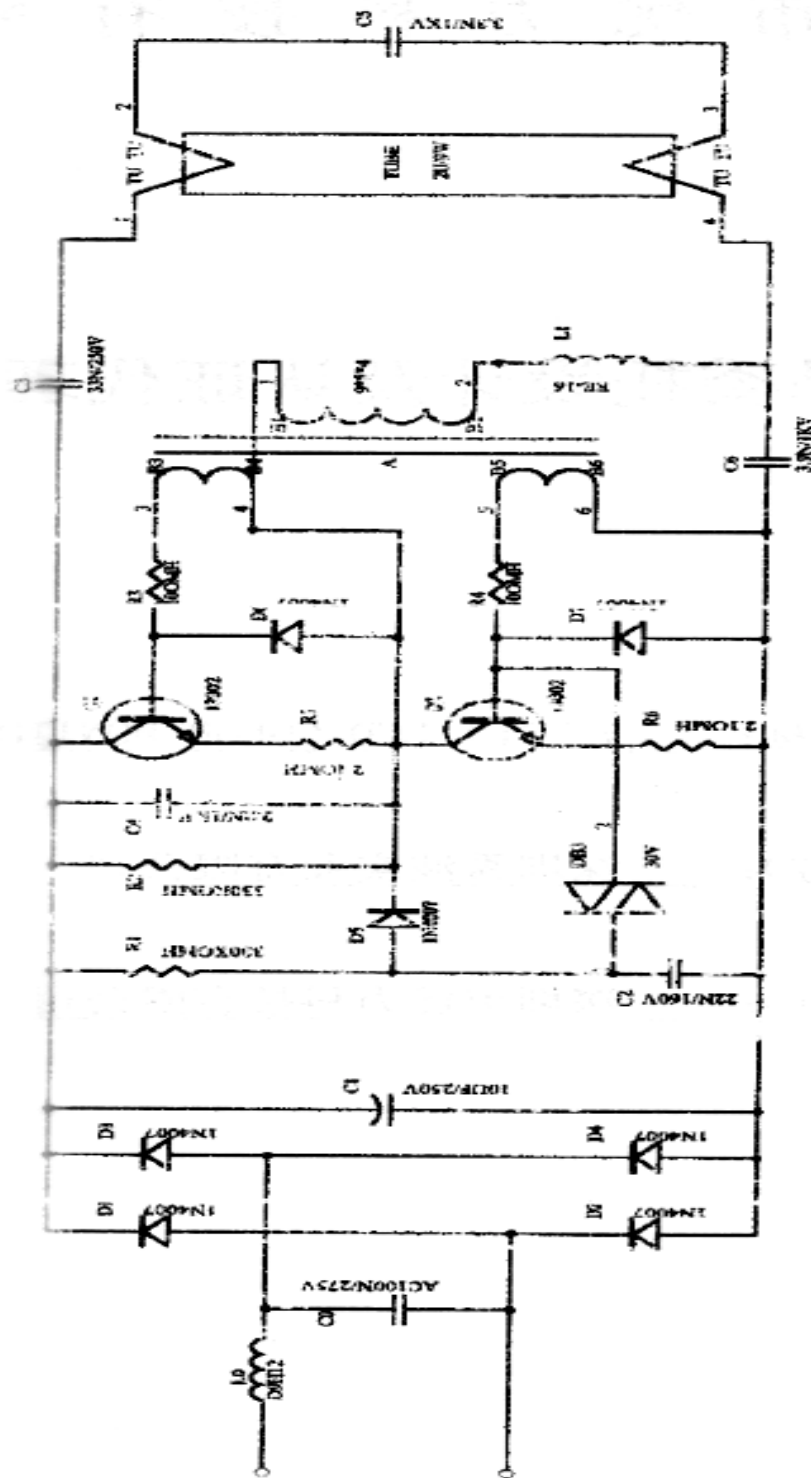
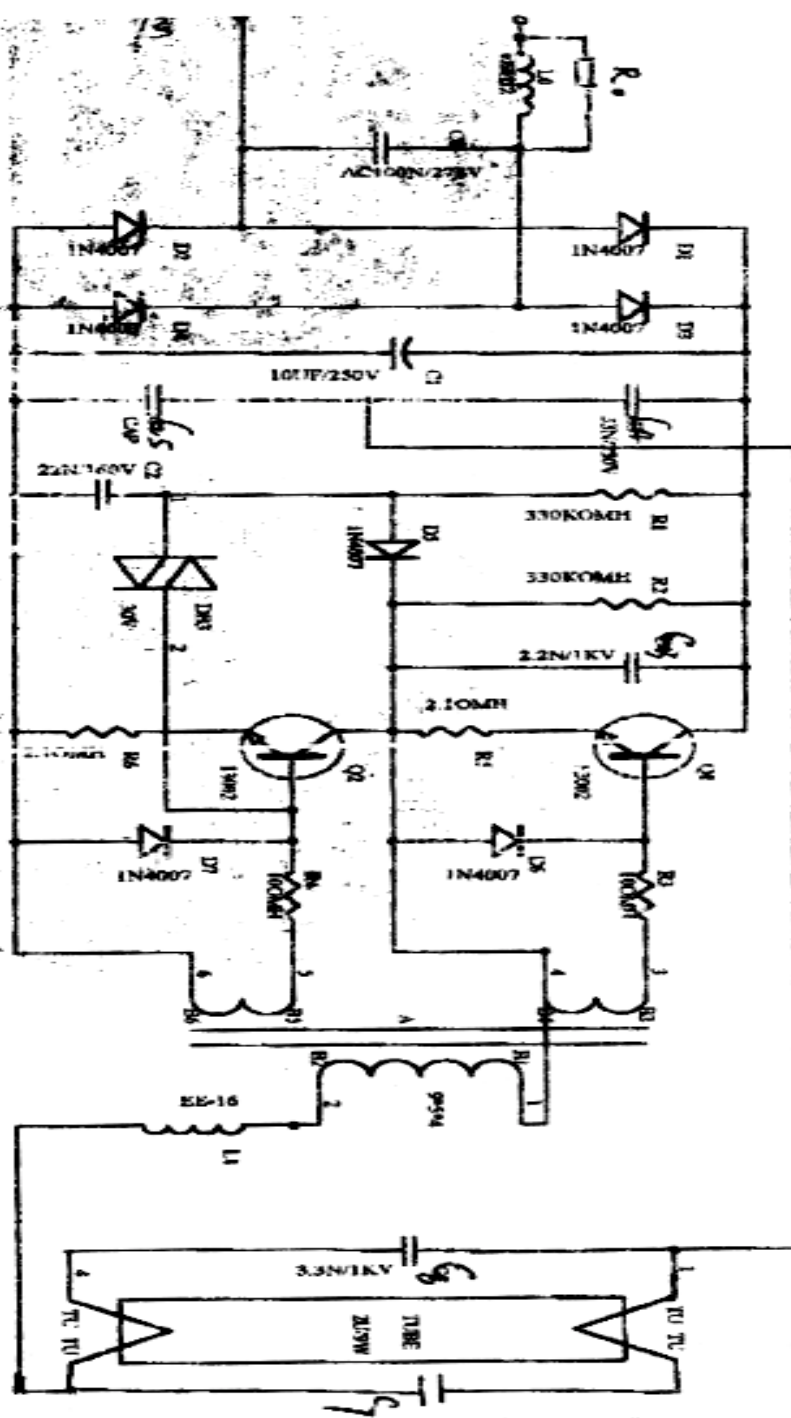
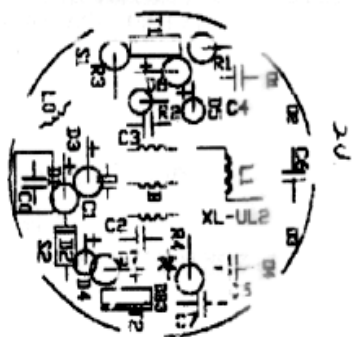


EXHIBIT 3

EUT Configuration Block Diagram Circuit Diagram







30. 畢沅