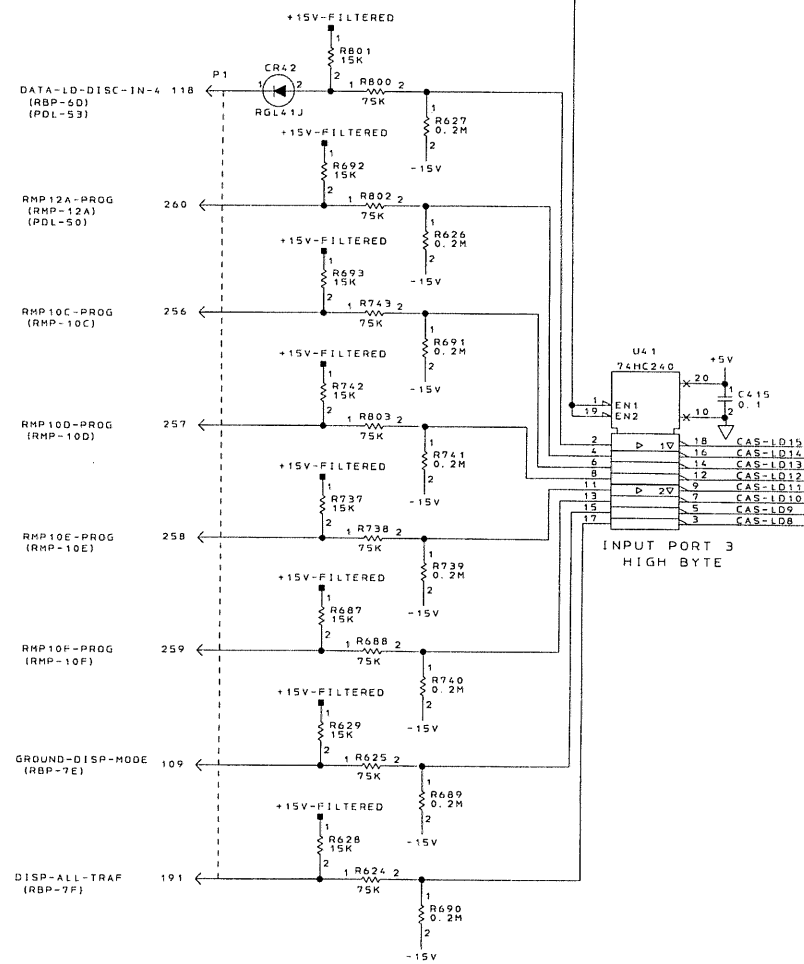
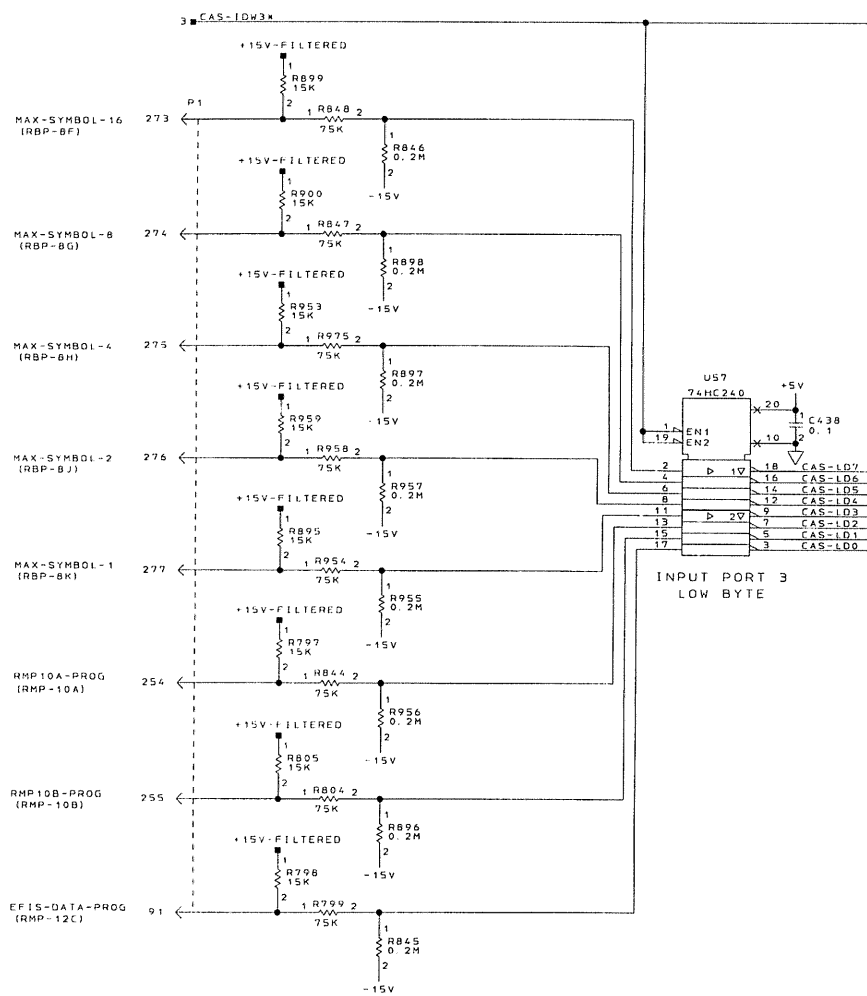




CAS-LD15:01

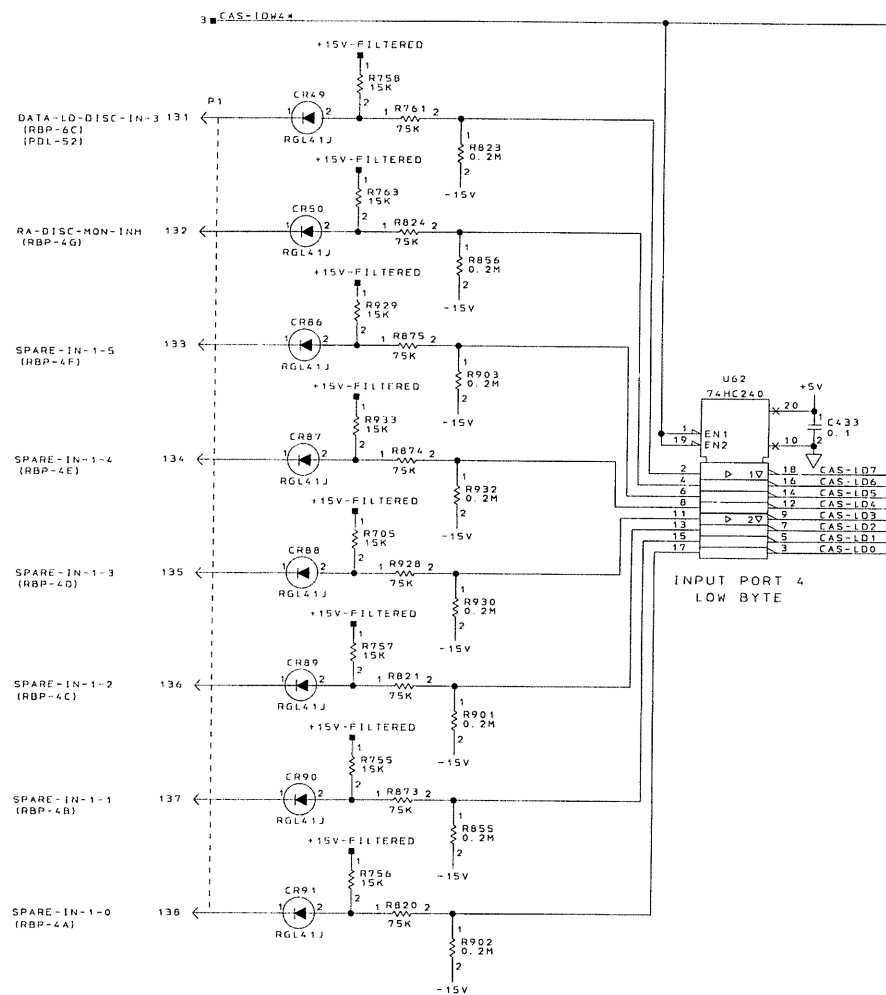
3-6, 8-11, 13-16, 22, 25

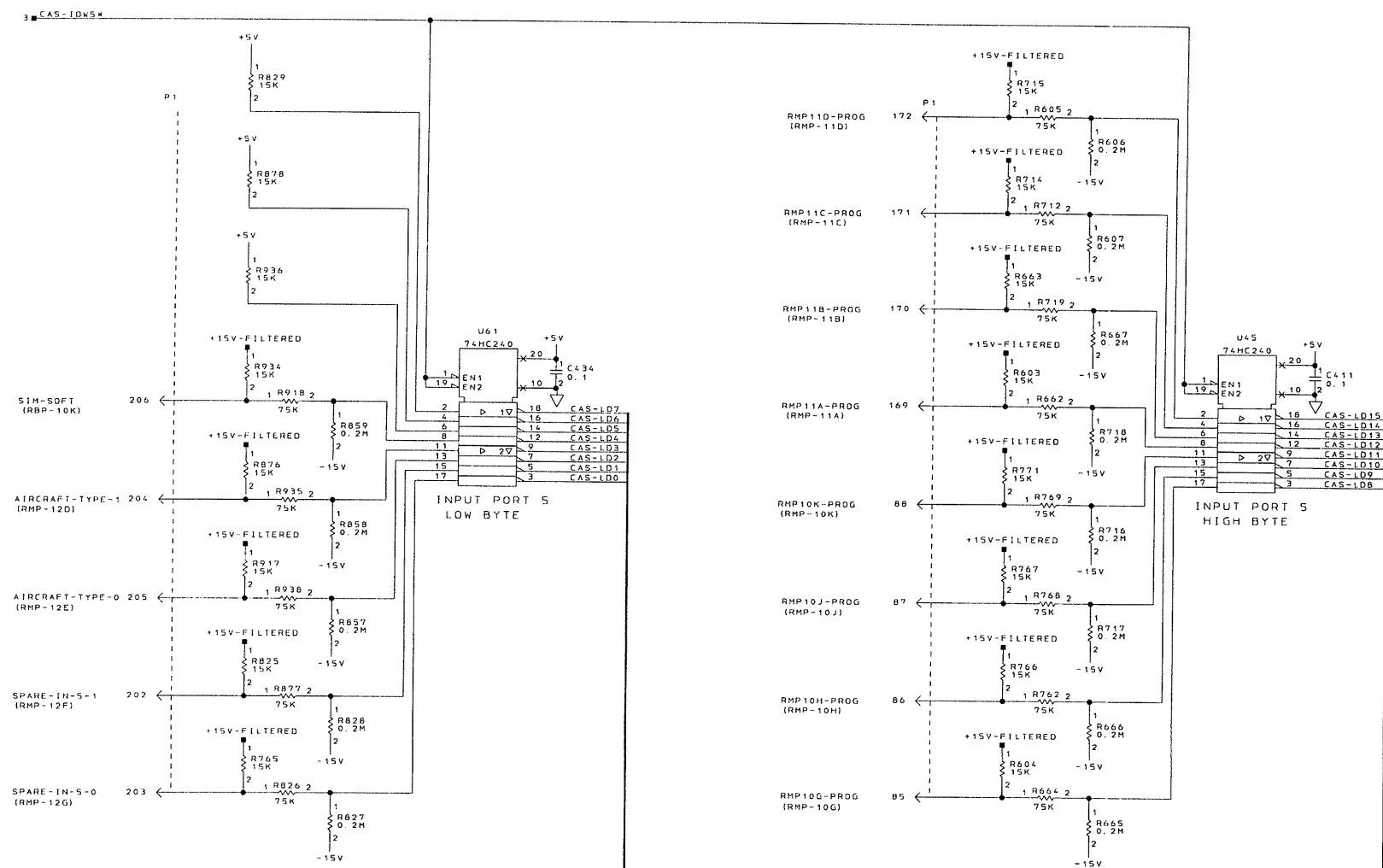
CAS CPU DISCRETE INPUTS

A2 PROCESSOR CCA

SCHEMATIC DIAGRAM SHEET NO. 12

COMPUTER GENERATED





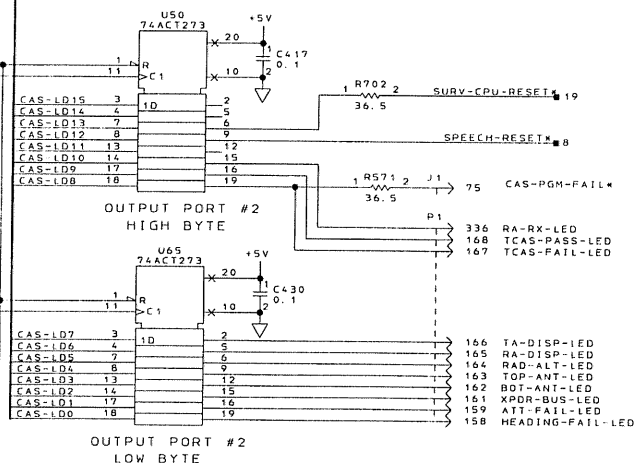
CAS-LD(15:0)
3-6, 8-13, 15, 16, 22, 25

CAS CPU DISCRETE INPUTS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 14

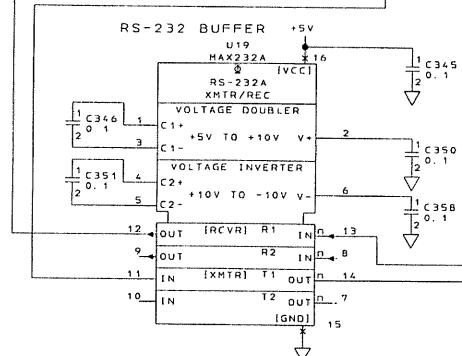
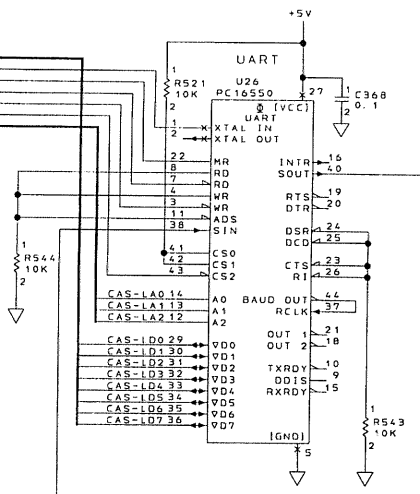
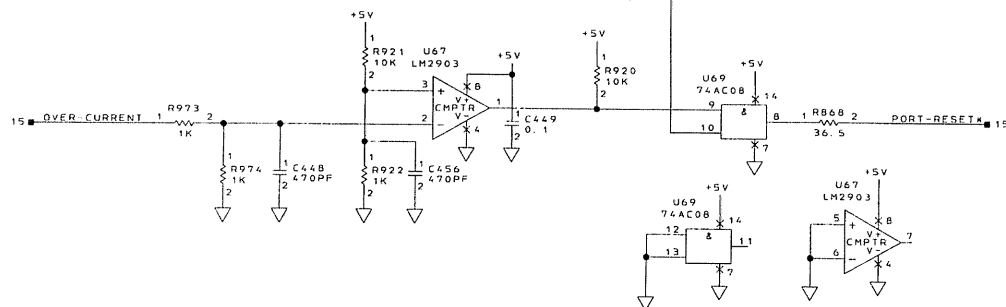
COMPUTER GENERATED

3-6, 8-15, 22, 25 CAS-LD15 01
 20 15M12-U
 4, 6, 20 PWR-RESET
 2-6, 8, 22 CAS-LRFDX
 2, 5, 6, 8 CAS-LWRITEX
 3 CAS-LUARTX
 3-6, 8, 22, 25 CAS-LA119 01

2, 3, 8, 24, 25 CAS-RESETX
 3 CAS-ODW2X



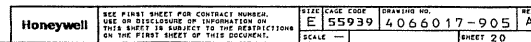
OUTPUT PORT LIGHTNING SENSE

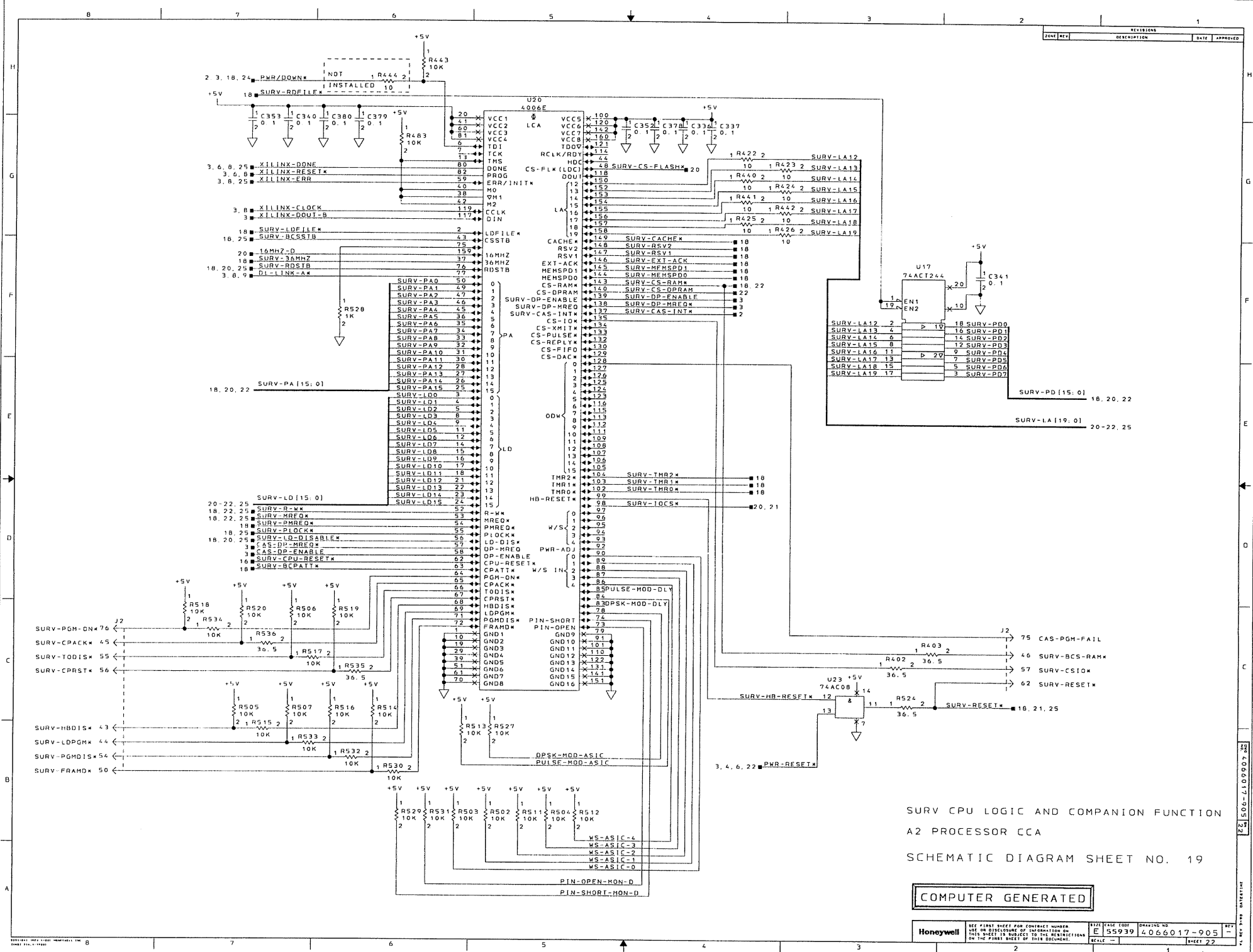


CAS CPU UART AND OUTPUT PORTS
 A2 PROCESSOR CCA
 SCHEMATIC DIAGRAM SHEET NO. 16

COMPUTER GENERATED

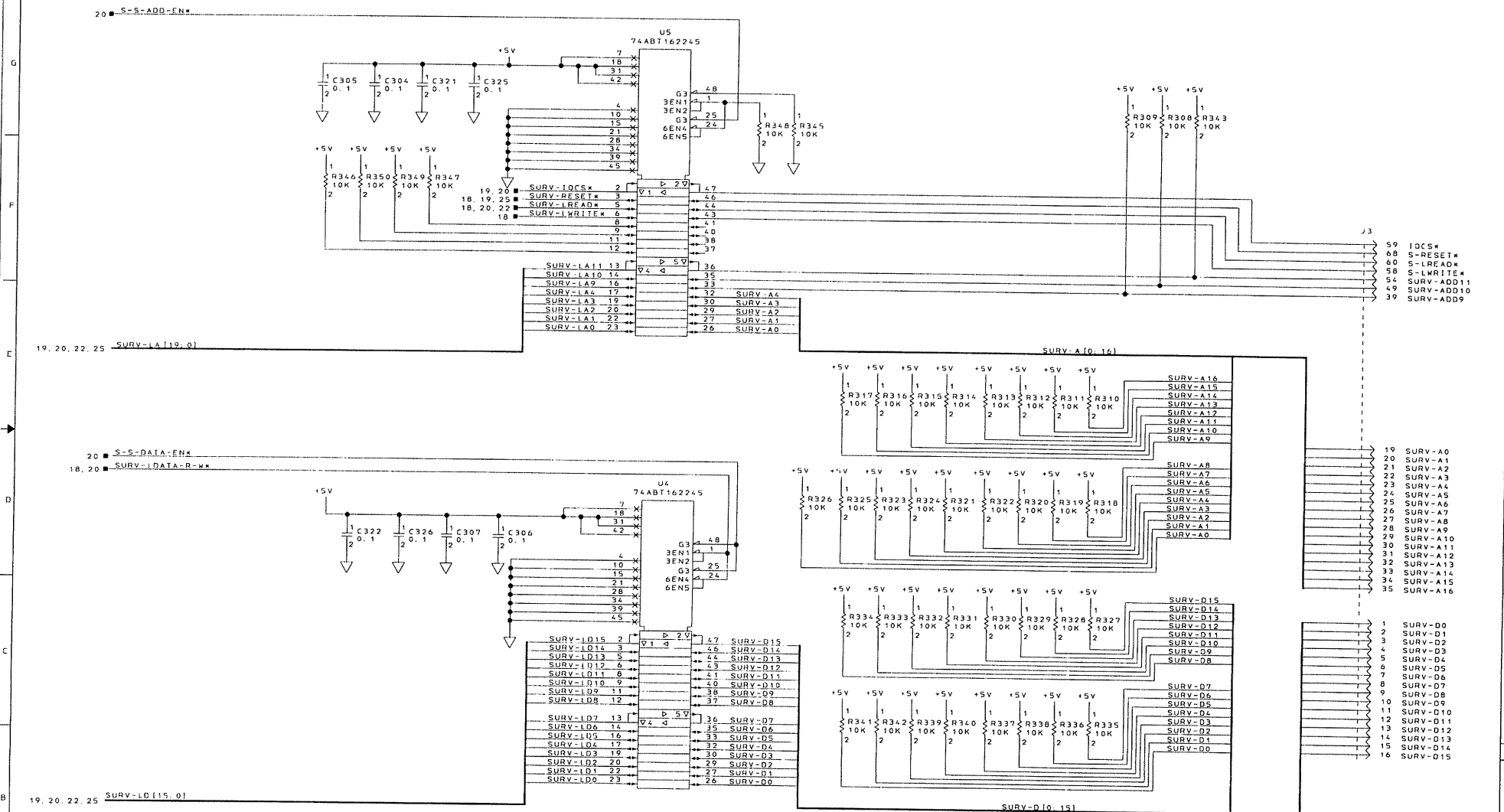
3 ■ MUX-SEL-0
3 ■ MUX-SEL-1
3 ■ MUX-SEL-2





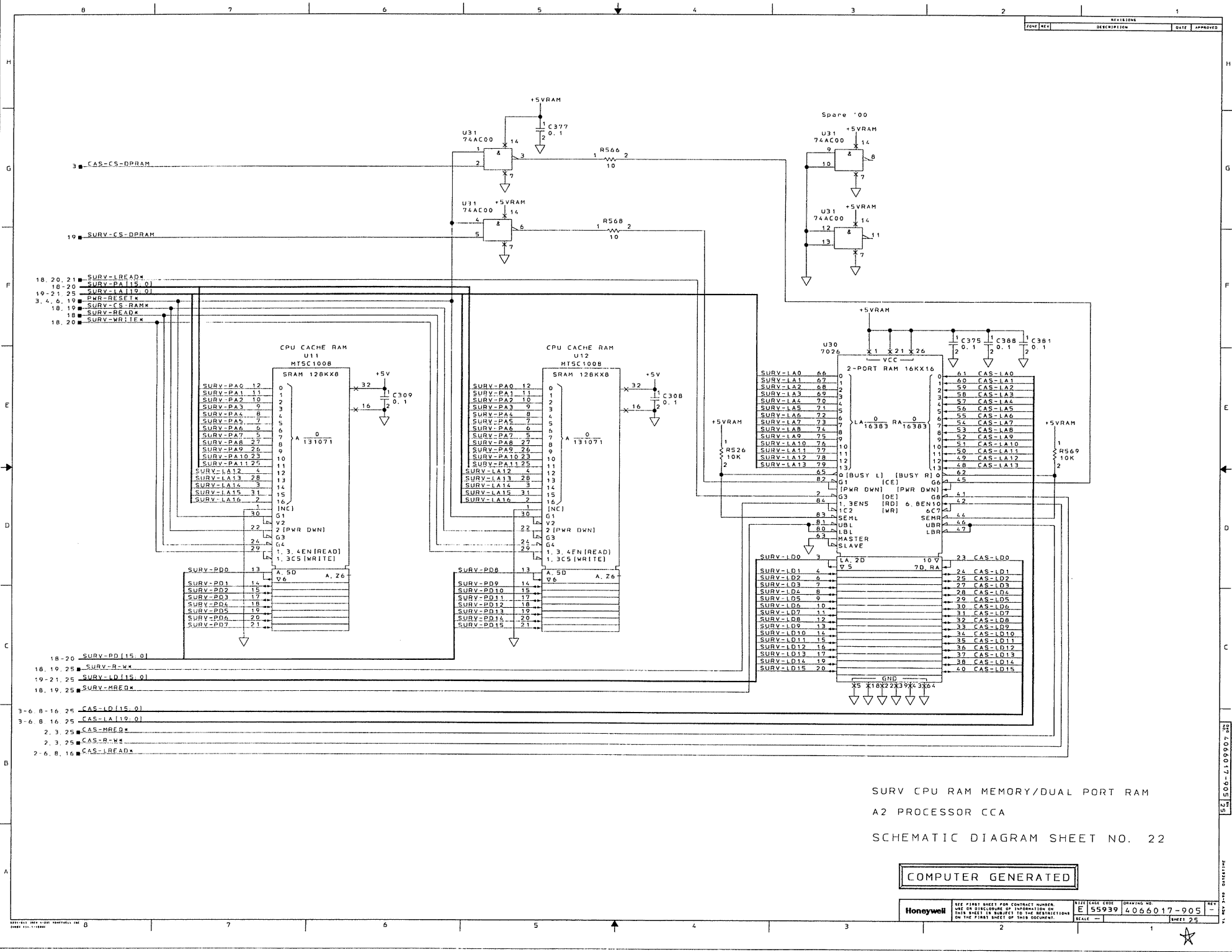
SURV CPU LOGIC AND COMPANION FUNCTION
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 19

REV	DESCRIPTION	DATE	APPROVED
1			



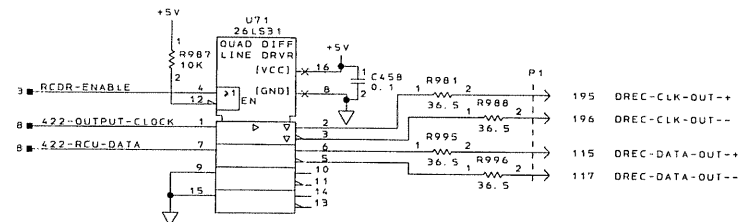
SURV CPU FLASH EPROM MEMORY
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 21

COMPUTER GENERATED



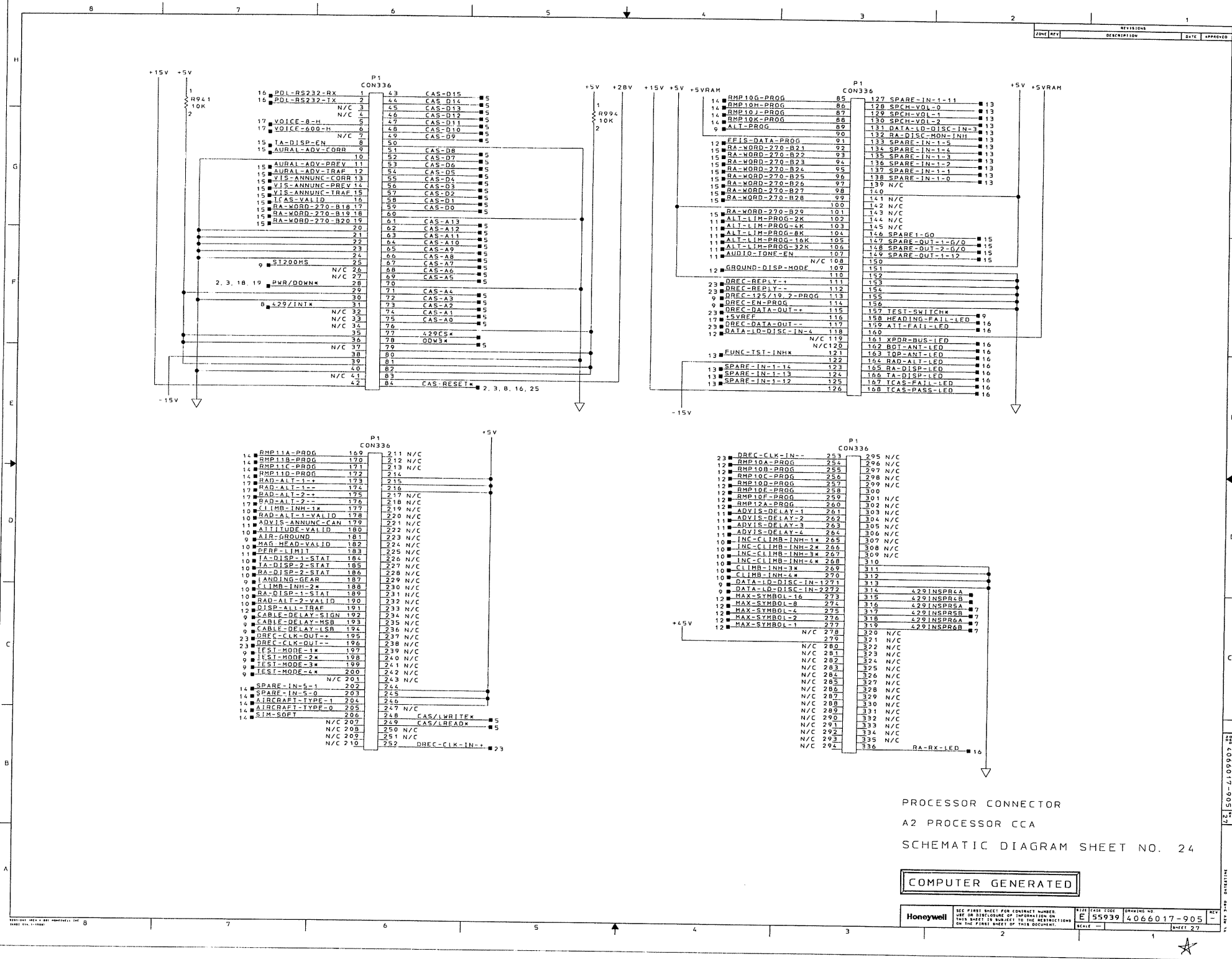
SURV CPU RAM MEMORY/DUAL PORT RAM
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 22

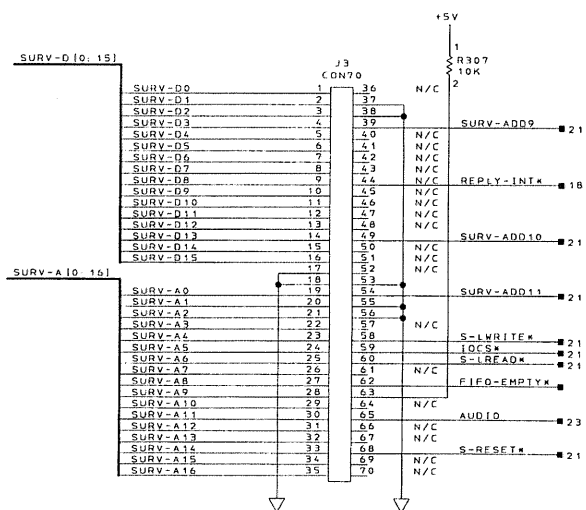
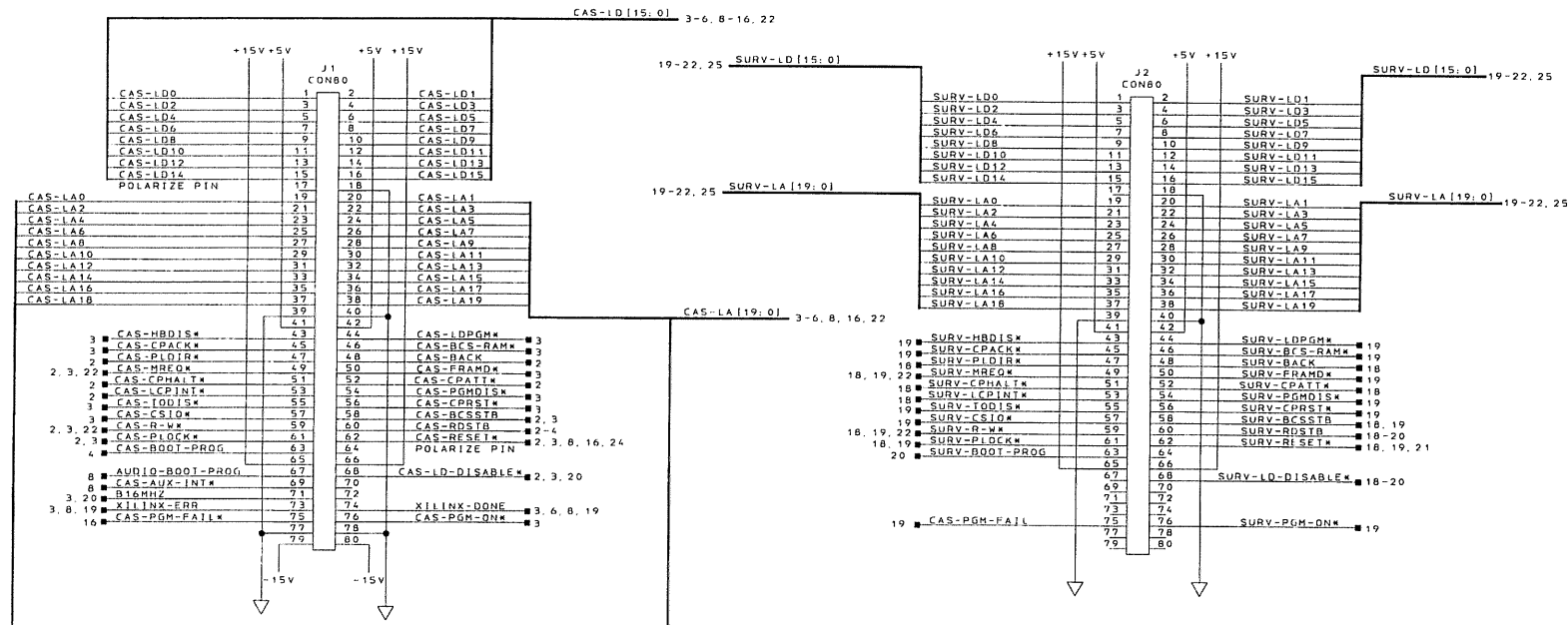
COMPUTER GENERATED



SCHEMATIC DIAGRAM SHEET NO. 23

COMPUTER GENERATED





CAS CPU/ SURV CPU/ SPARE CONNECTORS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 25

COMPUTER GENERATED