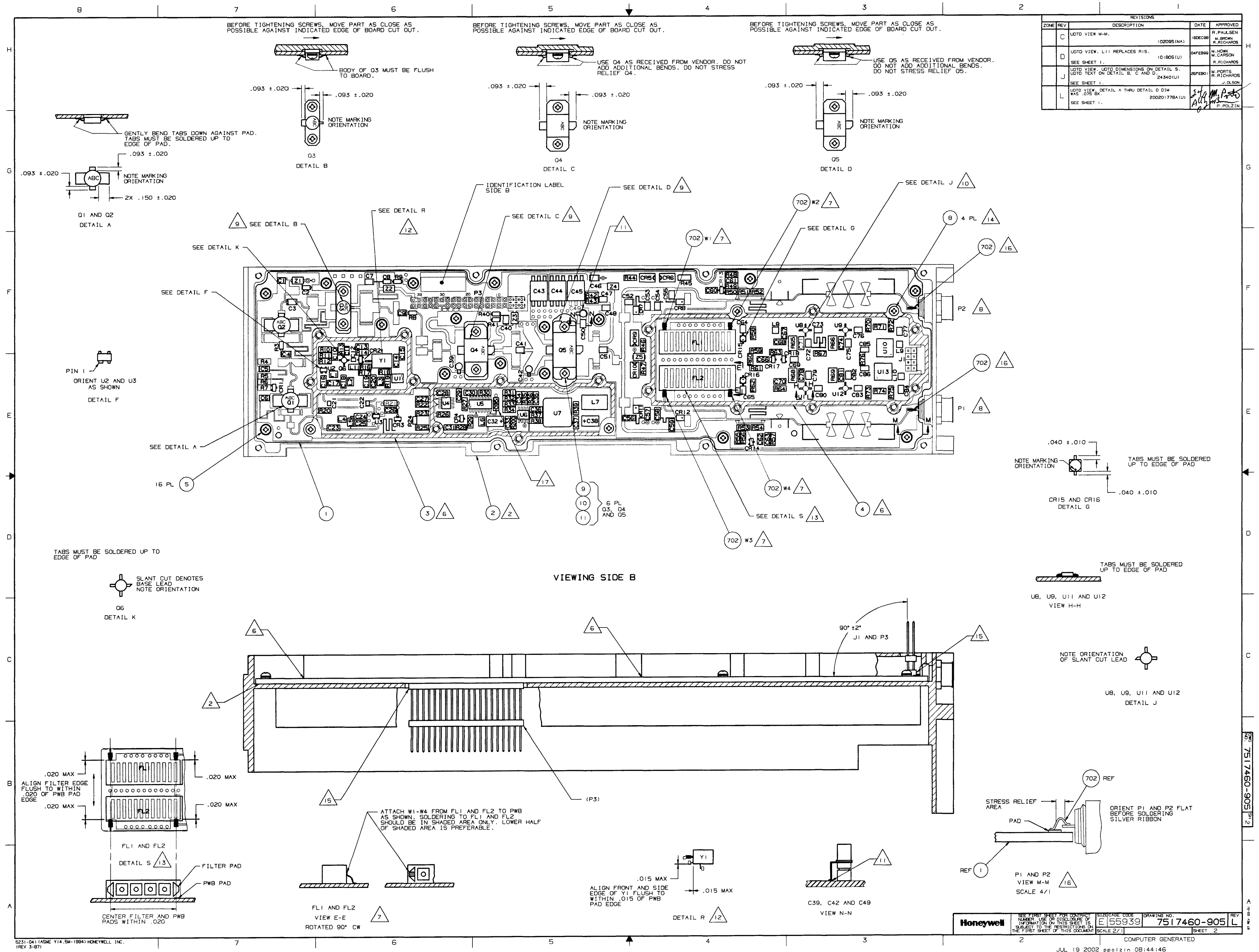
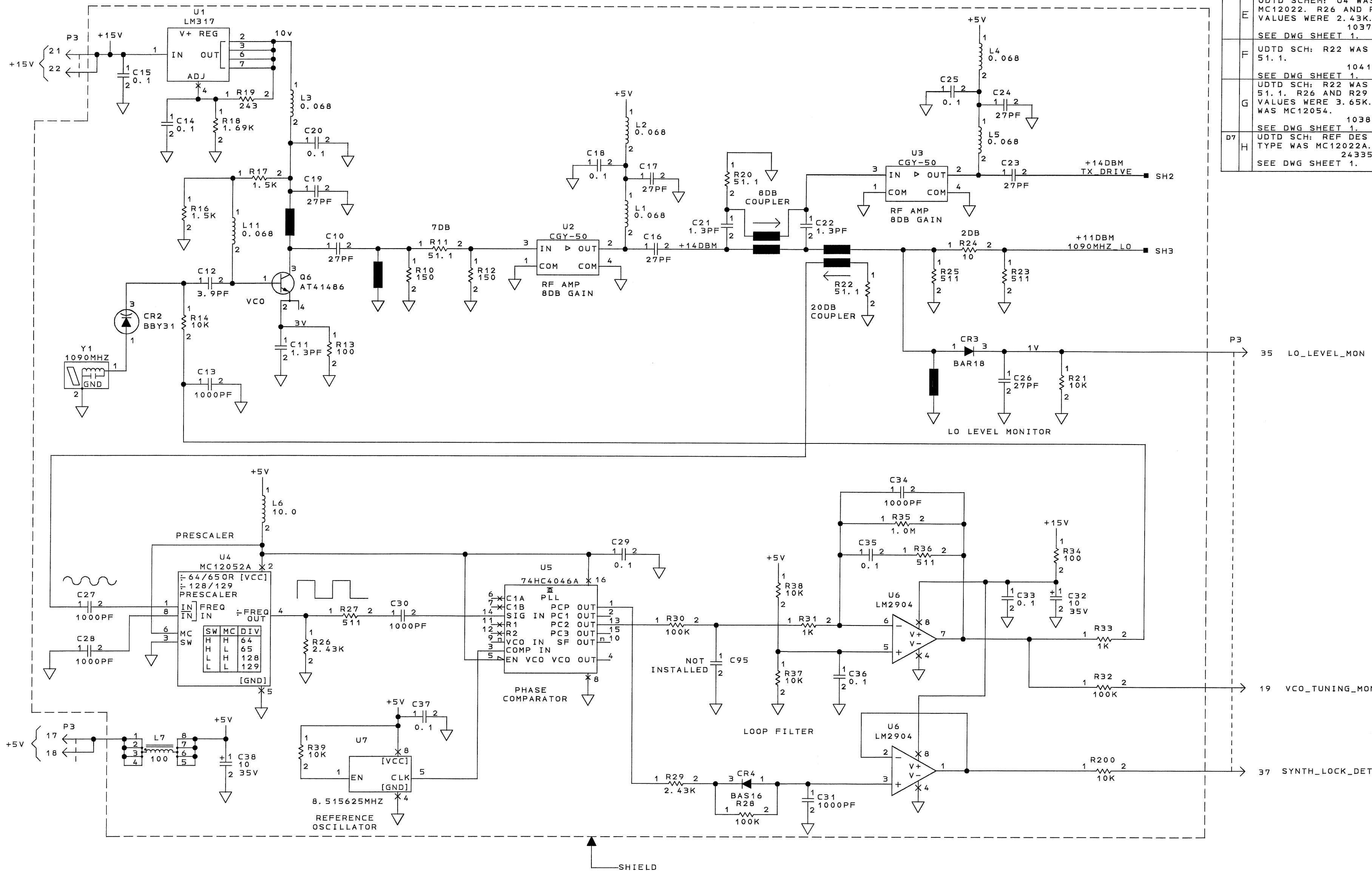




REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
F7	D	UDTD SCHEM: L11 REPLACES R15. SEE DWG SHEET 1.	4 FEB 99	M. HOWK M. CARSON R. RICHARDS
E	E	UDTD SCHEM: U4 WAS MC12022. R26 AND R29 VALUES WERE 2.43K. SEE DWG SHEET 1.	1 SEP 99	M. HOWK M. BROWN L. WHISLER
F	F	UDTD SCH: R22 WAS 51.1. SEE DWG SHEET 1.	1 SEP 99	M. HOWK M. BROWN L. WHISLER
G	G	UDTD SCH: R22 WAS 51.1. R26 AND R29 VALUES WERE 3.65K. U4 WAS MC12054. SEE DWG SHEET 1.	1 SEP 99	M. HOWK M. BROWN L. WHISLER
D7	H	UDTD SCH: REF DES U4 TYPE WAS MC12022A. 24335 (U) SEE DWG SHEET 1.	10 NOV 00	M. HOWK R. RICHARDS



- NOTES:
UNLESS OTHERWISE SPECIFIED:
1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 2. ALL RESISTANCE VALUES ARE IN OHMS.
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
 4. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS PREFIX PART DESIGNATION WITH SUBASSEMBLY DESIGNATION.
 5. INTERPRET DRAWING PER ANSI Y32.2-1975 AND ANSI/IEEE STD 991-1986. LOGIC SYMBOLS CONFORM TO ANSI/IEEE STD 91-1984.
 6. ALL VOLTAGES ARE DC UNLESS OTHERWISE NOTED.

1090 MHZ LOCAL OSCILLATOR

SCHEMATIC DIAGRAM SHEET NO. 1 OF 5

COMPUTER GENERATED

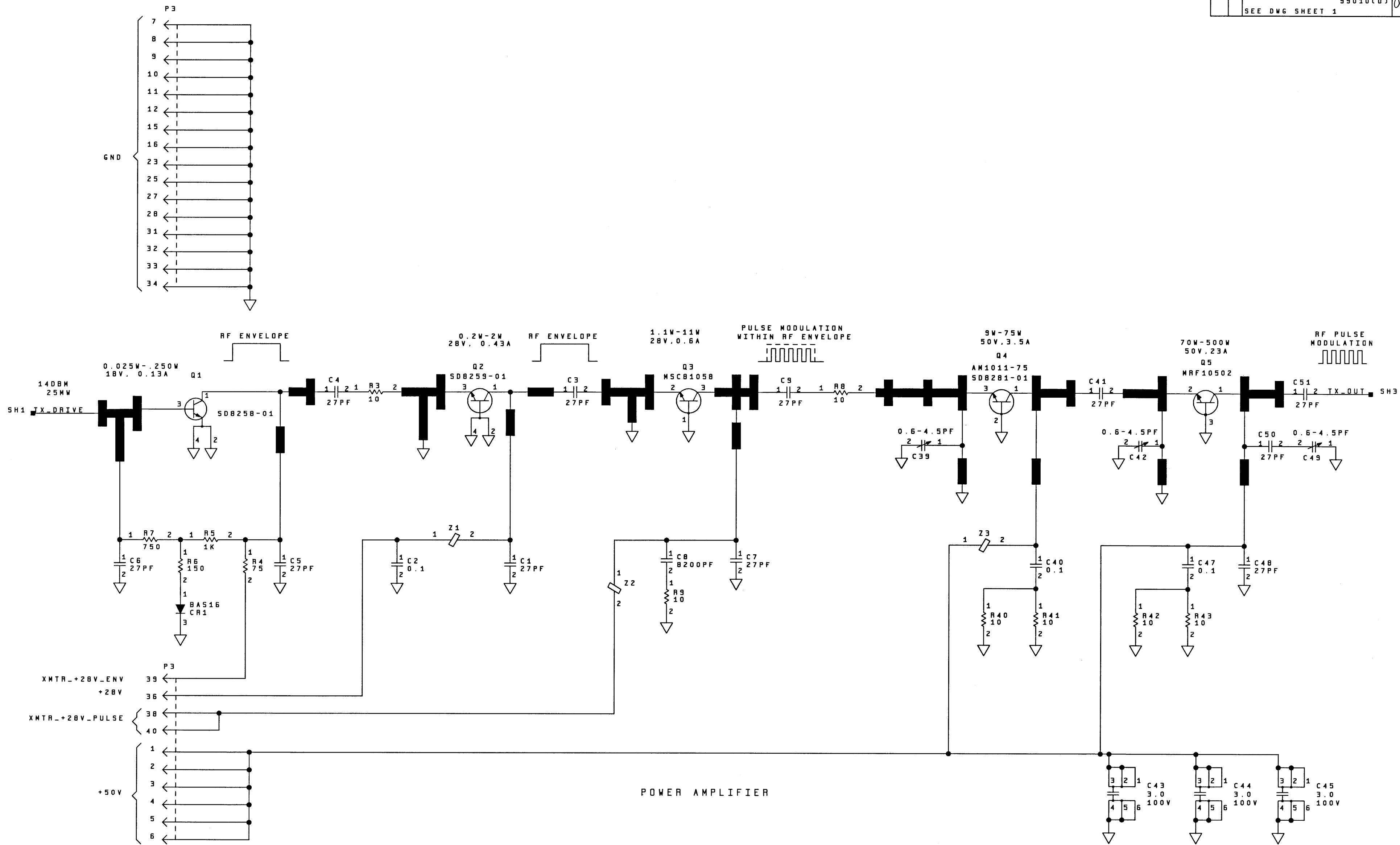
Honeywell

SEE FIRST SHEET FOR CONTRACT NUMBER.
USE OR DISCLOSURE OF INFORMATION ON
THIS SHEET IS SUBJECT TO THE RESTRICTIONS
ON THE FIRST SHEET OF THIS DOCUMENT.

SIZE E 55939
DRAWING NO. 7517460-905
SCALE
SHEET 3

17 REV 3-92 DATE/STAMP

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
E2	B	UDTD SCH: CHANGED REF DES Q5 TYPE TO MRF10502, 99010(U) SEE DWG SHEET 1	16 Oct 98
			APPROVED
			M. NOREAU

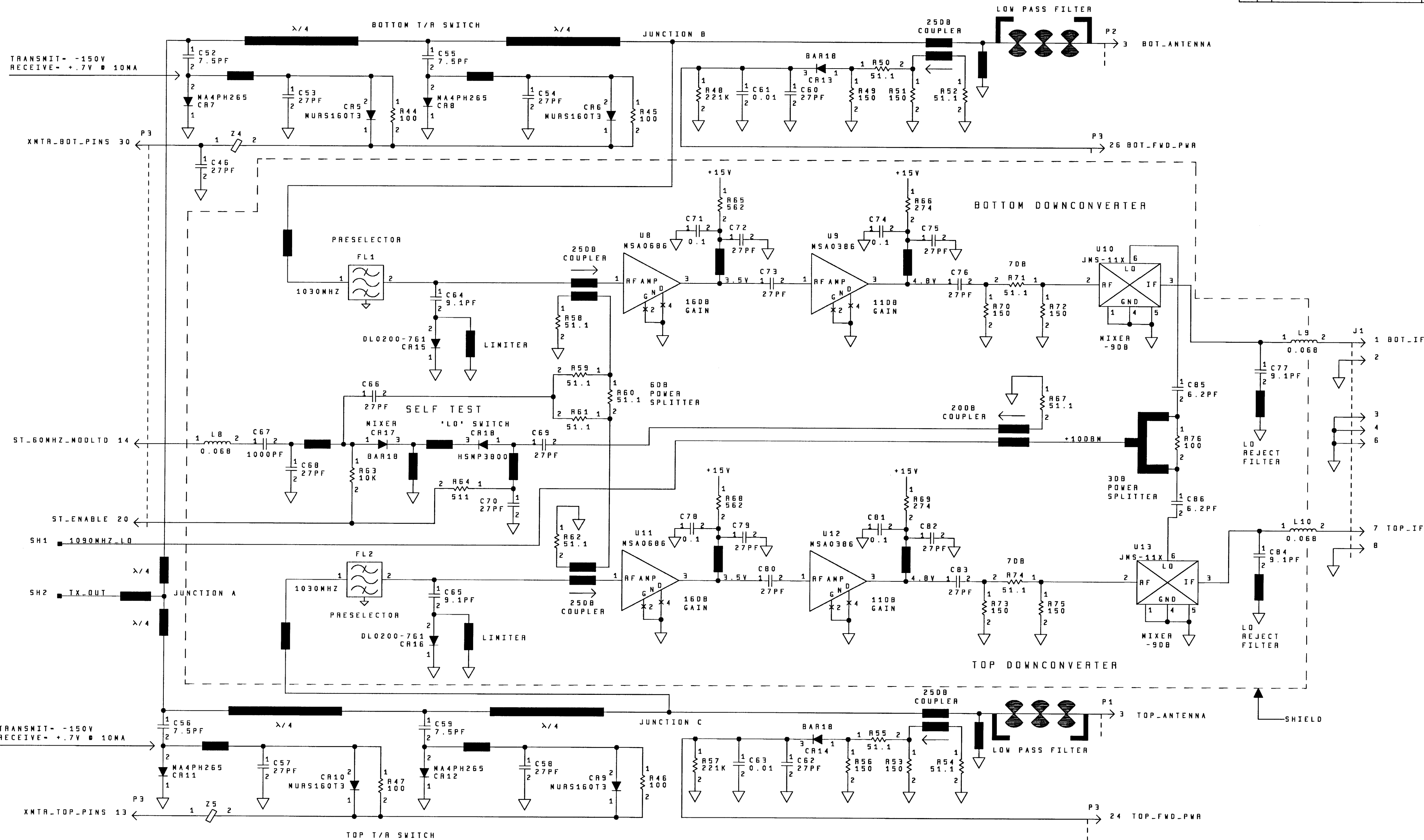


POWER AMPLIFIER
SCHEMATIC DIAGRAM SHEET NO. 2

COMPUTER GENERATED

Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE CASE CODE E 55939	DRAWING NO. 7517460-905	REV B
		SCALE	SHEET 4		

REV		DESCRIPTION	DATE	APPROVED
C2	H2	UDTD SCH: ADD MISSING PIN NUMBER (3) TO CONNECTORS P1 AND P2. SEE DWG SHEET 1	16 Oct 98	M. MOREAU



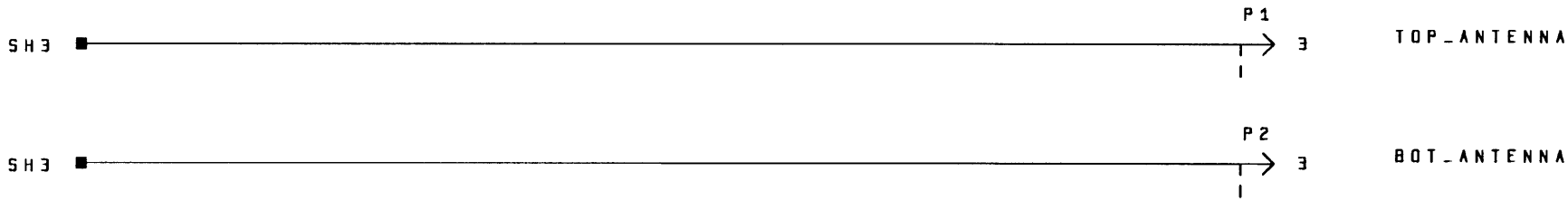
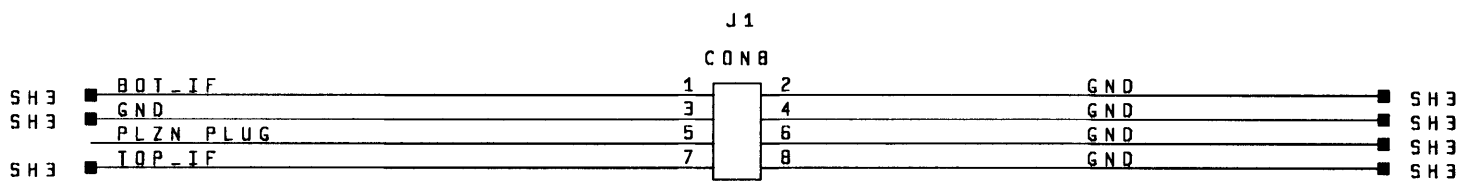
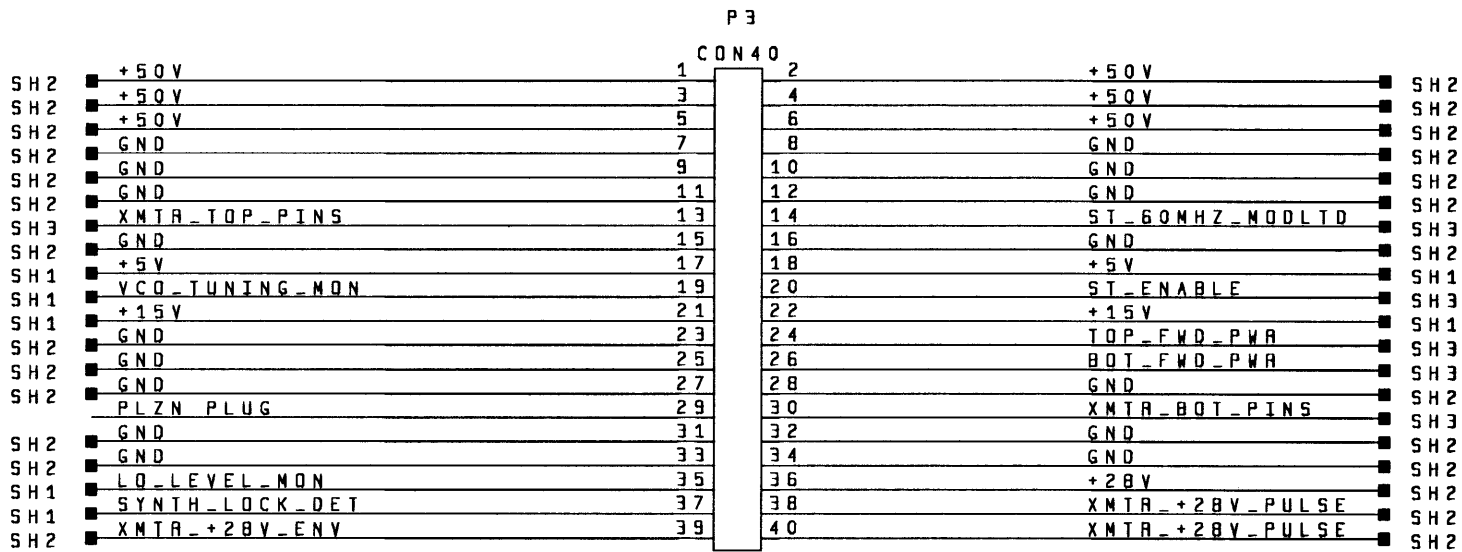
DUPLEXER/DOWNCONVERTERS/SELF-TEST

SCHEMATIC DIAGRAM SHEET NO. 3

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE/CAGE CODE E 55939	DRAWING NO. 7517460-905	REV A
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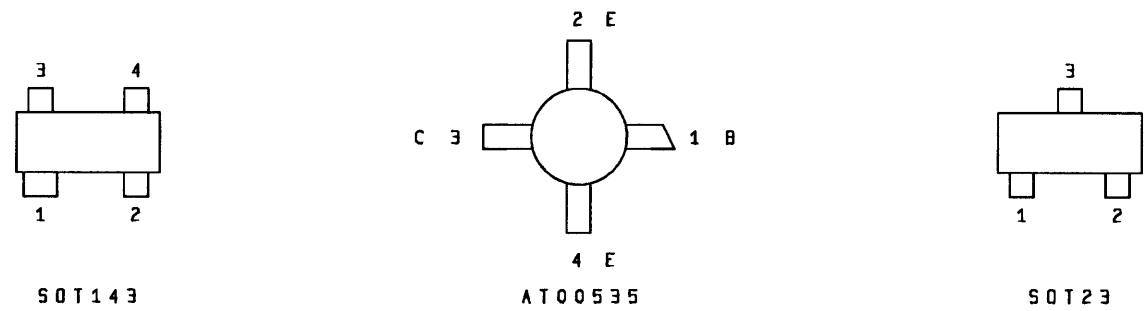
REVISIONS			
ZONE	REV	DESCRIPTION	DATE
E 4	A	UDTD SCH. ADD PIN NUMBER (3) TO CONNECTORS P1 AND P2.	16 Oct 98
SEE DWG SHEET 1			APPROVED



COMMON LABELS
ON TOP OF SMT DEVICES

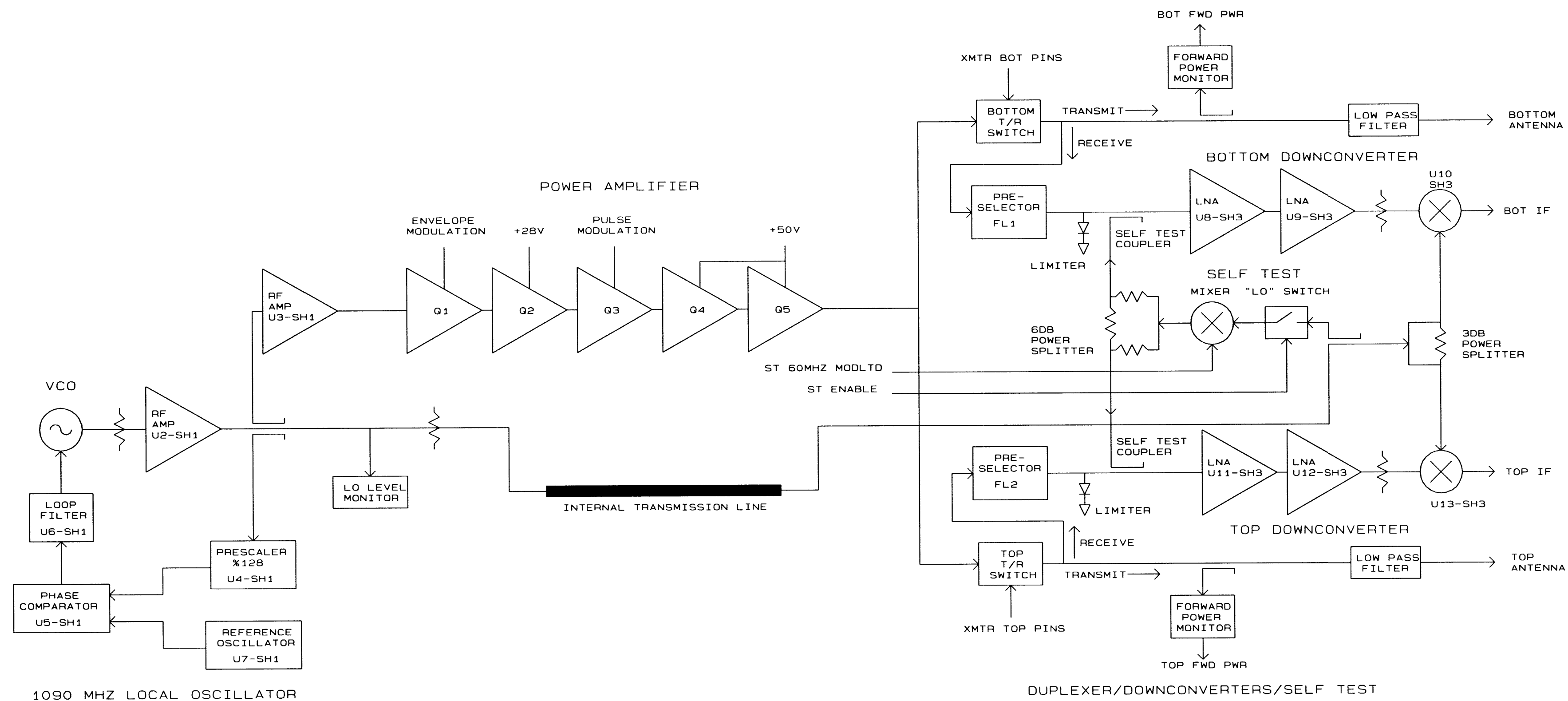
PART NAME	LABEL
BAS16	A6
BAR18	5F, D76
BZX84-C13	Y3
BSR14	UB
MMBT5550	1F
BSR16	T8
SMBG15A	LM
SMBG5A	KE
MURS160T3	U1J
MMBT5401	2L
BZX84-C10	6Z
SMBG48A	MW
PZTA92	ZTA92
CGY50	G2
MSA0686	A06
MSA0386	A03
HSMP3800	D0
AT00535	NONE
HSMP3820	F0

PINOUTS OF SMT DEVICES



CONNECTOR PIN-OUT
SCHEMATIC DIAGRAM SHEET NO. 4

COMPUTER GENERATED



BLOCK DIAGRAM
SCHEMATIC DIAGRAM SHEET NO. 5

COMPUTER GENERATED