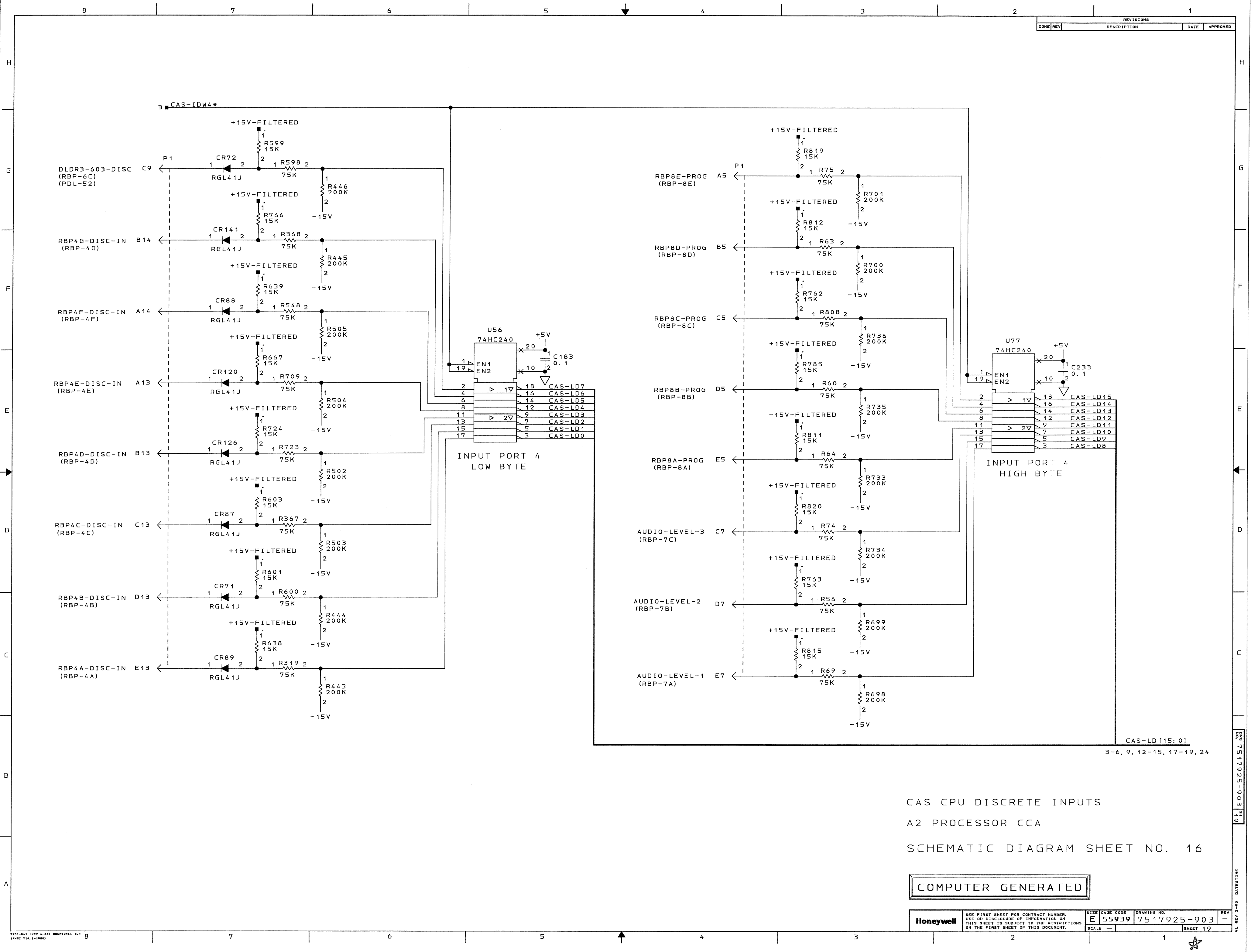
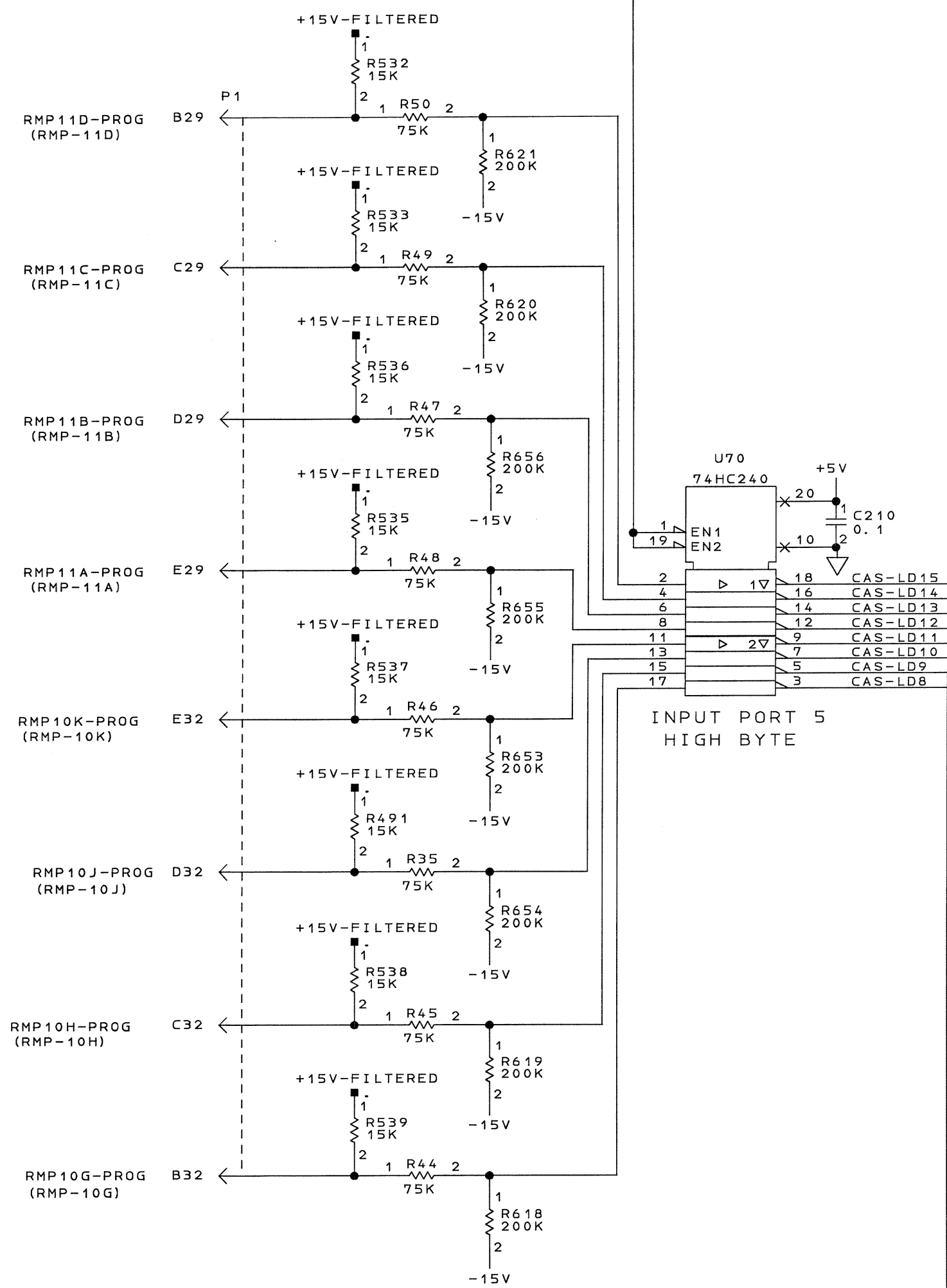
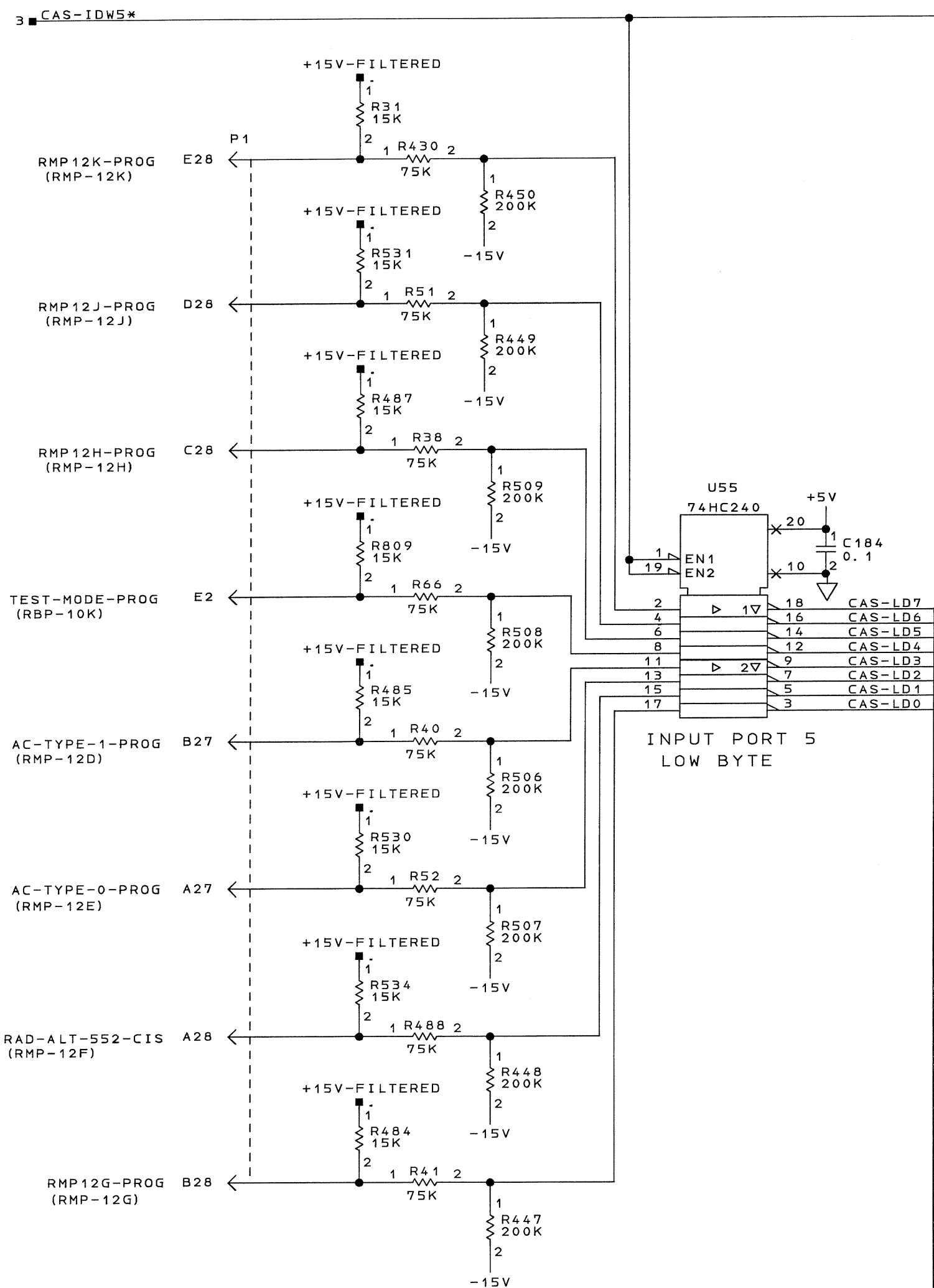




CAS CPU DISCRETE INPUTS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 16

COMPUTER GENERATED

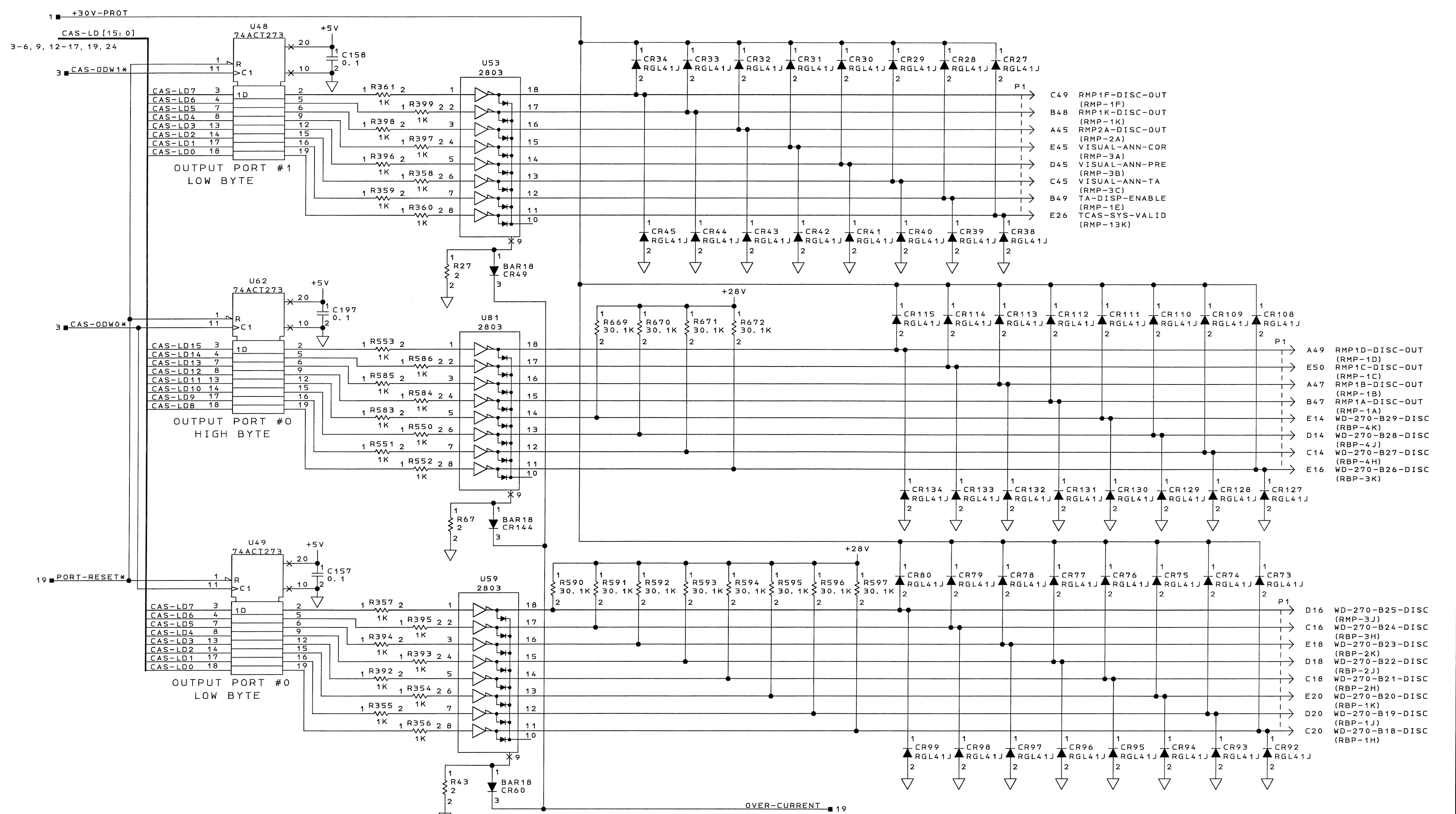
Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	E 55939	DRAWING NO.	7517925-903	REV	1
		SCALE	—	DATE	—	REV	19



CAS-LD [15: 0]
3-6, 9, 12-16, 18, 19, 24

CAS CPU DISCRETE INPUTS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 17

COMPUTER GENERATED

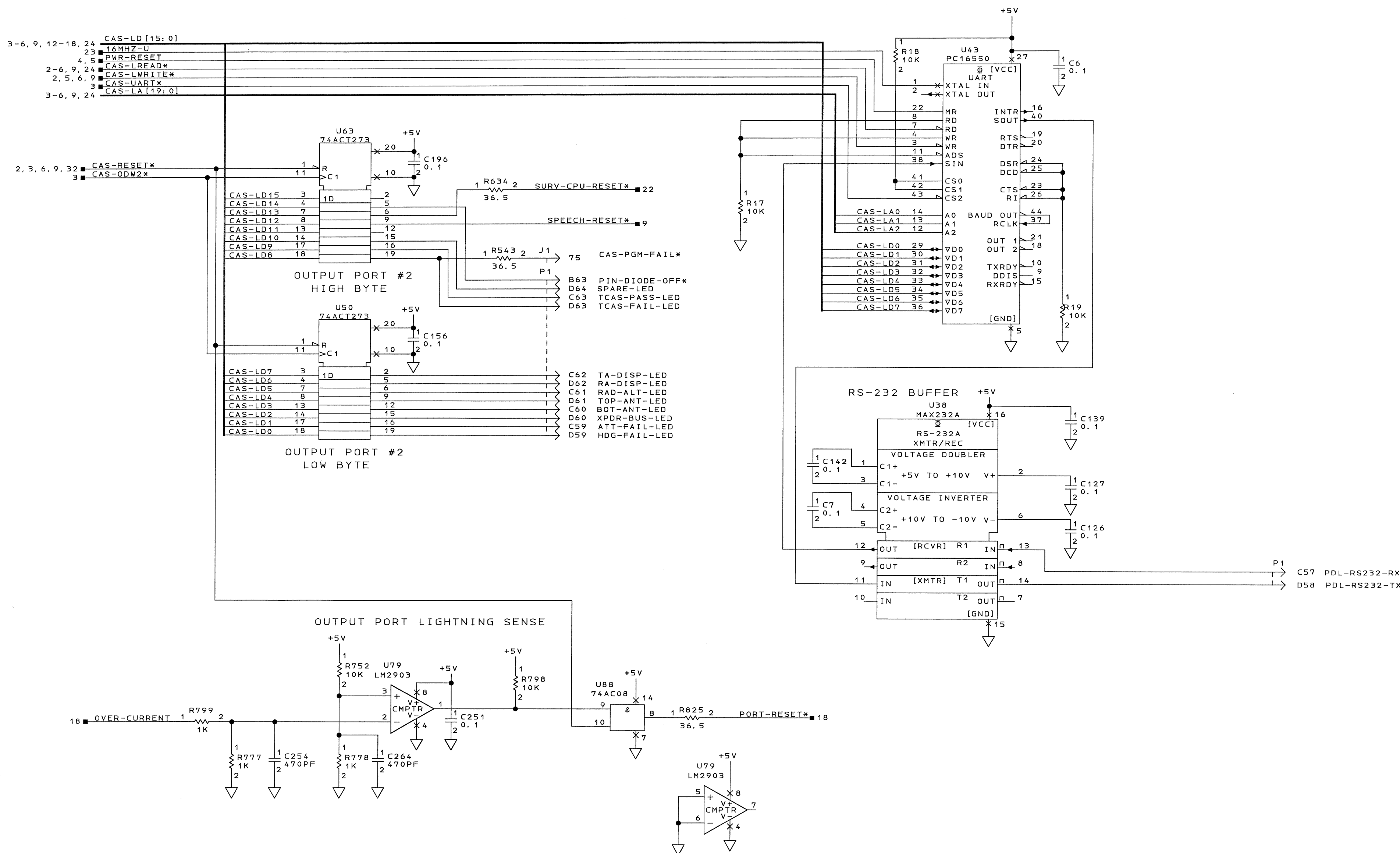


CAS CPU DISCRETE OUTPUTS

A2 PROCESSOR CCA

SCHEMATIC DIAGRAM SHEET NO. 18

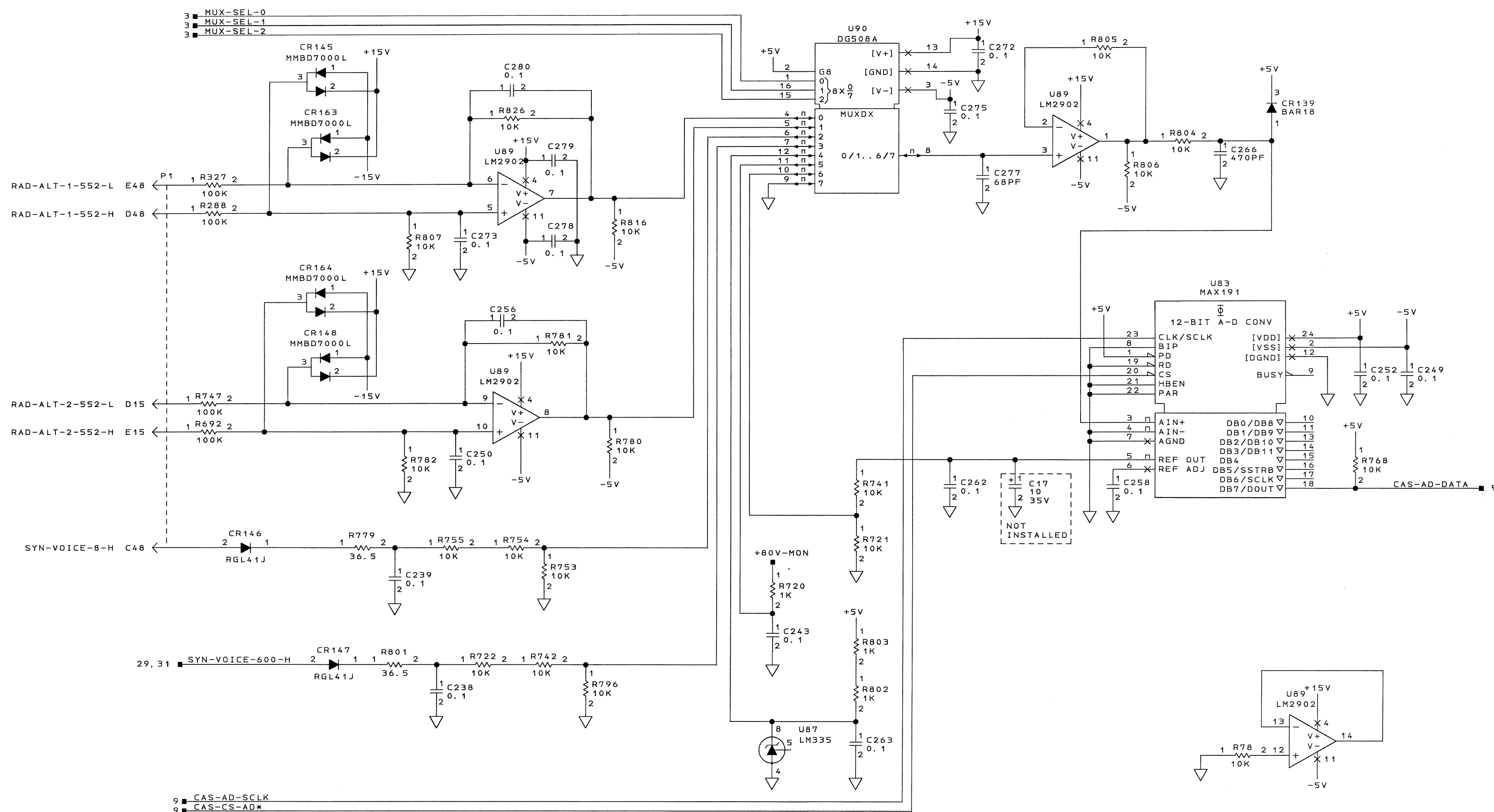
COMPUTER GENERATED



CAS CPU UART AND OUTPUT PORTS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 19

COMPUTER GENERATED

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
			APPROVED

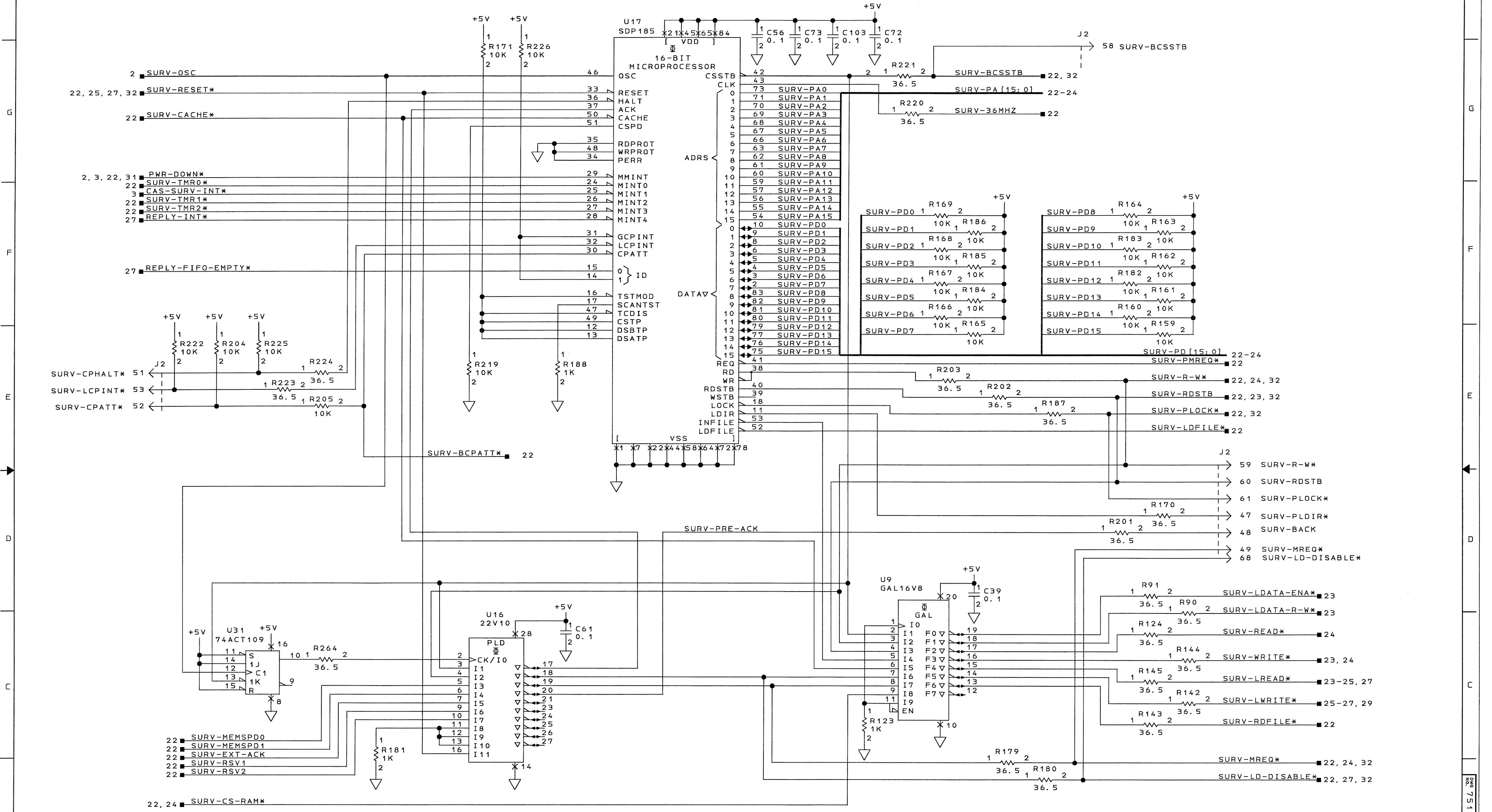


CAS CPU ANALOG INPUTS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 20

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7517925-903	-
		SCALE —		SHEET 23	

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
			APPROVED

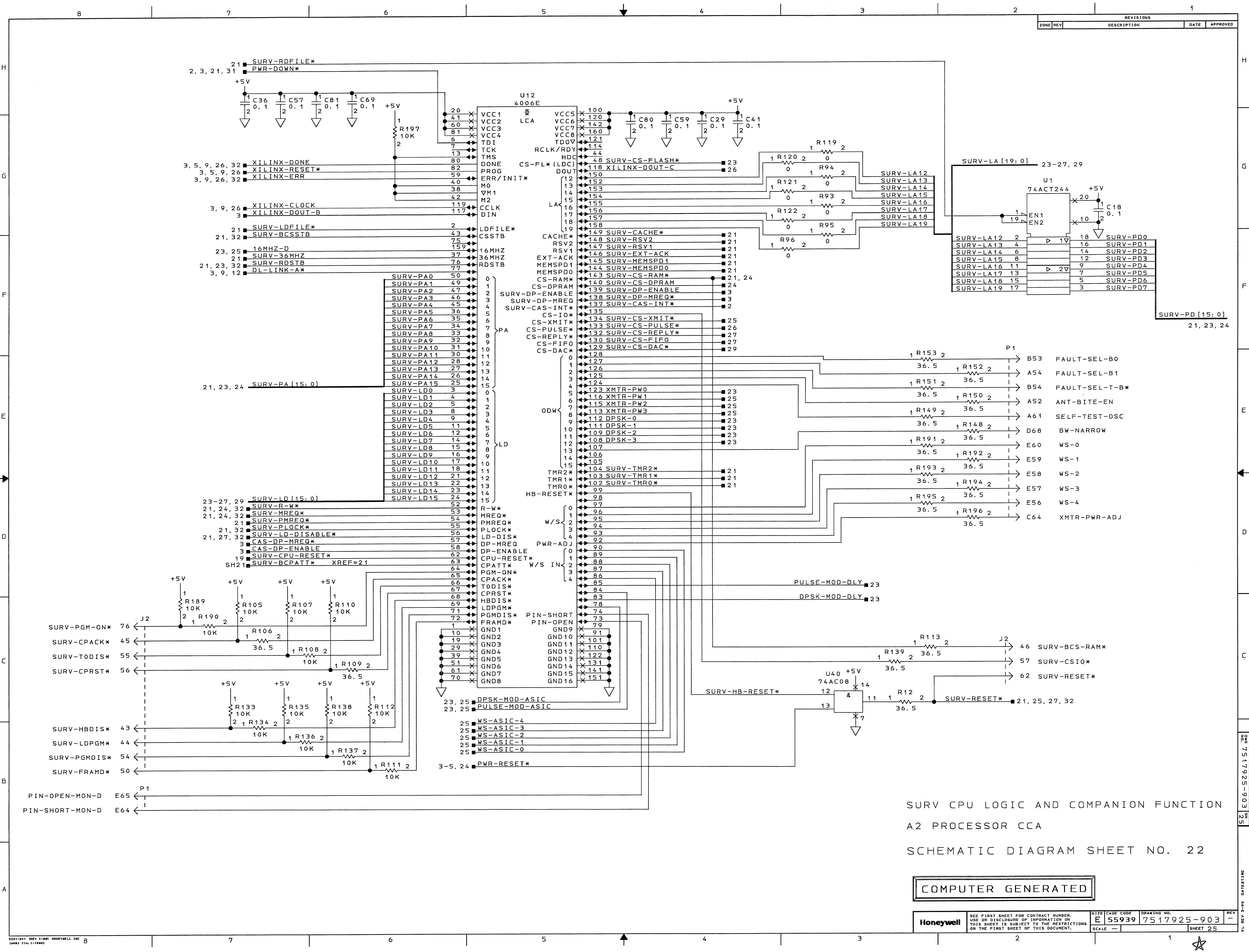


SURV CPU SDP185 PROCESSOR
A2 PROCESSOR CCA

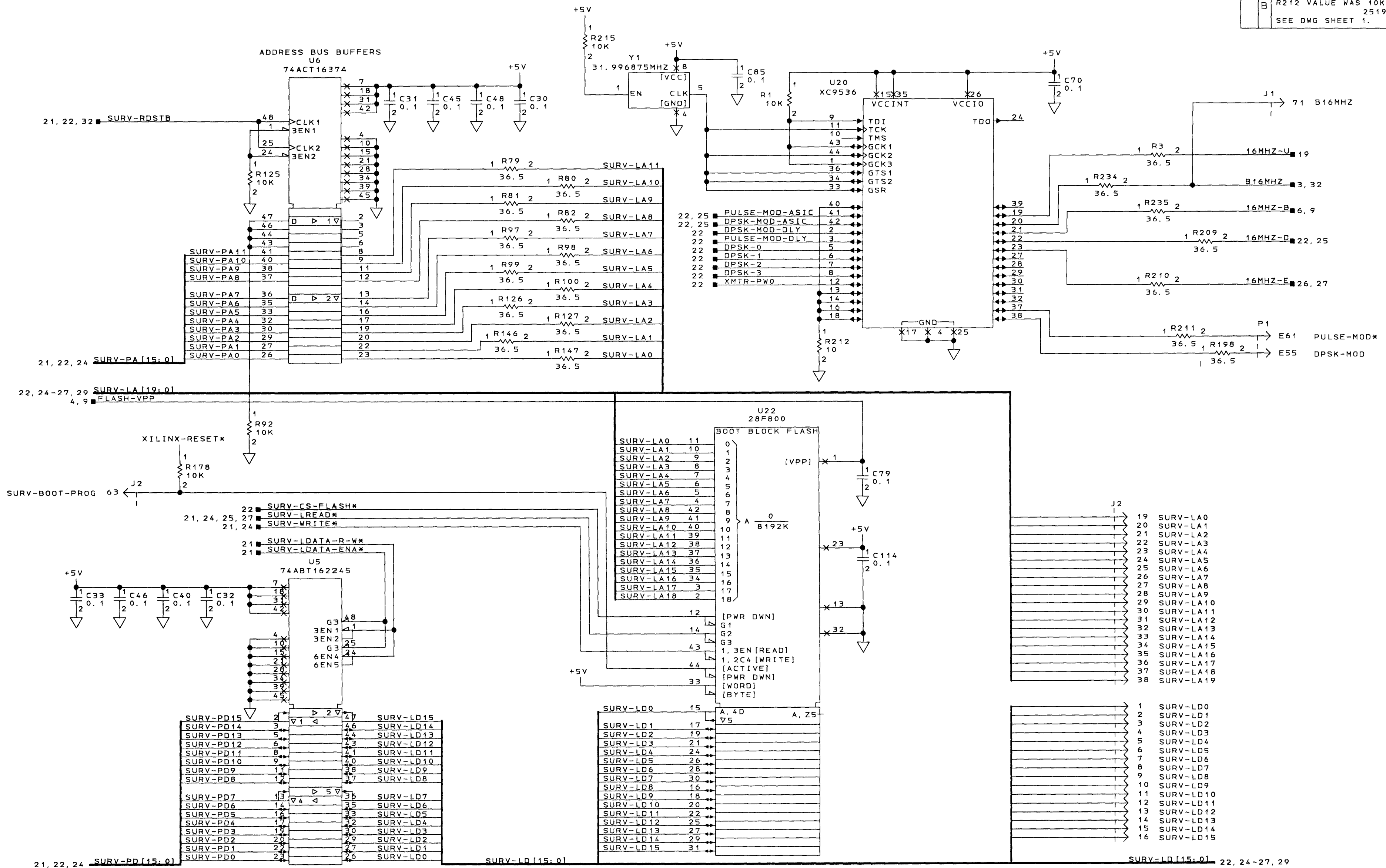
SCHEMATIC DIAGRAM SHEET NO. 21

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7517925-903	21



REVISIONS		DATE	APPROVED
ZONE	REV		
B	1	10/1/83	H. RICHARDS
DESCRIPTION			
UDTD SCHEM; REF DES			
R212 VALUE WAS 10K.			
SEE DWG SHEET 1.			

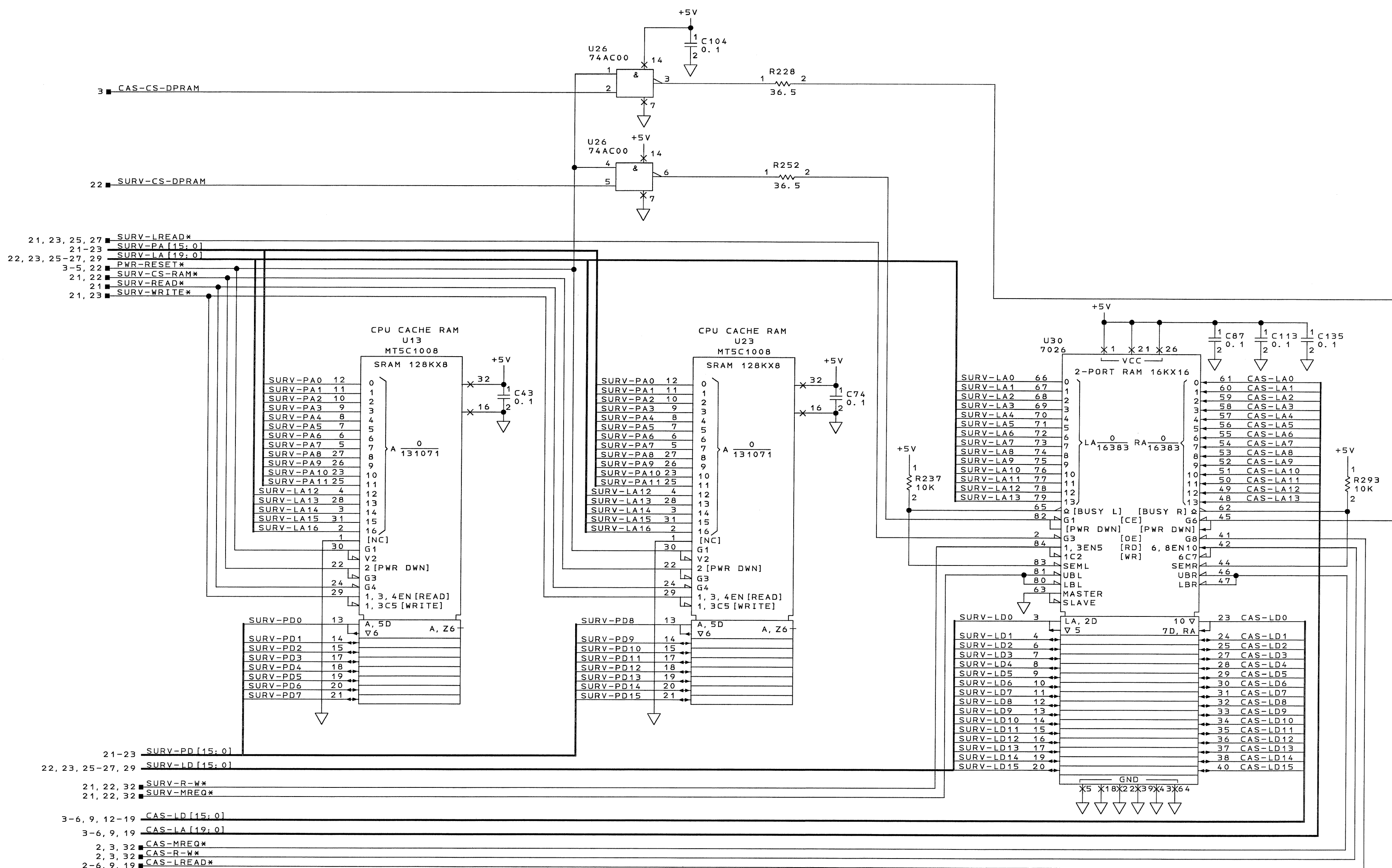


SURV CPU FLASH EPROM MEMORY
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 23

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER, USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7517925-903	B
		SCALE			SHEET 26

7517925-903 26



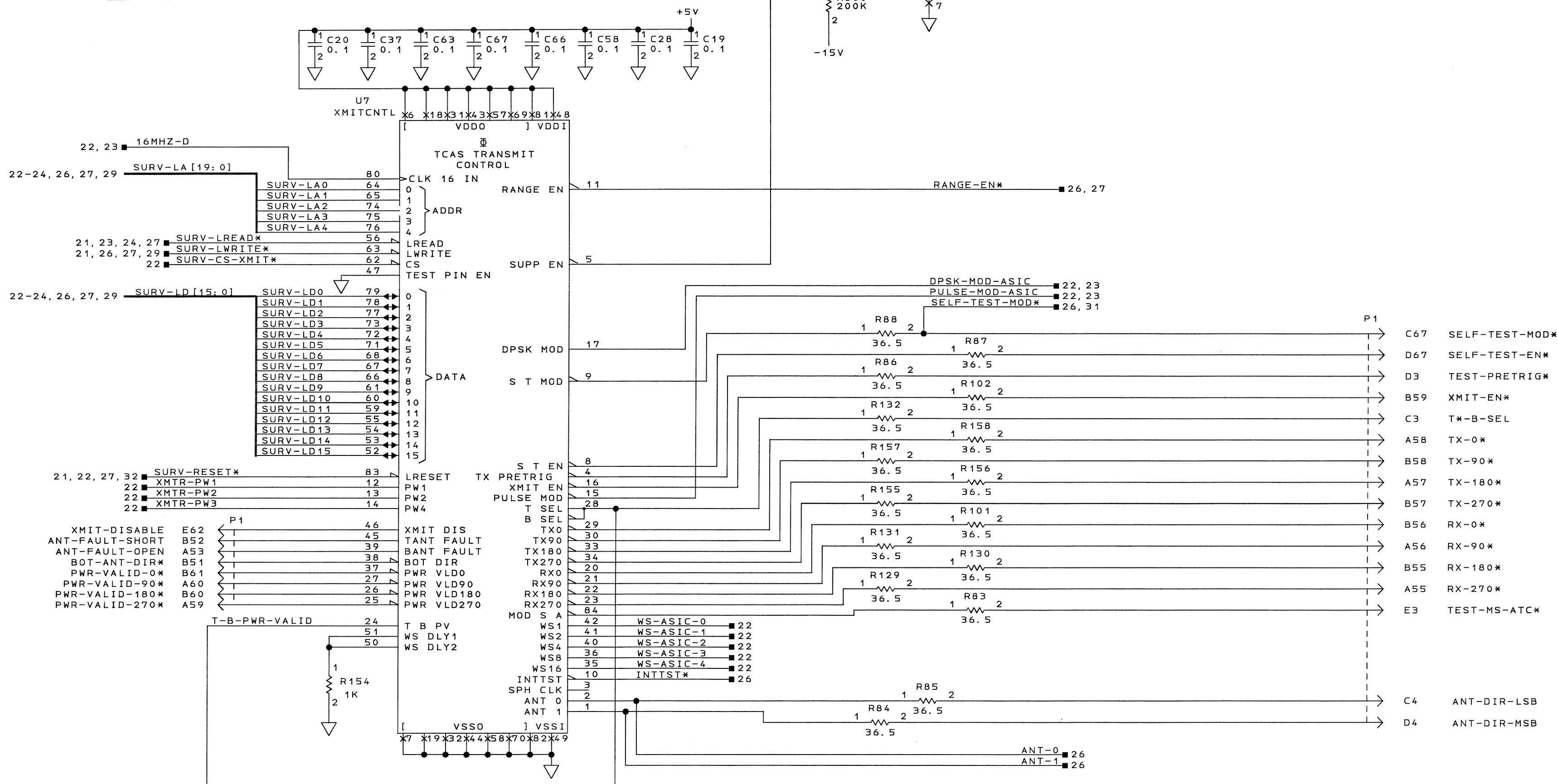
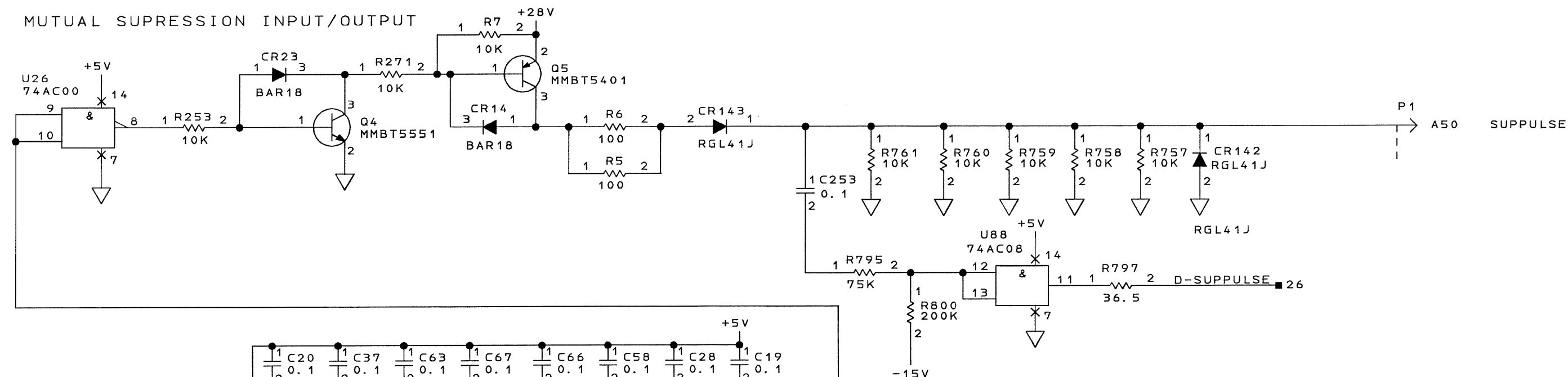
SURV CPU RAM MEMORY/DUAL PORT RAM

A2 PROCESSOR CCA

SCHEMATIC DIAGRAM SHEET NO. 24

COMPUTER GENERATED

MUTUAL SUPPRESSION INPUT/OUTPUT



SURV TRANSMITTER ASIC CONTROL
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 25

COMPUTER GENERATED