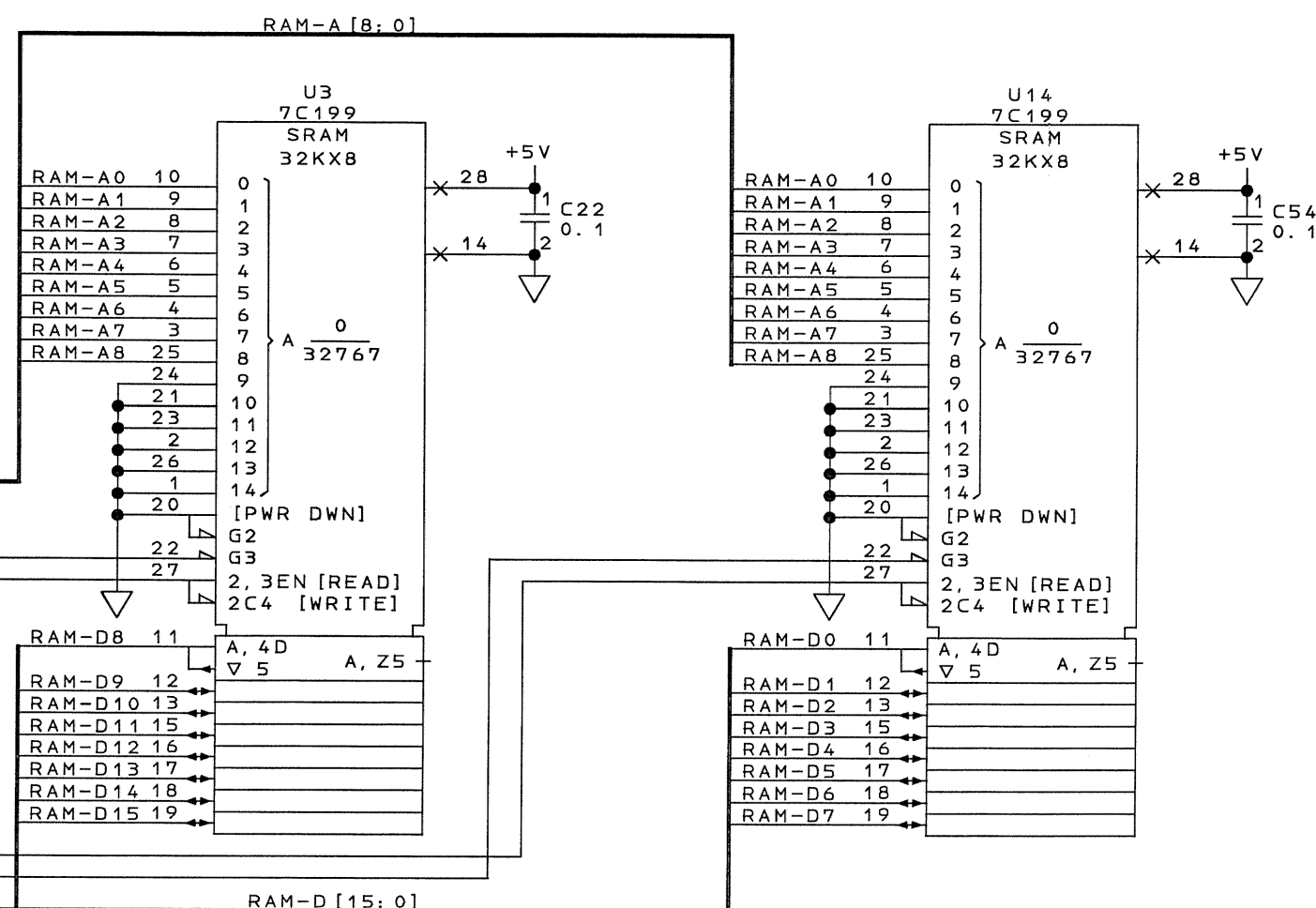
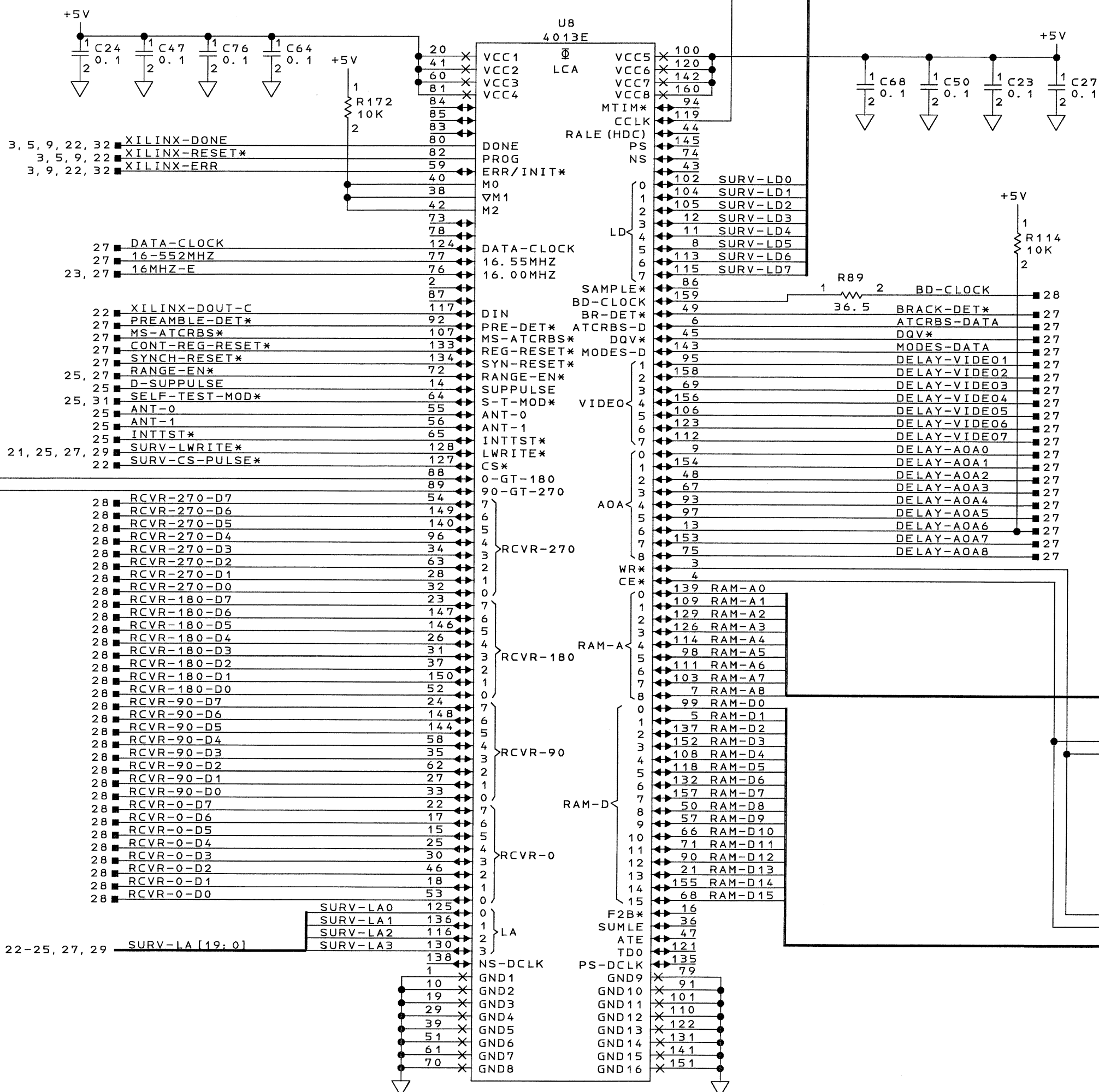
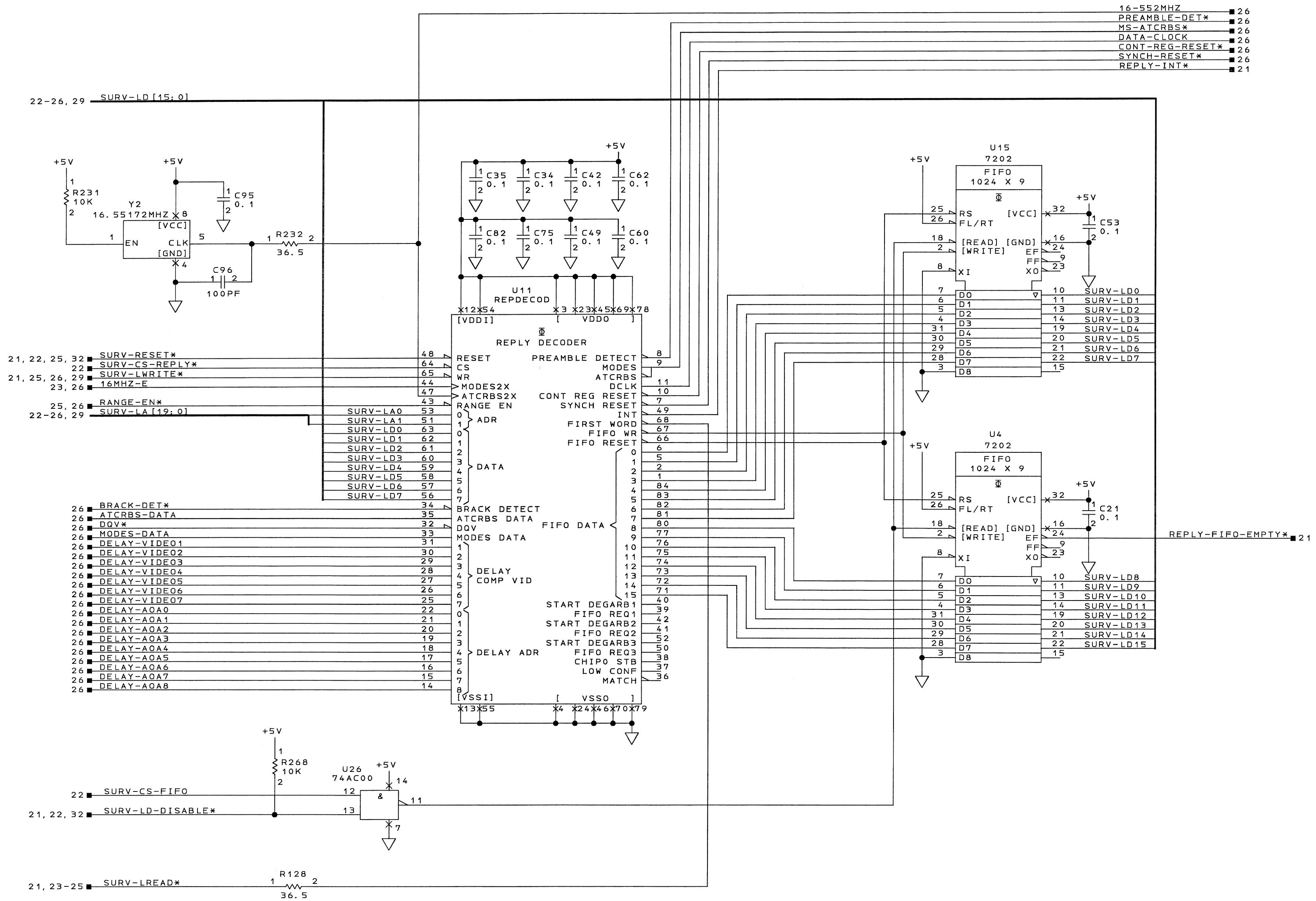


22-25, 27, 29 SURV-LD [15:0]
3, 9, 22 XILINX-CLOCK



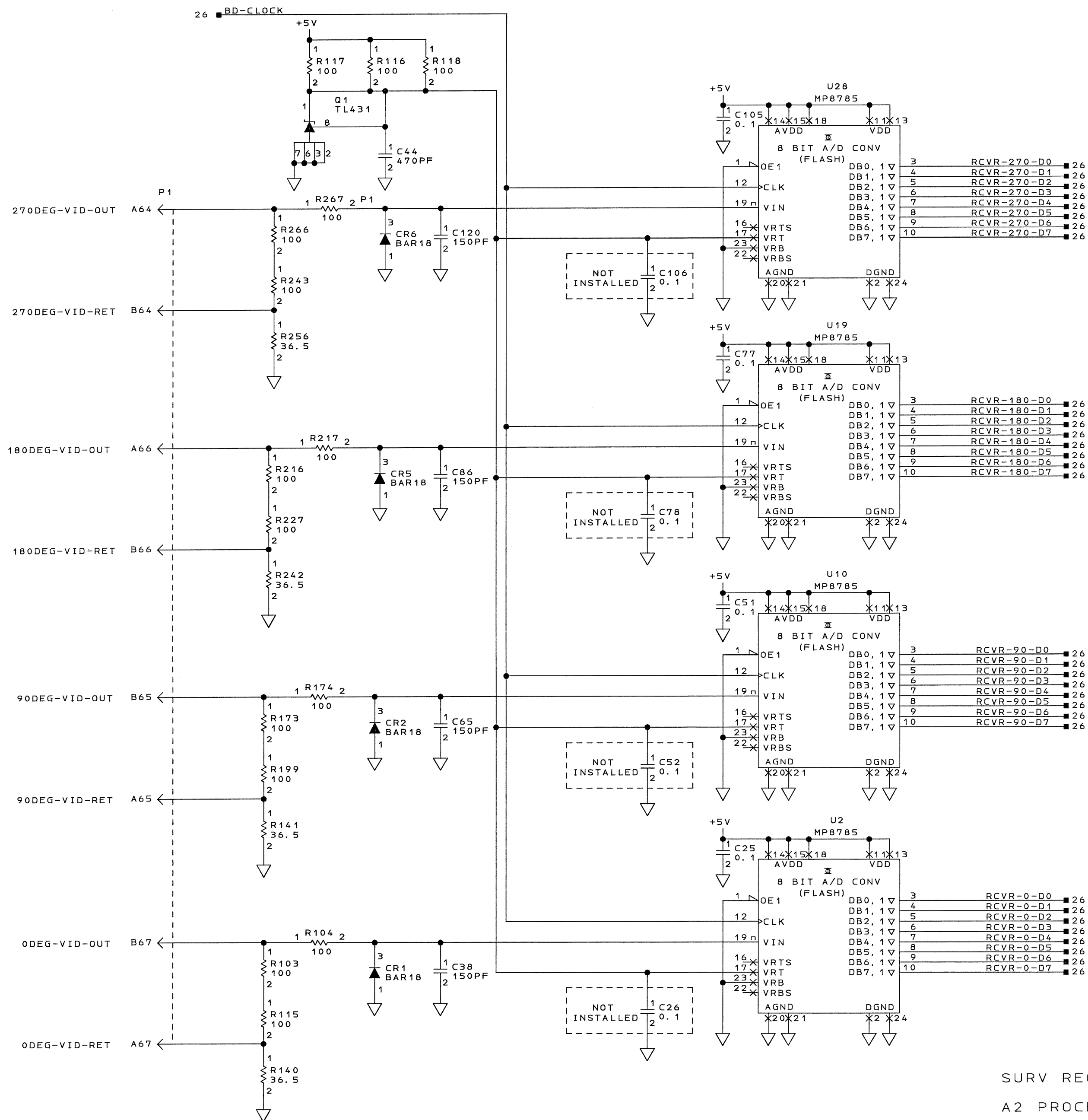
SURV RECEIVER PULSE DECODER
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 26

COMPUTER GENERATED



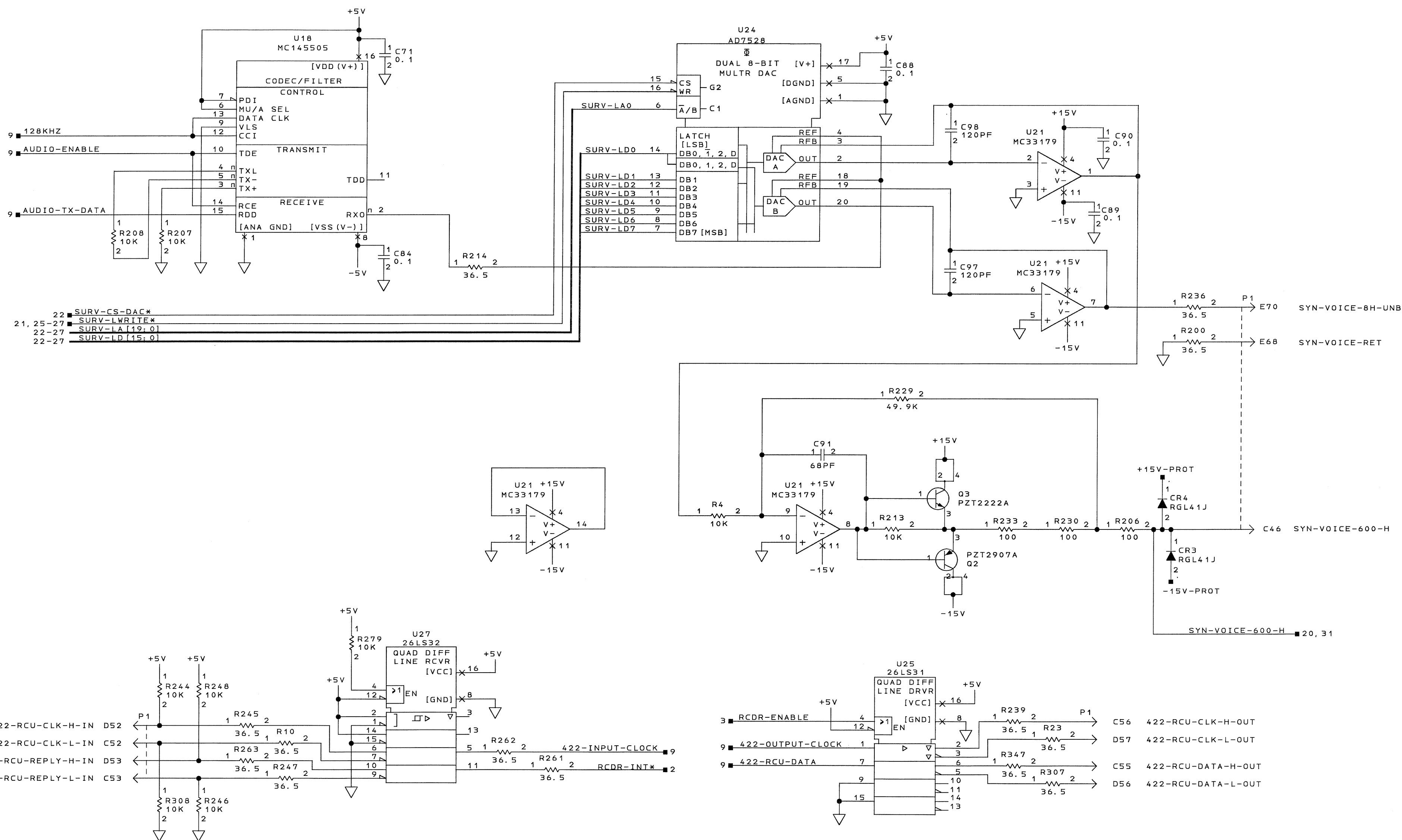
SURV RECEIVER REPLY DECODER
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 27

COMPUTER GENERATED



SURV RECEIVER VIDEO A/D CONVERTERS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 28

COMPUTER GENERATED



DIGITAL AUDIO DRIVERS
 A2 PROCESSOR CCA
 SCHEMATIC DIAGRAM SHEET NO. 29

COMPUTER GENERATED



SPARE CCA CONNECTIONS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 30

COMPUTER GENERATED

