

Date October 8th, 2025

Declaration

To whom it may concern

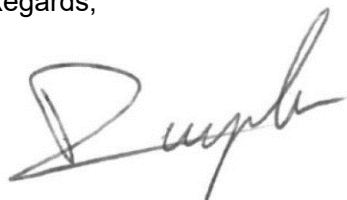
By this letter, Intel Corporation ("Intel") hereby declare that DPCSA (Dynamic Power Control and SAR/PD Algorithm) is implemented in the following Intel wireless modules family as described in Intel DPCSA Operational Report provided for KDB Inquiry:

- AX200xxx
- AX201xxx
- AX210xxx
- AX211xxx
- AX411xxx
- BE200xxx
- BE201xxx
- BE211xxx

Where xxx may be alphanumeric aa-z, 0-9 which designate different HW form factor.

The algorithm that is implemented is identical in all models and doesn't change from model to model. SAR Level input for DPCSA algorithm is specific to each host and assessed in relevant Host SAR report.

Regards,



Signature & Contact info Intel Rep

Date

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