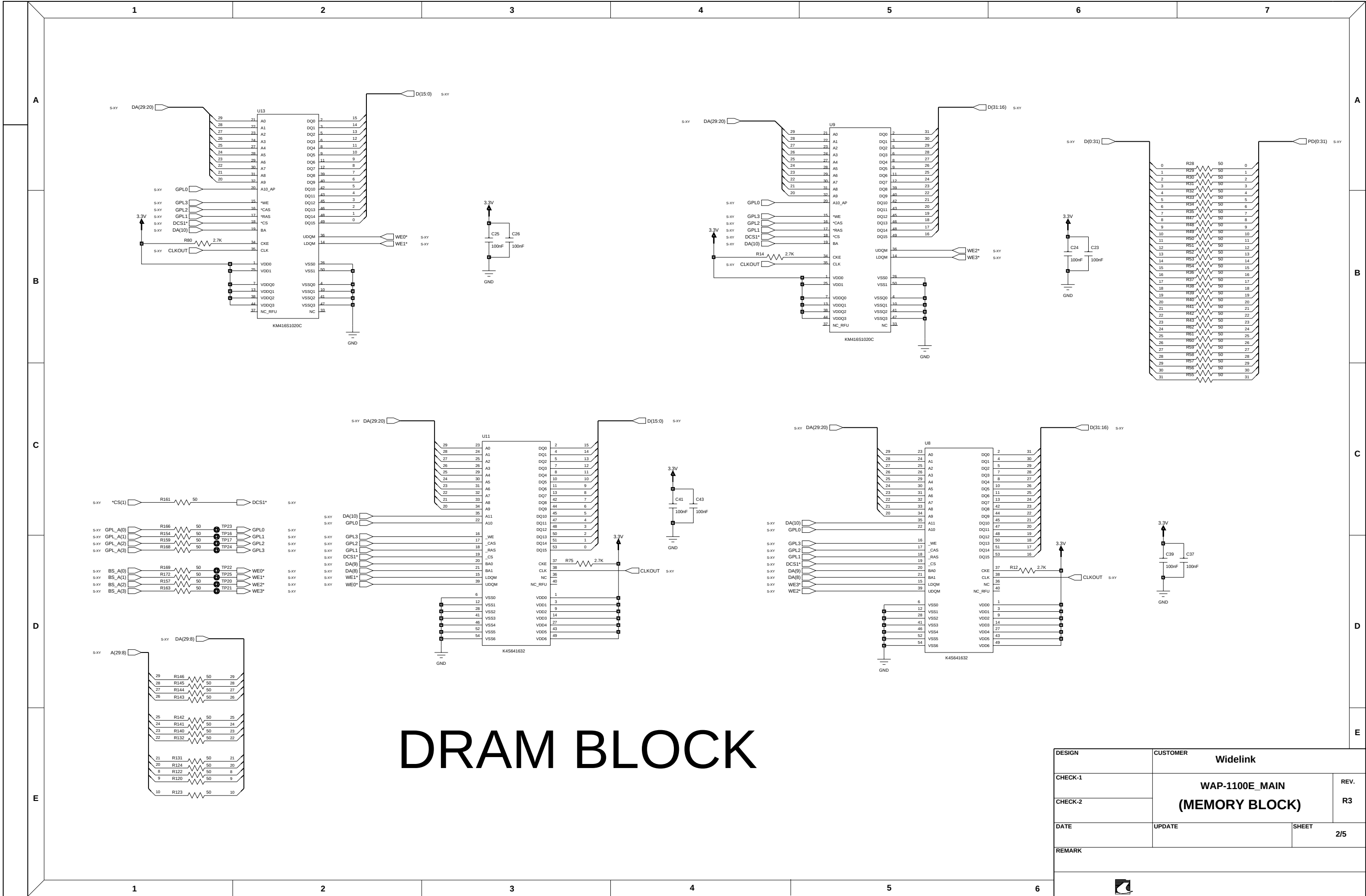
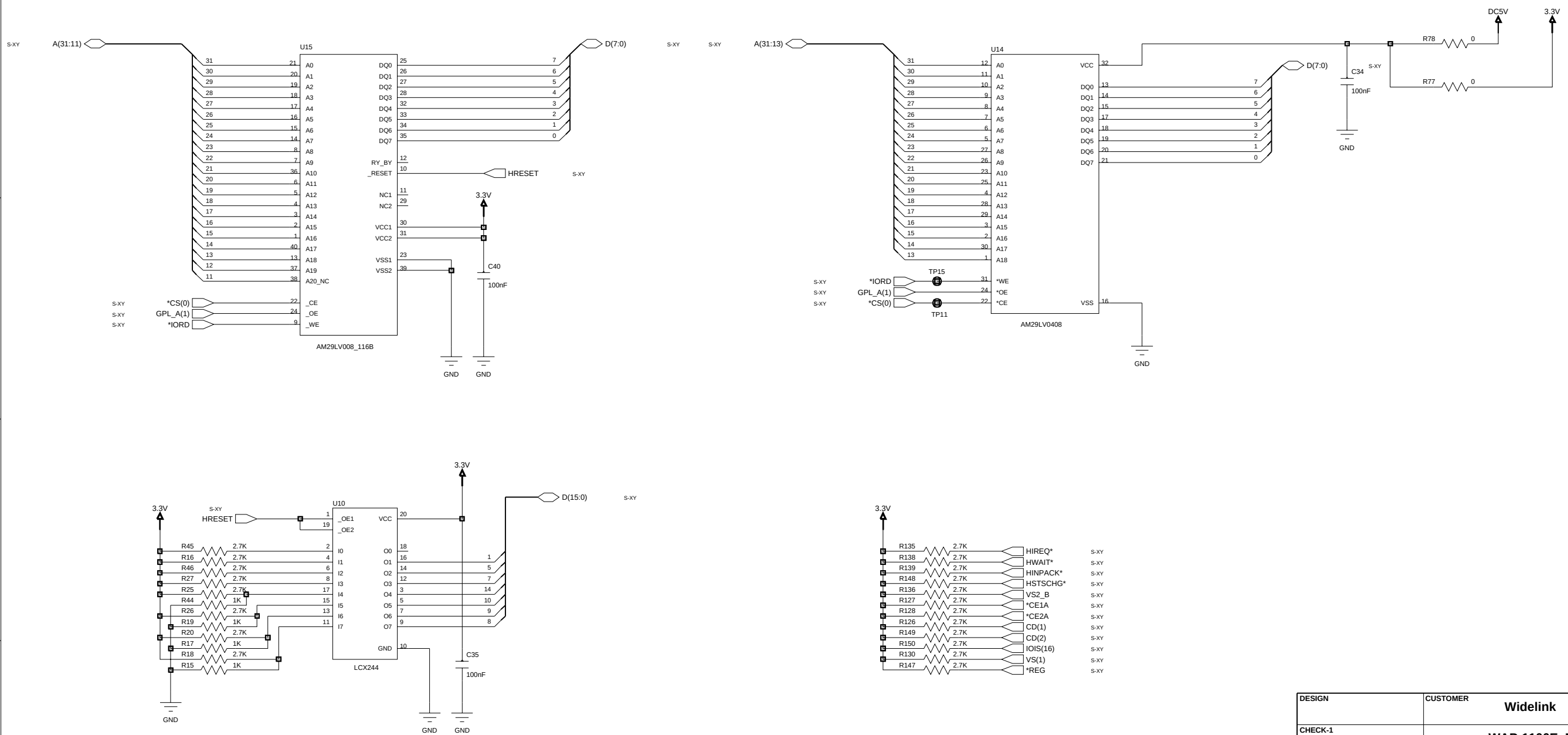




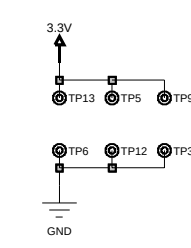
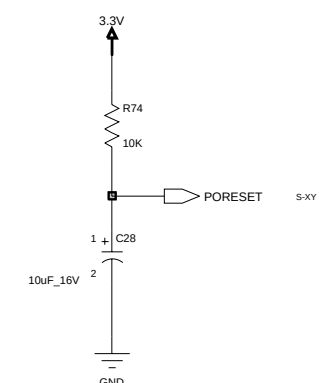
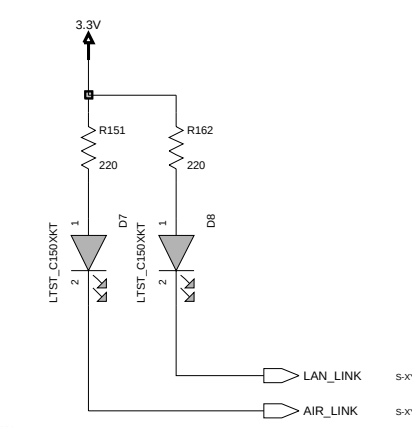
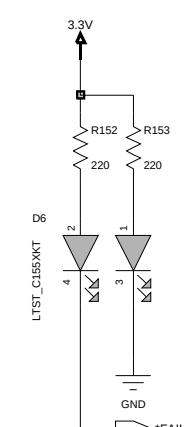
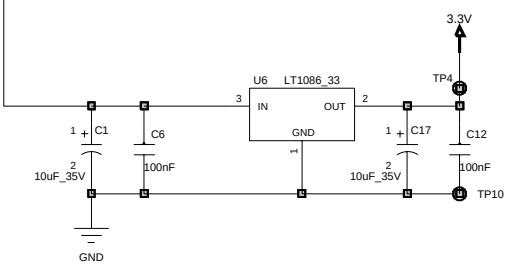
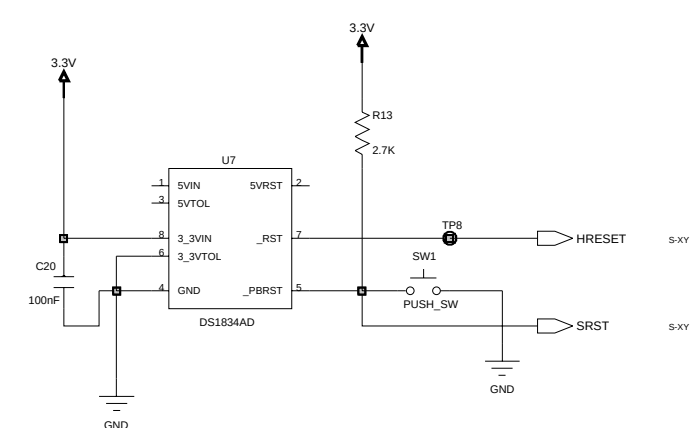
FLASH ROM BLOCK



DESIGN	CUSTOMER Widelink		
CHECK-1	WAP-1100E_MAIN		REV.
CHECK-2			R3
DATE	UPDATE	SHEET	3/5
REMARK			

DC/DC POWER

The schematic diagram illustrates a DC/DC power supply circuit. The input power is provided by a source labeled J1 (HEC3900), which is connected to the VIN pin of the MIC5205BM5 DC-DC converter (U1). The GND pin of U1 is connected to ground. The VOUT pin (pin 5) is connected to the output of the converter, which is then connected to a 1kΩ resistor (R1) and a 3kΩ resistor (R5) in series to ground. A 470pF capacitor (C7) is connected between the EN pin (pin 3) and ground. A 2.2μF capacitor (C5) is connected between the output and ground. The output voltage is measured at TP1, which is connected to the output line and ground. The output voltage is labeled as DC5V.



DESIGN	CUSTOMER Widelink		
CHECK-1	WAP-1100E_MAIN (RESET&POWER)		REV.
CHECK-2			R3
DATE	UPDATE	SHEET 4/5	
REMARK			

