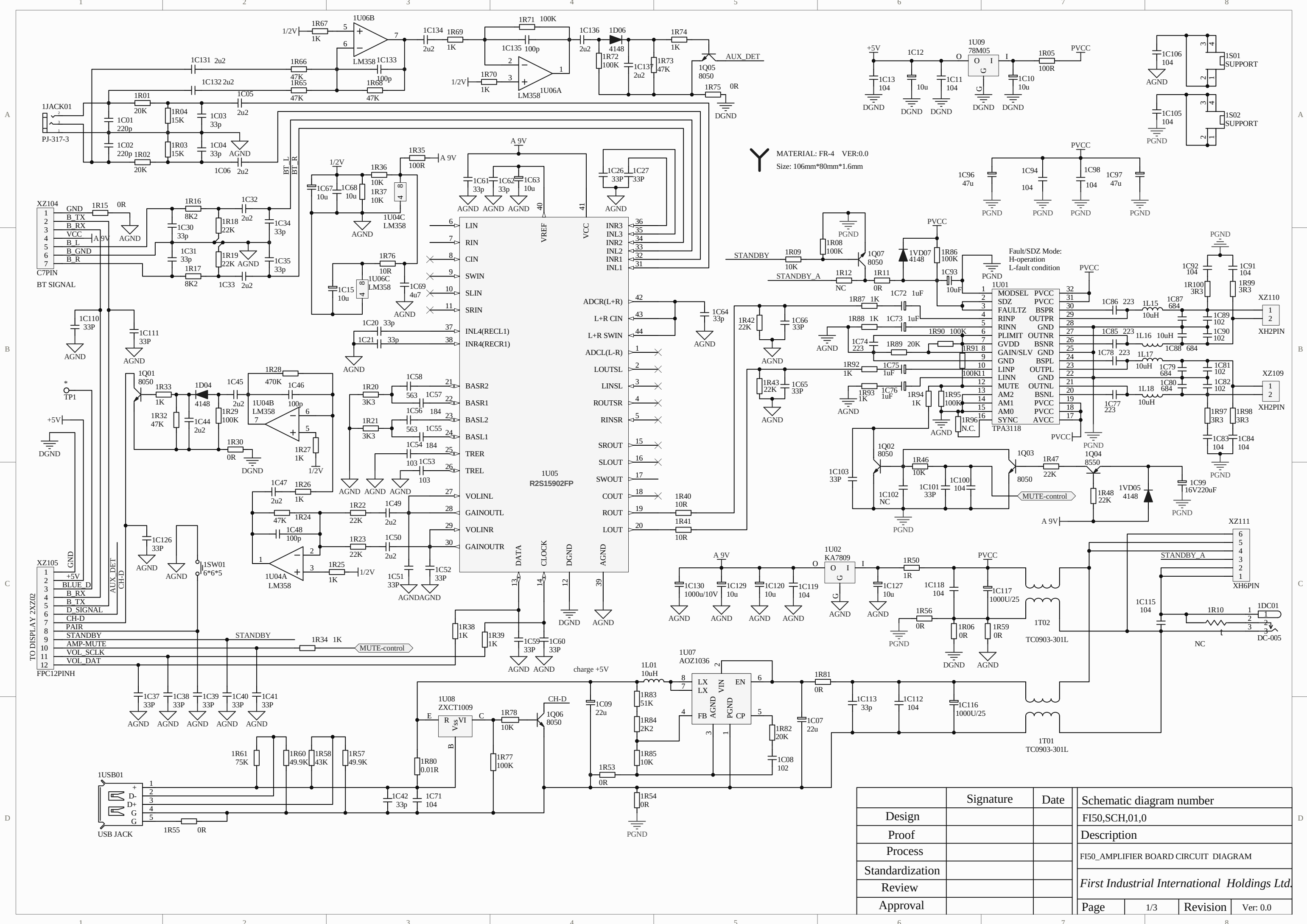




	Signature	Date	Schematic diagram number			
Design			FI50,SCH,01,0			
Proof			Description			
Process			FI50_AMPLIFIER BOARD CIRCUIT DIAGRAM			
Standardization			First Industrial International Holdings Ltd.			
Review						
Approval			Page	1/3	Revision	Ver: 0.0