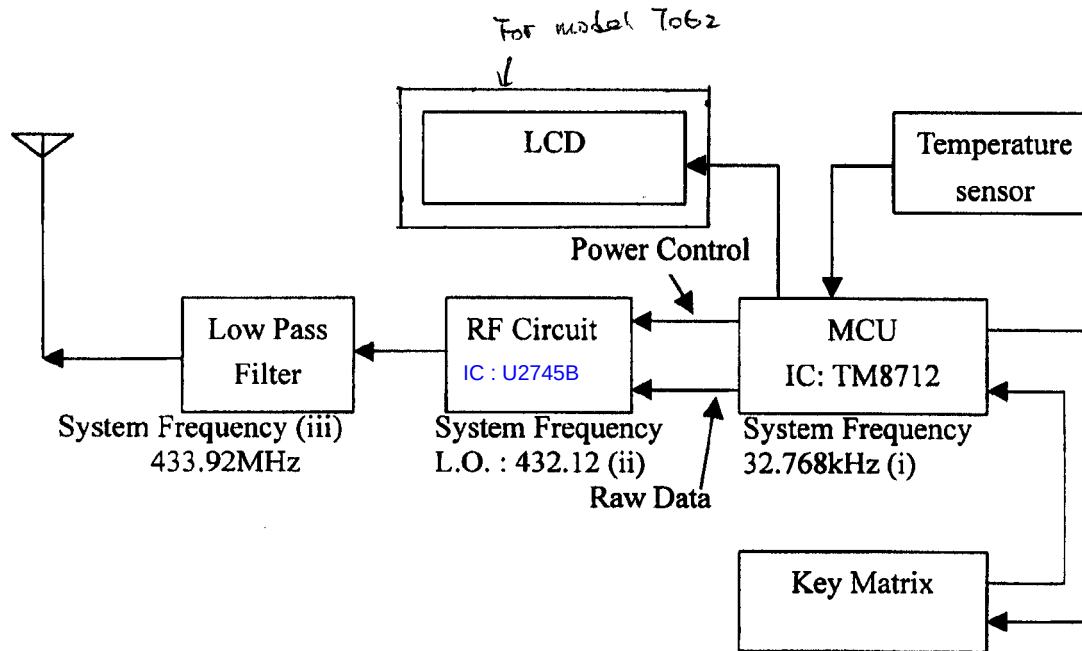
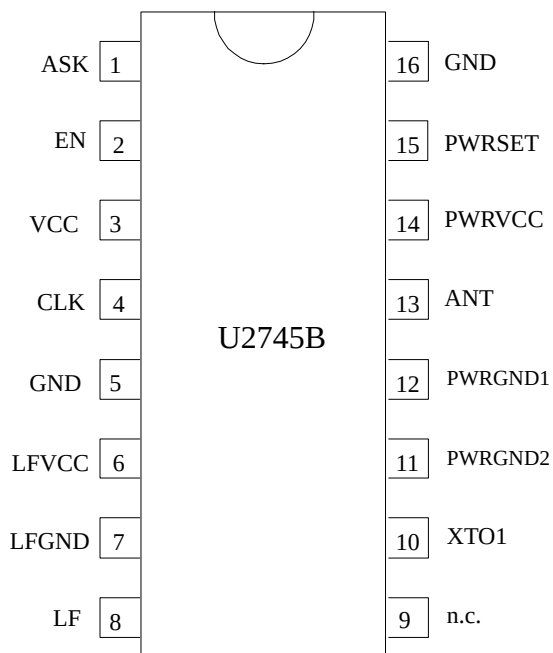


Block Diagram of 7062/3 (Tx)



- (i) The 32.768kHz is the MCU clock frequency for normal operation.
- (ii) The 13.56MHz is the frequency reference of the RF circuit since it use PLL to generate 433.92MHz
- (iii) The 433.92MHz is the output of the RF circuit

Pin Description



Pin	Symbol	Function
1	ASK	Modulation input ASK
2	EN	Enable input
3	VCC	Supply voltage
4	CLK	Clock output
5	GND	Ground
6	LFVCC	Supply voltage VCO
7	LFGND	VCO ground
8	LF	Circuit PLL loop
9	n.c.	Not connected
10	XTO1	Connection for crystal
11	PWRGND2	Power GND2
12	PWRGND1	Power GND1
13	ANT	RF output
14	PWRVCC	Supply voltage power amplifier
15	PWRSET	Applied to VCC
16	GND	Ground

Figure 2. Pinning SSO16

Block Diagram

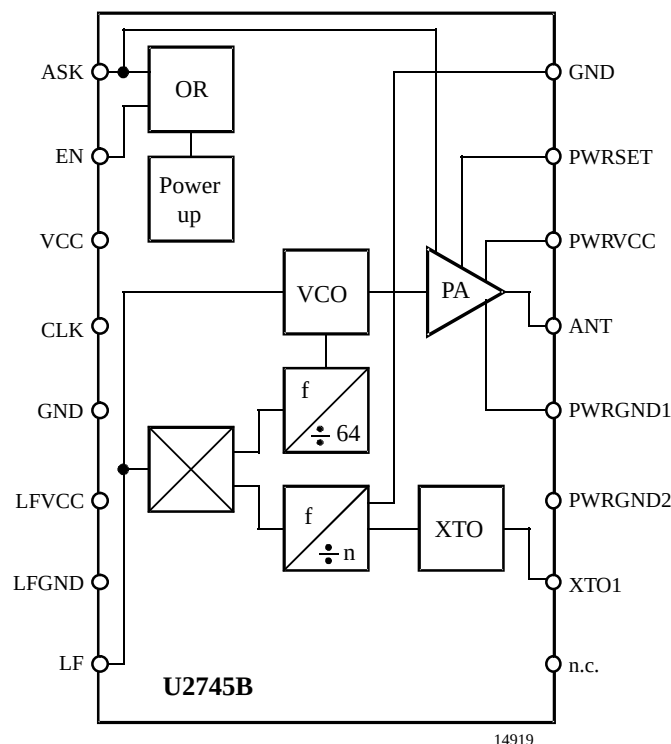


Figure 3. Block diagram