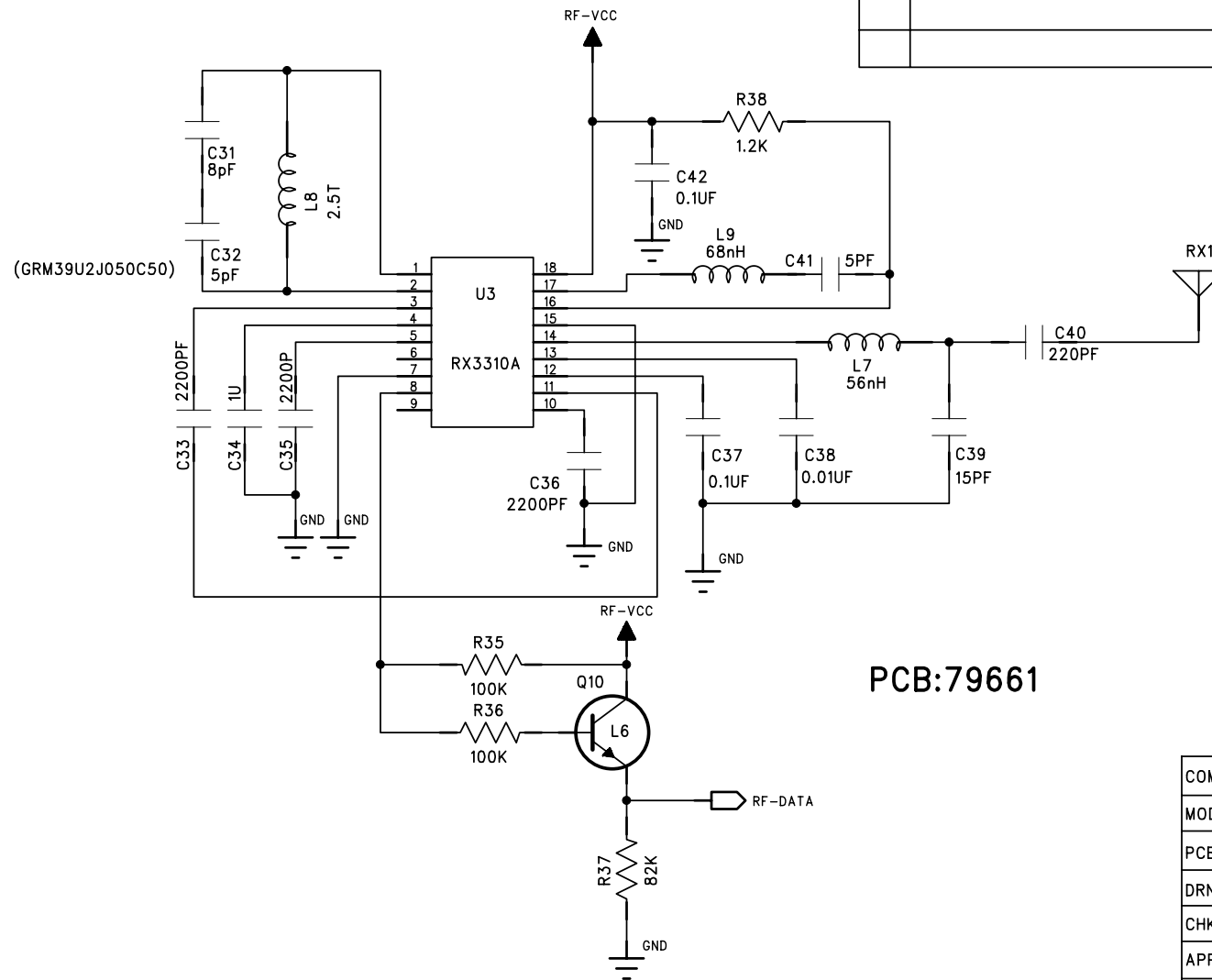




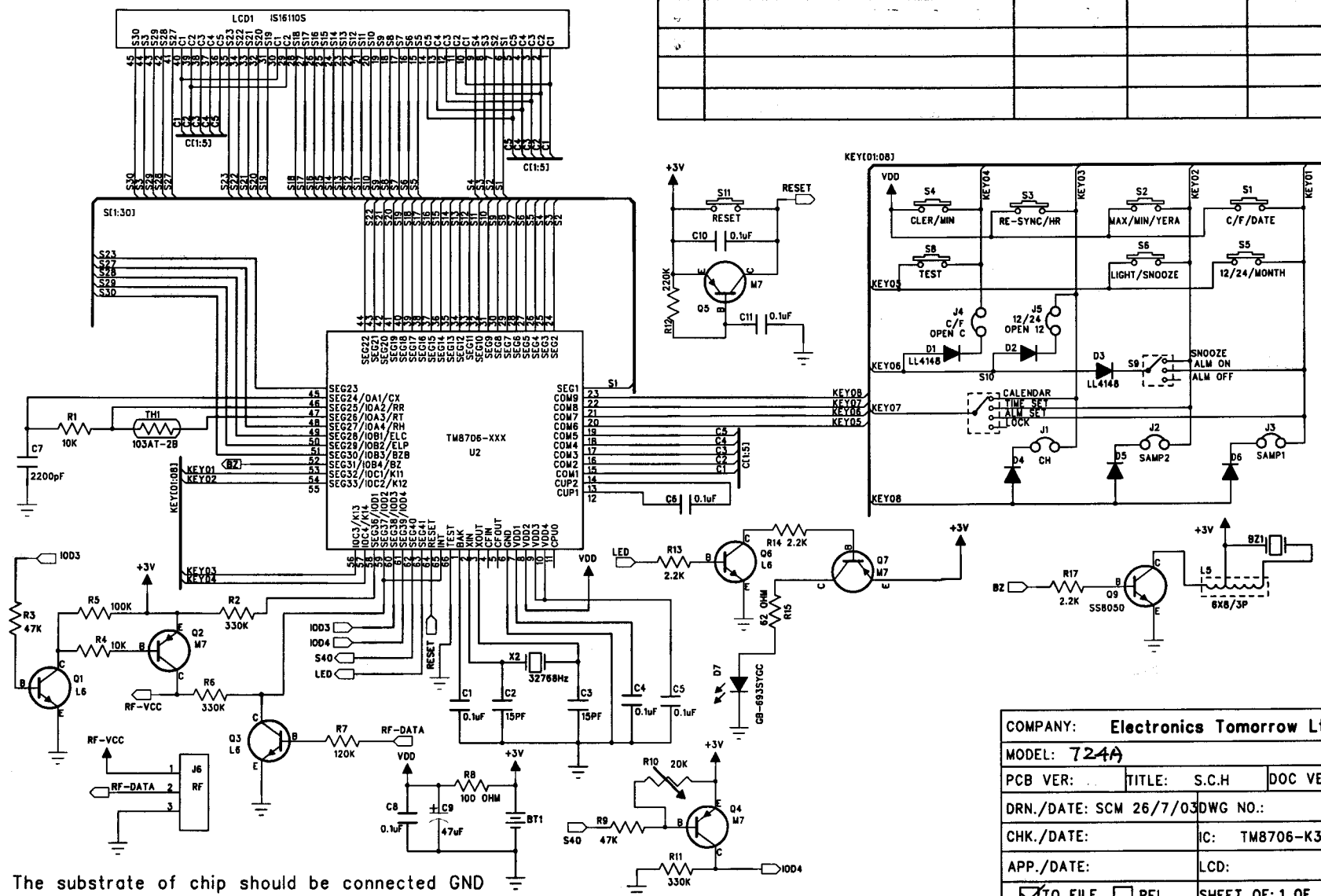
REV.	MODIFICATION	DATE:	CHK:	APP:

PCB:79661

COMPANY: Electronics Tomorrow Ltd.					
MODEL: 699E/F					
PCB VER:	T1.0	TITLE:	SCH	DOC VER:	00
DRN. /DATE: CHENZHAIMING 20-02-2003			DWG NO: 7966E10240		
CHK. /DATE:			IC: RX3310A		
APP. /DATE:			LCD:		
☒ TO FILE ☐ REL			SHEET OF: 3 OF 3		

EE005

NO.	REVISION	DATE:	CHK:	APP:
1				
2				
3				
4				
5				



The substrate of chip should be connected GND

COMPANY: Electronics Tomorrow Ltd.		
MODEL: 724A		
PCB VER:	TITLE: S.C.H	DOC VER:
DRN./DATE: SCM 26/7/03		DWG NO.:
CHK./DATE:	IC: TM8706-K323	
APP./DATE:	LCD:	
☒ TO FILE ☐ REL		SHEET OF: 1 OF 1 EE005