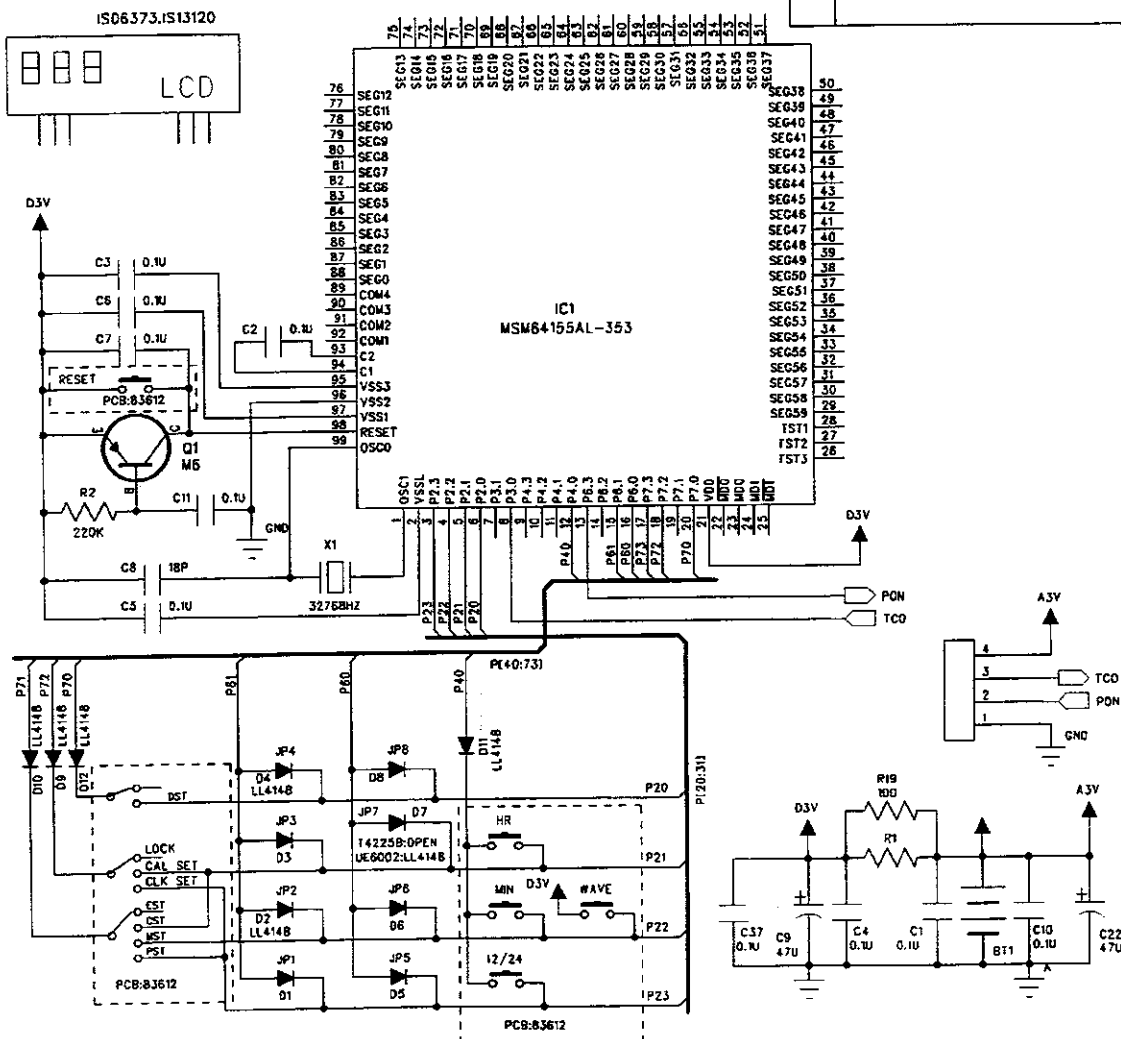


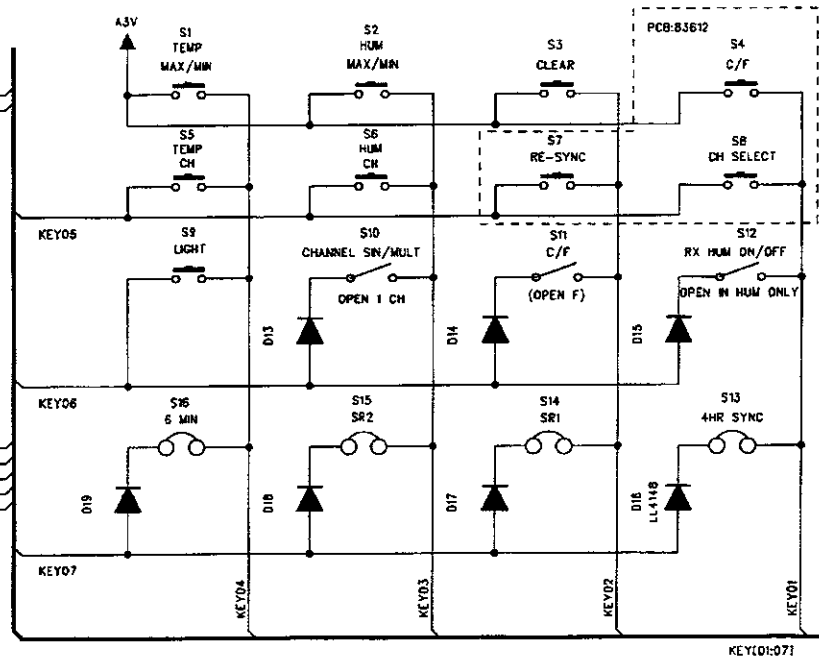
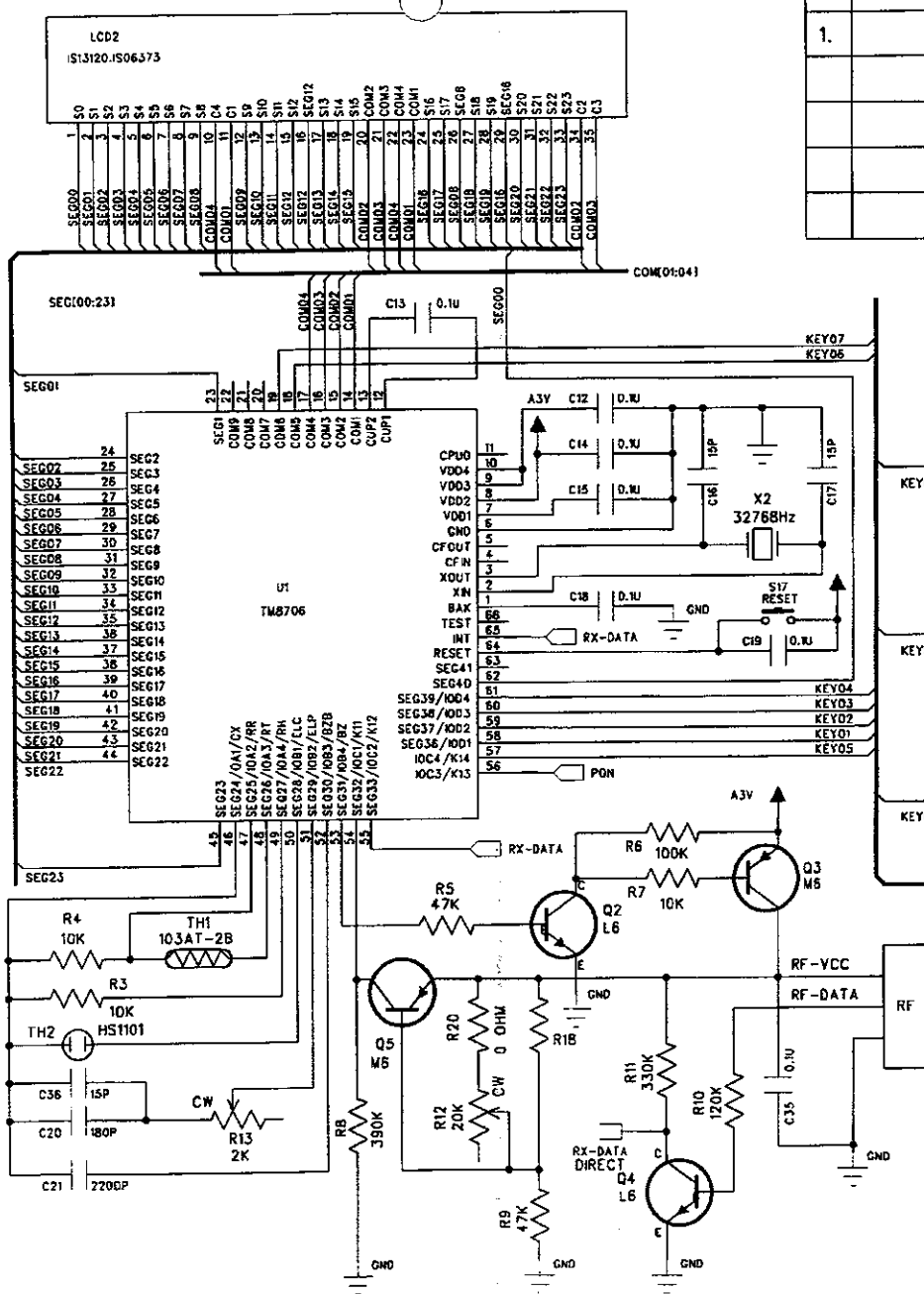
NO.	REVISION	DATE:	CHK:	APP:
1.				



JP1 (D1:LL4148)	LCD OPEN-DESKTOP
JP2 (D2:LL4148)	RETRY RATE OPEN-30 MINUTES
JP3 (D3:LL4148)	UPDATE RATE OPEN-12 hrs
JP4 (D4:LL4148)	PERIOD FOR RECEIVING TIME-CODE SIGNAL OPEN-10 MINUTES
JP5 (D5:LL4148)	RCC FUNCTION OPEN-ENABLE
JP6 (D6:LL4148)	ENGLISH DAY OF WEEK DISPLAY OPEN-ENABLE
JP7 (D7:LL4148)	NOT RECEIVING OPEN-SIGNAL STRENGTH
JP8 (D8:LL4148)	RECEIVING OPEN-SIGNAL STRENGTH

COMPANY: Electronics Tomorrow Ltd		
MODEL: 8361		
PCB VER: V1.0	TITLE: SCH	DOC VER: 00
DRN./DATE: CHEN ZHAI MING 29-06-2002	DWG NUM: 8361E10131	
CHK./DATE: 06/07/02	IC: MSM64155-353	
APP./DATE:	LCD: IS06373.IS13120	
☒ TO FILE	☒ REL 1/2	SHEET 1 OF 3 EF005

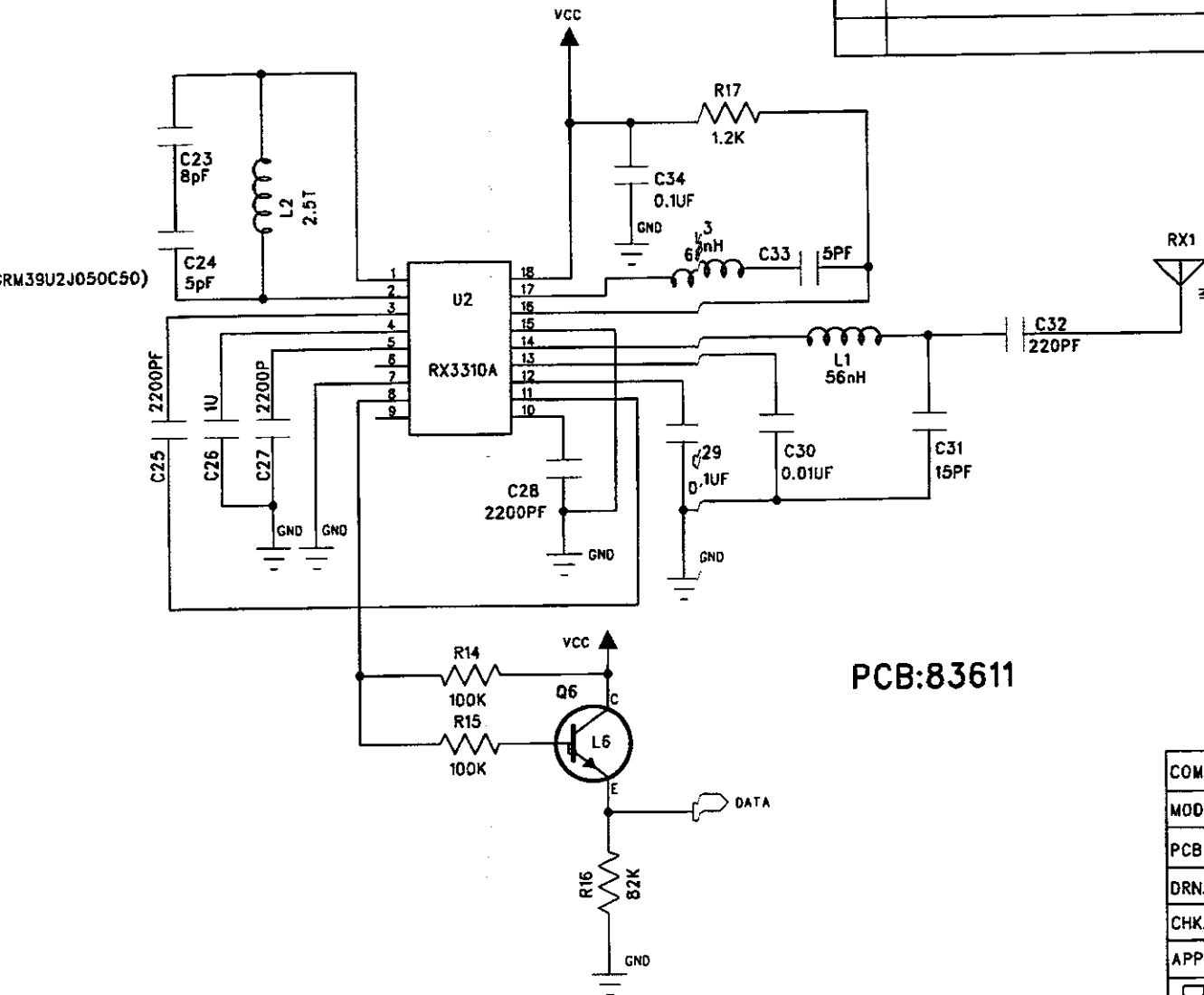
NO.	REVISION	DATE:	CHK:	APP:
1.				



COMPANY: Electronics Tommorrow Ltd		
MODEL: 8361		
PCB VER: V1.0	TITLE: SCH	DOC VER: 00
DRN./DATE: CHEN ZHAI MING 29-06-2002	DWG NUM: 8361E10131	
CHK./DATE: <i>Chen Zhai Ming</i>	IC: TM8706	
APP./DATE: <i>Chen Zhai Ming</i>	LCD: IS06373.IS13120	
☒ TO FILE	☒ REE	SHEET 2 OF 3

EE005

REV.	MODIFICATION	DATE:	CHK:	APP:



PCB:83611

COMPANY: Electronics Tomorrow Ltd.				
MODEL: 8361				
PCB VER:	V1.0	TITLE:	SCH	DOC VER: 00
DRN. /DATE:	CHENZHAMING 29-06-2002	DWG NO:	B361E10131	
CHK. /DATE:	6/21/2002	IC:	RX3310A	
APP. /DATE:	6/21/2002	LCD:		
☒ TO FILE				SHEET OF: 3 OF 3

FE005