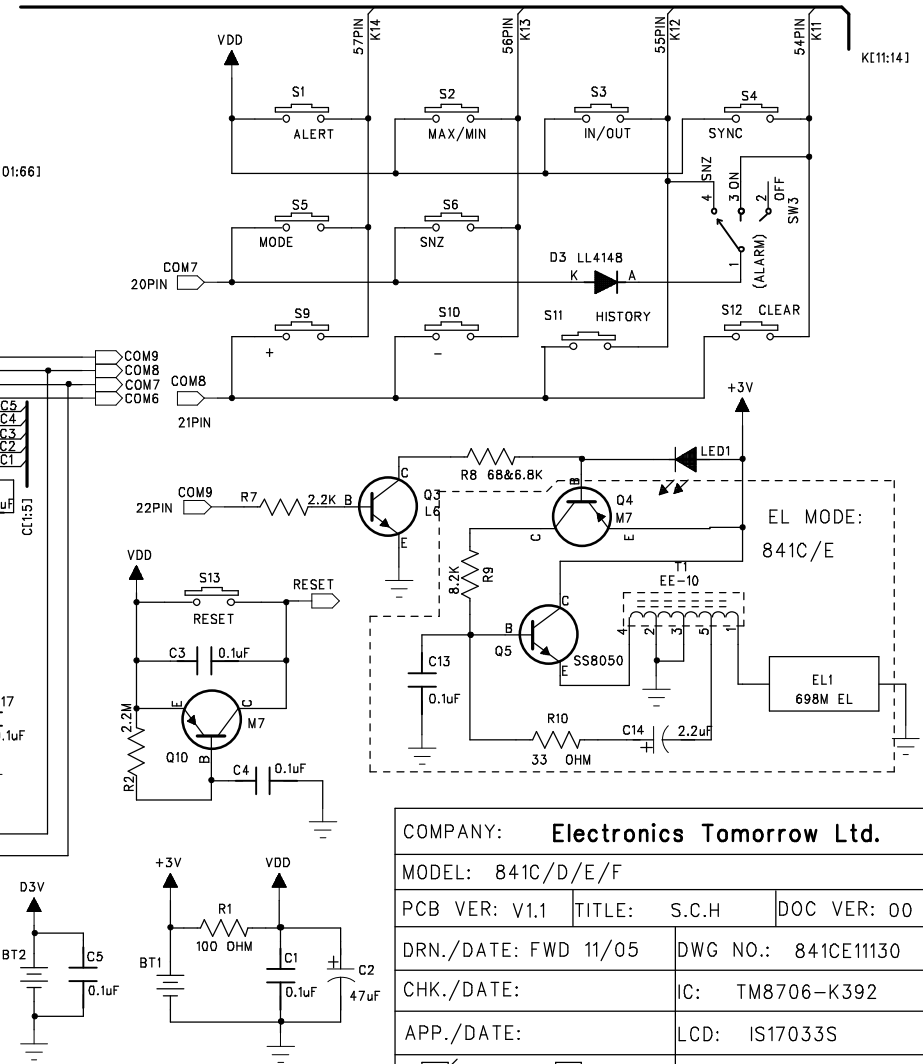
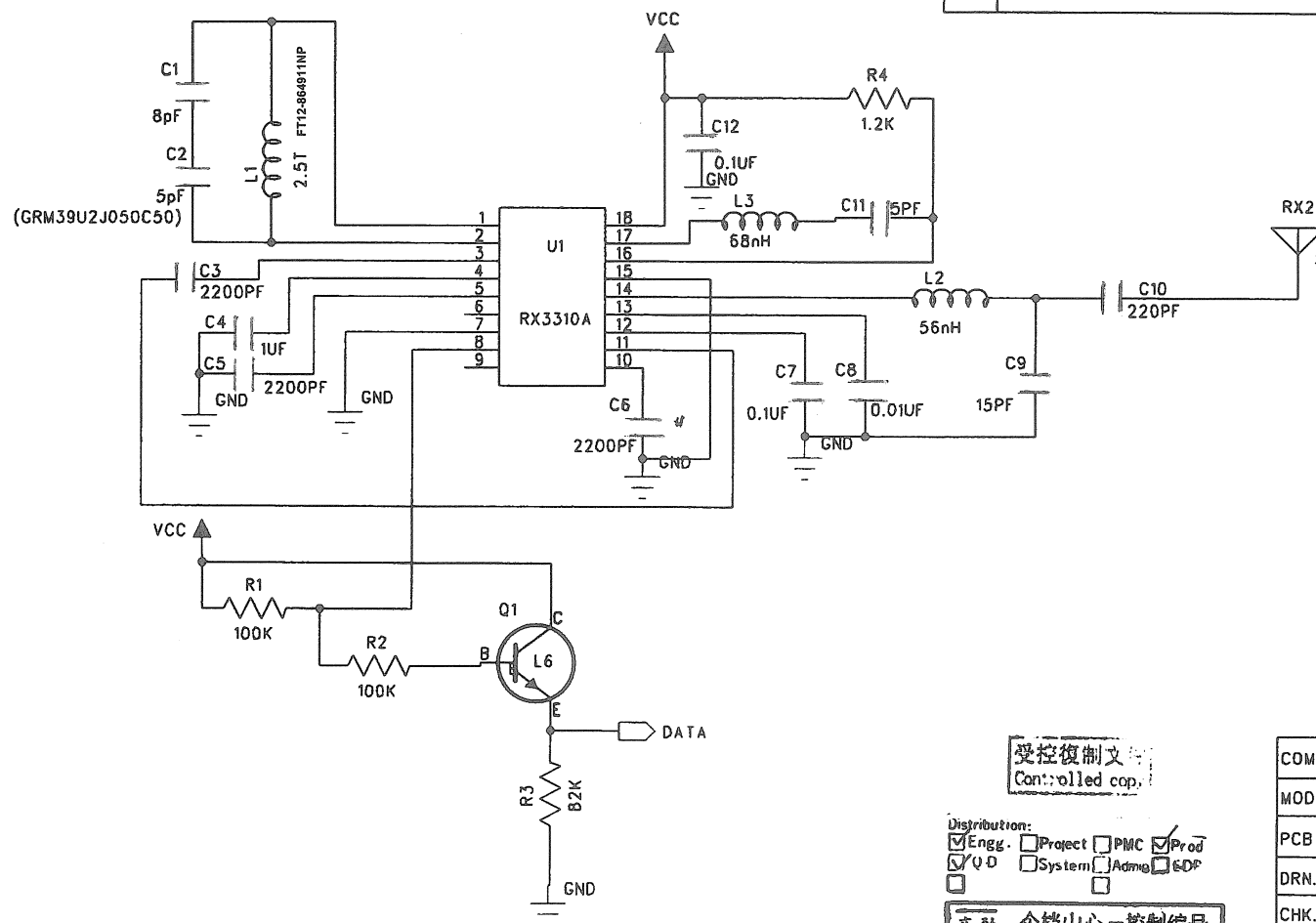


NO.	REVISION	DATE:	CHK:	APP:
1	T1.0			
2	V1.0			
3	V1.1 (LED / DRILL)			
4				
5				

The substrate of chip should be connected GND



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受控複製文
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Distribution:

☒ Engg.	☐ Project	☐ PMC	☒ Prod
☒ Q D	☐ System	☐ Admin	☐ EDP
☐	☐	☐	☐

高劲 企档中心—控制编号
CDC 04 03172
日期: 16 MAR 2004

COMPANY: Electronics Tomorrow Ltd.				
MODEL: 7966				
PCB VER:	V1.1	TITLE:	SCH	DOC VER: 01
DRN. /DATE: ZB 16/3/04			DWG NO: 7966E11231	
CHK. /DATE: 22/10/04			IC: RX3310A	
APP. /DATE: 22/10/04			LCD:	
☒ TO FILE		☒ REL		SHEET OF: 5500