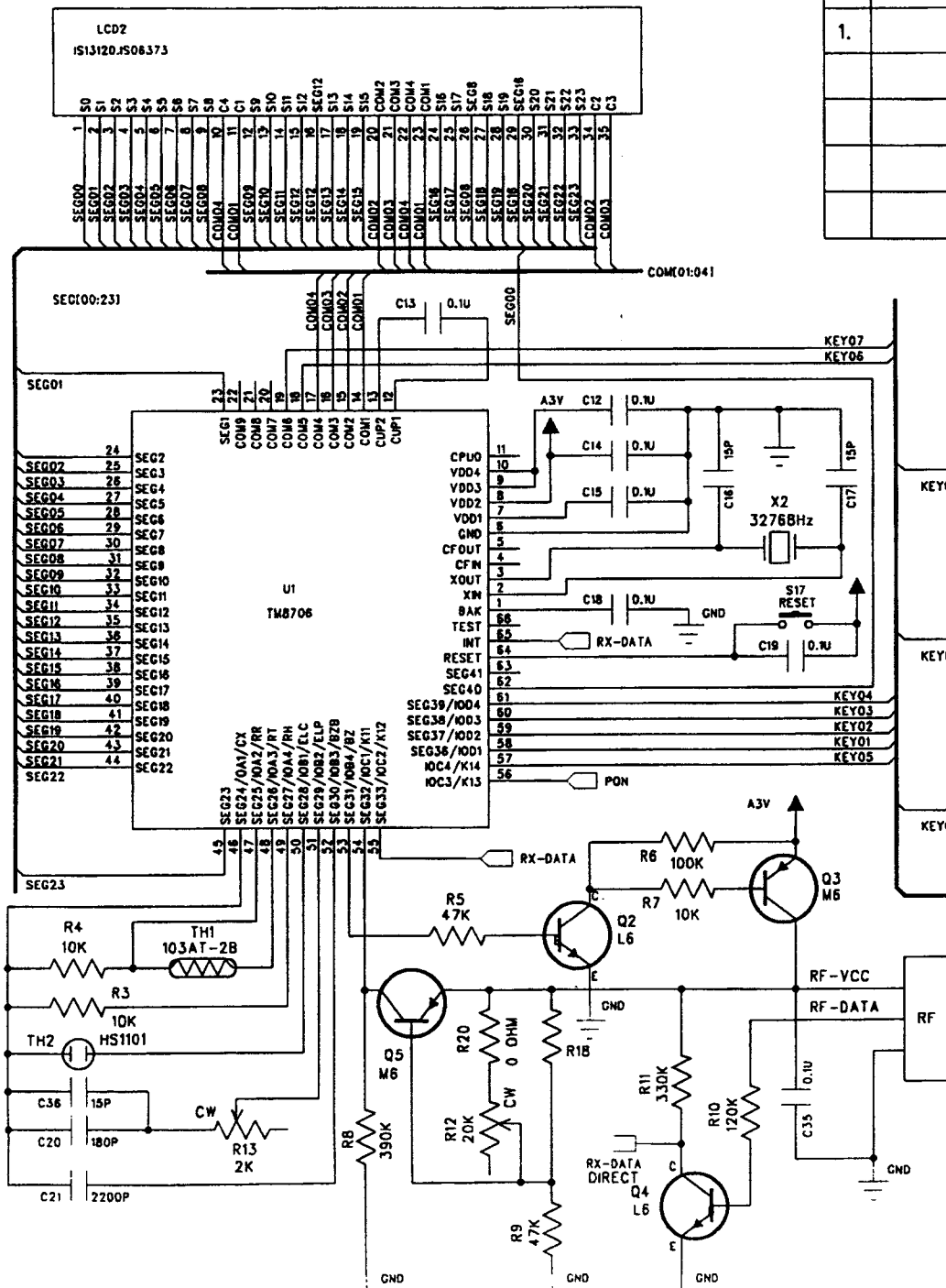
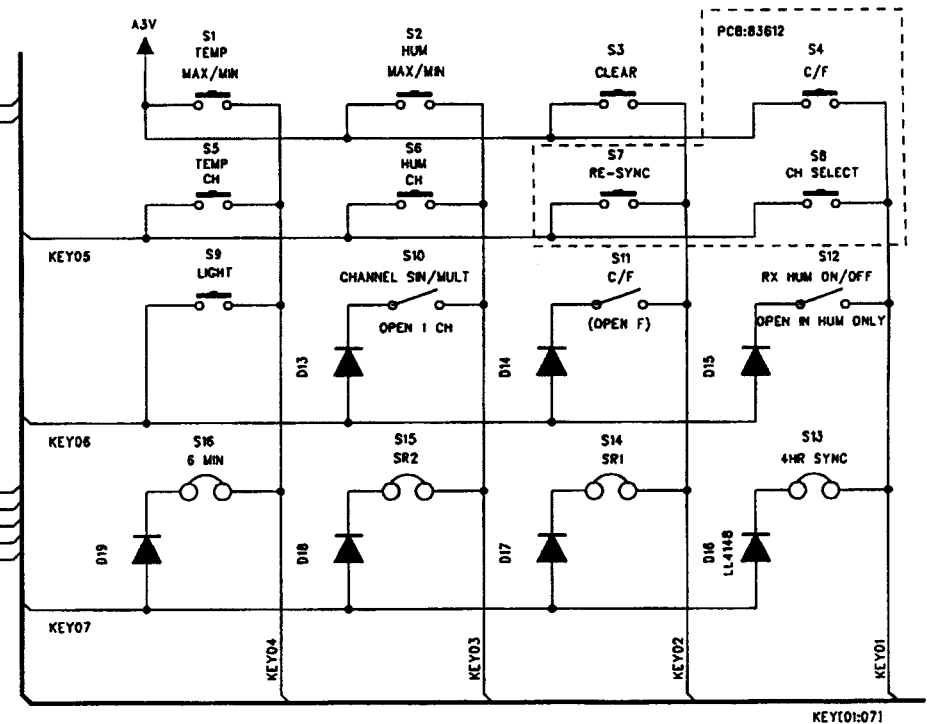


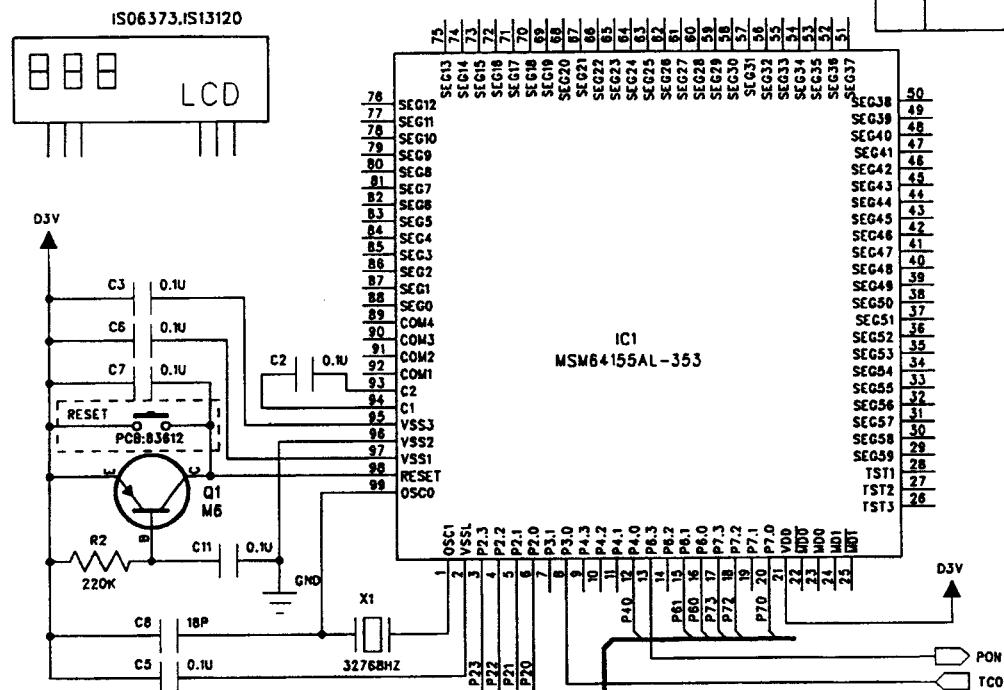


NO.	REVISION	DATE:	CHK:	APP:
1.				

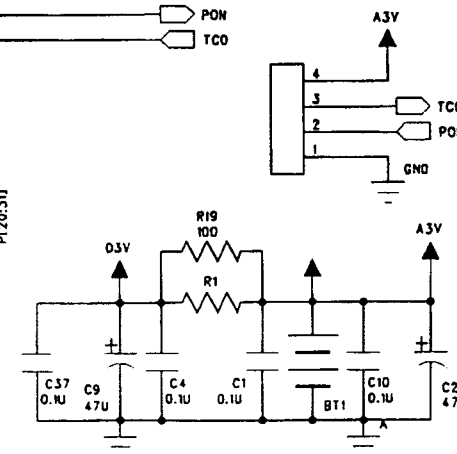
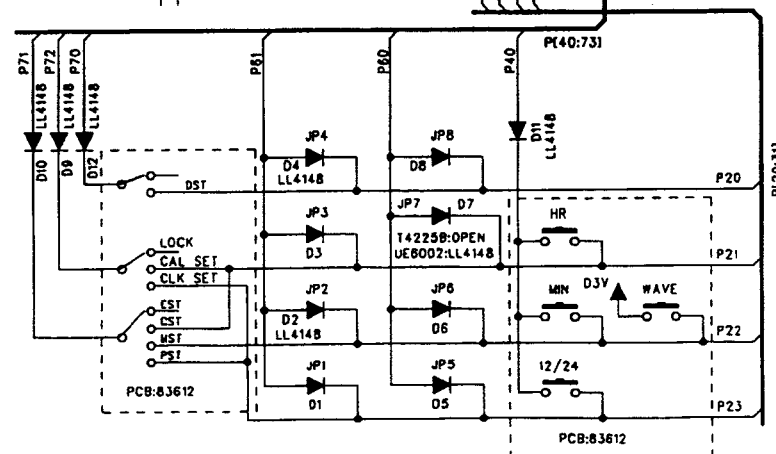


COMPANY: Electronics Tommorrow Ltd					
MODEL: 8733					
PCB VER: V1.0		TITLE: SCH		DOC VER: 00	
DRN./DATE: CHEN ZHAI MING 29-06-2002		DWG NUM:			
CHK./DATE: Chen Zhai Ming		IC: TM8706			
APP./DATE: Chen Zhai Ming		LCD: IS06373.IS13120			
☒ TO FILE		REL 1/1		SHEET 2 OF 3	

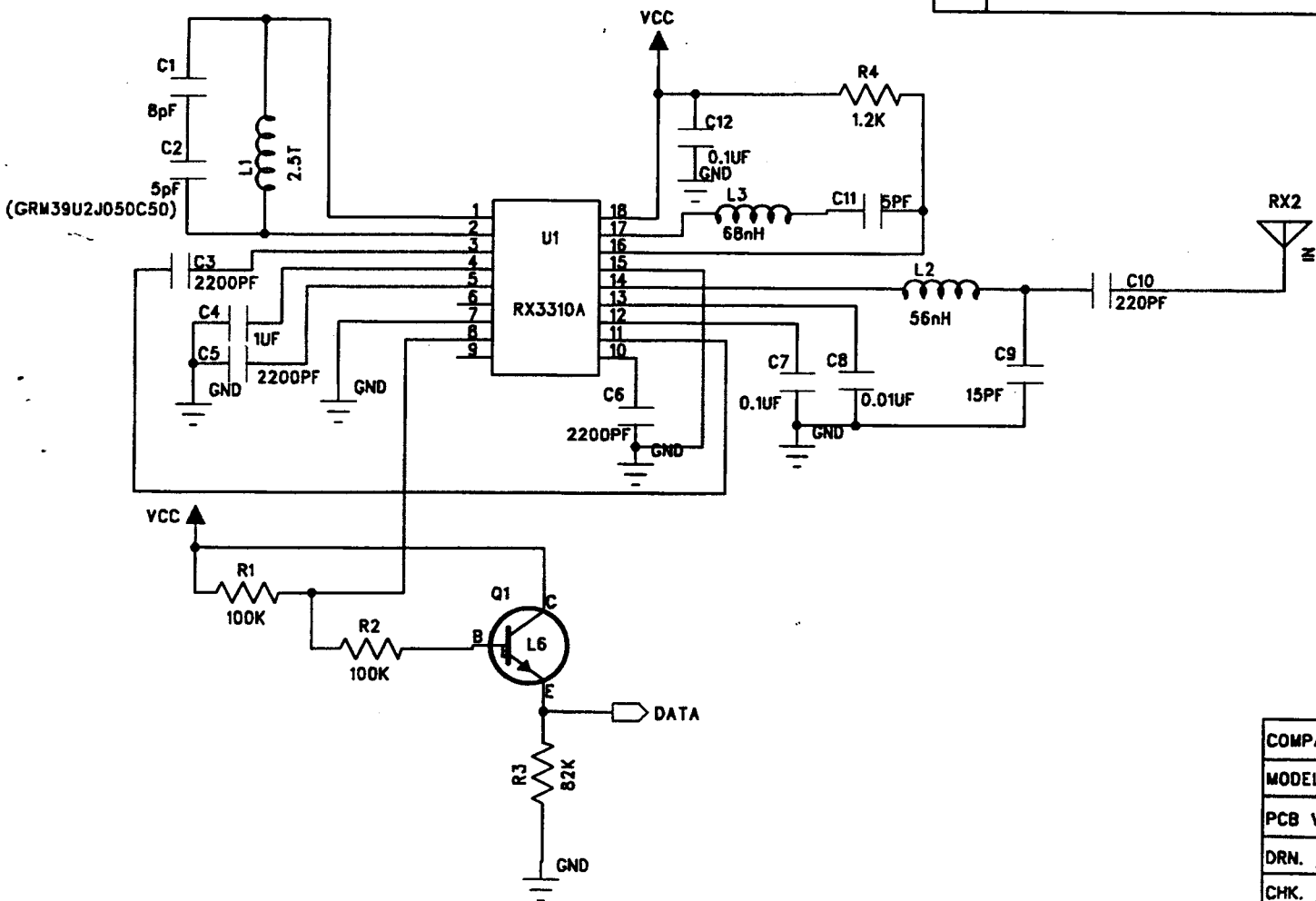
NO.	REVISION	DATE:	CHK:	APP:
1.				



JP1 (D1:LL4148)	LCD OPEN-DESKTOP
JP2 (D2:LL4148)	RETRY RATE OPEN-30 MINUTES
JP3 (D3:LL4148)	UPDATE RATE OPEN-12 Hrs
JP4 (D4:LL4148)	PERIOD FOR RECEIVING TIME-CODE SIGNAL OPEN-10 MINUTES
JP5 (D5:LL4148)	RCC FUNCTION OPEN-ENABLE
JP6 (D6:LL4148)	ENGLISH DAY OF WEEK DISPLAY OPEN-ENABLE
JP7 (D7:LL4148)	MDT RECEIVING OPEN-SIGNAL STRENGTH
JP8 (D8:LL4148)	RECEIVING OPEN-SIGNAL STRENGTH



COMPANY: Electronics Tommorrow Ltd		
MODEL: P733		
PCB VER: V1.0	TITLE: SCH	DOC VER: 00
DRN./DATE: CHEN ZHAI MING 29-06-2002	DWG NUM:	
CHK./DATE: 06/07-02	IC: MSM64155-353	
APP./DATE:	LCD: ISO6373.IS13120	
TO FILE		



(GRM39U2J050C50)

REV.	MODIFICATION	DATE:	CHK:	APP:

COMPANY: Electronics Tomorrow Ltd.					
MODEL: 8733					
PCB VER:	T2.0	TITLE:	SCH	DOC VER:	01
DRN. /DATE: ZB 19/3/02			DWG NO:		
CHK. /DATE: 6/4/03			IC: RX3310A		
APP. /DATE: 7/4/03			LCO:		
☒ TO FILE			☐ REL		
SHEET OF:			EF005		